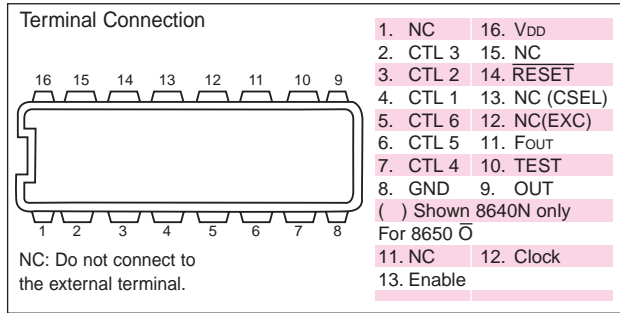




# Crystal Oscillators - Programmable - Seiko Epson

## SPG Series



## Explanation of Terminals

Terminal Name	Function
CTL1 to 6	Programs dividing ratio. (pull-down resistor incorporated)
OUT	Output frequency preset by CTL1-6. (Refer to the setting procedure of output frequency)
FOUT	Constantly outputs the oscillation source frequency of built-in quartz crystal.
RESET	Stops output at RESET="L". (Pull up resistor incorporated)
TEST	Used for the input terminal for testing. When CTL4 is H, output will be 1000 times larger than the preset value at TEST="H". (Pull-down resistor incorporated)
EXC (8640N only)	Serves as input terminal when using an external clock by changing to the built-in oscillator. Effective only when CSEL is H.
CSEL (8640N only)	When this terminal is made H, the external clock is selected (Pull down resistor incorporated).

Note: treatment of empty terminals. When RESET terminal is not used, this should be connected to VDD, when TEST terminal, CSEL terminal, and CTL1 to 6 terminals are not used, to GND.

**Additional explanation of terminals for 8650D series**

Clock	Clock input (max. 1MHz)
ENABLE	Be sure to connect to V <sub>DD</sub>

## Setting of Divider Output

CTL1	CTL2	CTL3	Dividing ratio	CTL4	CTL5	CTL6	Dividing ratio
0	0	0	1/1	0	0	0	1/1 (1 <sup>0</sup> )
0	0	1	1/10	0	0	1	1/10 (1 <sup>1</sup> )
0	1	0	1/2	0	1	0	1/10 <sup>2</sup> (1/2 <sup>2</sup> )
0	1	1	1/3	0	1	1	1/10 <sup>3</sup> (1/2 <sup>3</sup> )
1	0	0	1/4	1	0	0	1/10 <sup>4</sup> (1/2 <sup>4</sup> )
1	0	1	1/5	1	0	1	1/10 <sup>5</sup> (1/2 <sup>5</sup> )
1	1	0	1/6	1	1	0	1/10 <sup>6</sup> (1/2 <sup>6</sup> )
1	1	1	1/12	1	1	1	1/10 <sup>7</sup> (1/2 <sup>7</sup> )

\*0 = L 1 = H

(8650D)

## Setting of Output Frequency

### 8640AN

Set terminal	CTL4	CTL5	CTL6	CTL7	CTL8	CTL9	CTL10	CTL11	CTL12
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0

### 8640BN

Set terminal	CTL4	CTL5	CTL6	CTL7	CTL8	CTL9	CTL10	CTL11	CTL12
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0

### 8650A 8651A

Set terminal	CTL4	CTL5	CTL6	CTL7	CTL8	CTL9	CTL10	CTL11	CTL12
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0

### 8650B 8651B

Set terminal	CTL4	CTL5	CTL6	CTL7	CTL8	CTL9	CTL10	CTL11	CTL12
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0

### 8650E 8651E

Set terminal	CTL4	CTL5	CTL6	CTL7	CTL8	CTL9	CTL10	CTL11	CTL12
0	0	0	0	0	0	0	0	0	0
0	0	1	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0
0	1	1	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0
1	0	1	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	0	0

## Baud Rate Generator

### 8640CN

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (fo/16)(bits/sec)
0	0	0	0	0	0	768k	48000
1	0	1	0	0	0	153.6	9600
0	0	1	0	0	0	76.8	4800
0	1	0	0	0	1	38.4	2400
1	0	0	0	0	1	19.2	1200

### 8650C

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (fo/16)(bits/sec)
0	0	0	0	0	0	96.0	6000
1	0	1	0	0	0	19.2	1200
0	0	1	0	0	0	9.6	600
0	1	0	0	0	1	4.8	300
0	1	1	0	0	1	3.2	200
1	0	0	0	0	1	2.4	150
1	1	0	0	0	1	1.6	100
1	1	1	0	0	1	0.8	50

### 8650D

CTL1	CTL2	CTL3	CTL4	CTL5	CTL6	Output frequency (kHz)	Baud rate output example (fo/16)(bits/sec)
0	0	0	0	0	0	153.6	9600
0	0	0	0	0	1	76.8	4800
0	0	0	0	1	0	38.4	2400
0	0	0	0	1	1	19.2	1200
0	0	0	1	0	1	4.8	300
0	1	1	1	0	0	3.2	200
0	0	0	1	1	0	2.4	150
1	1	0	1	0	0	1.6	100
0	0	0	1	1	1	1.2	75
1	1	1	1	0	0	0.8	50

Note: Lower digits are omitted



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