



Digital Attenuator, 4-Bit, Single Control 30 dB, 0.5-2.0 GHz

AT-264 V6

Features

- · Positive Single Control
- 2-dB Attenuation Steps to 30 dB
- Low DC Power Consumption
- TSSOP-16 Plastic Package

Description

M/A-COM's AT-264 is a 4-bit, 2-dB step GaAs MMIC digital attenuator in a low cost TSSOP-16 surface mount plastic package. The AT-264 is ideally suited for use where high accuracy, very low power consumption and low intermodulation products are required. Typical applications include radio, cellular, wireless LANs, GPS equipment and other gain/level control circuits.

The AT-264 is fabricated using a mature 1 micron GaAs MESFET process. The process features full chip passivation for increased performance and reliability.

Ordering Information

Part Number	Package
AT-264	TSSOP 16-Lead Plastic Package
AT-264TR*	Tape and Reel

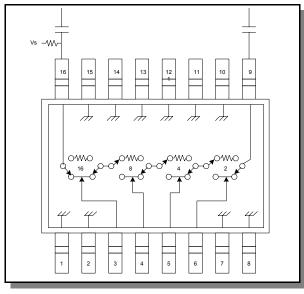
^{*} Reference Application Note M513 for reel size information.

Absolute Maximum Ratings ¹

Parameter	Absolute Maximum	
Input Power 50 MHz 500 - 2000 MHz	+27 dBm +34 dBm	
Control Voltage	-0.5 V ≤ V _C ≤ 8.5 V	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-65°C to +150°C	

^{1.} Exceeding any one or combination of these limits may cause permanent damage to this device.

Functional Schematic ^{2,3}



- 2. Blocking caps are required on all RF ports (39 pF used for data measurements).
- 3. Vs = $+5 \pm 0.2$ VDC must be applied at RF1 or RF2 using a 10 Kohm or greater pull-up resistor.

Pin Configuration

Pin No.	Function	Pin No.	Function	
1	Ground	9	RF2	
2	Ground	10	Ground	
3	VC1	11	Ground	
4	VC2	12	Ground	
5	VC3	13	Ground	
6	VC4	14	Ground	
7	Ground	15	Ground	
8	Ground	ound 16 RF1		

Visit www.macom.com for additional data sheets and product information.

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Electrical Specifications⁴: $T_A = 25^{\circ}C$, $Z_0 = 50 \Omega$, $V_S = 5 V$

Parameter	Test Conditions	Units	Min	Тур	Max
Reference Insertion Loss	0.5 - 1.0 GHz 0.5 - 2.0 GHz	dB dB	_	2.0 2.2	2.4 2.6
Attenuation Accuracy	0.5 - 1.0 GHz 0.5 - 2.0 GHz	± (0.15 dB + ± (0.3 dB +		uation settin uation settin	
VSWR	1.0 - 1.5 GHz 0.5 - 2.0 GHz	Ratio Ratio	_	1.5:1 1.9:1	_
Trise, Tfall	10% to 90% RF, 90% to 10% RF	nS	_	500	_
Ton, Toff	50% Control to 90% RF, 50% Control to 10% RF	nS	_	500	_
Transients	In Band	mV	_	75	_
1 dB Compression	Input Power 0.5 GHz 0.9 GHz	dBm dBm	_	25 25	_
IP ₂	0.5 GHz 0.5 - 2.0 GHz Measured Relative to Input (for two-tone Input Power up to +5 dBm)	dBm dBm		65 71	_
IP ₃	0.5 GHz 0.5 - 2.0 GHz Measured Relative to Input (for two-tone Input Power up to +5 dBm)	dBm dBm	_	43 47	_

^{4.} External DC blocking capacitors are required on all RF ports. Loss varies at 0.003 dB/°C.

Truth Table

VC1	VC2	VC3	VC4	Attenuation (dB)
1	1	1	1	Reference Insertion Loss
1	1	1	0	2
1	1	0	1	4
1	0	1	1	8
0	1	1	1	16
0	0	0	0	30

Logic $0 = 0 \pm 0.2 \text{ V}$.

Logic 1 = +5 V @ 30 μA maximum current total.

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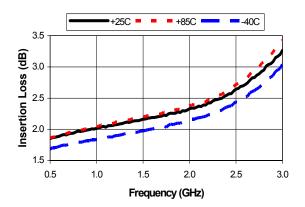


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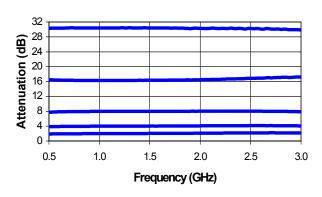
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Typical Performance Curves

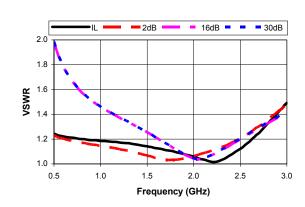
Insertion Loss



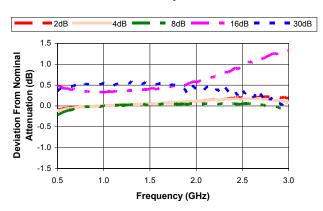
Attenuation



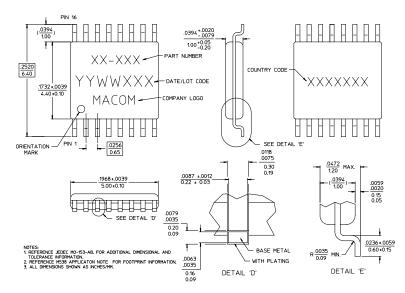
VSWR



Attenuation Accuracy



TSSOP-16



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