TIMER

GENERAL DESCRIPTION

The NJM555 monolithic timing circuit is a highly stable controller capable of producing accruate time delays or oscillation. In the time delay mode, delay time is precisely controlled by only two external parts: a resistor and a capacitor. For operation as an oscillator, both the free running frequency and the duty cycle are accurately controlled by two external resistors and a capacitor.

Terminals are provided for triggering and resetting. The circuit will trigger and reset on falling waveforms. The output can source or sink up to 200mA or drive TTL circuits.

 $(4.5V \sim 16V)$

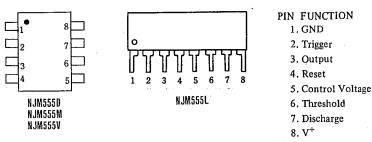
DIP8, DMP8, SSOP8, SIP8

■ FEATURES

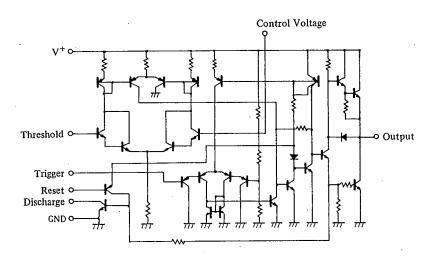
JRC

- Operating Voltage
- Less Number of External Components
- Package Outline
- Bipolar Technology

PIN CONFIGURATION

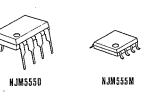


EQUIVALENT CIRCUIT



-New Japan Radio Co.,Ltd.

PACKAGE OUTLINE







ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | RATINGS | UNIT V | |
|-----------------------------|--------|-------------|-----------|--|
| Supply Voltage | V+ | 18 | | |
| Power Dissipation | PD | (DIP8) 500 | mW | |
| | | (DMP8) 300 | mW | |
| | | (SSOP8) 250 | mW | |
| | | (SIP8) 800 | mW | |
| Operating Temperature Range | Topr | -40~+85 °C | | |
| Storage Temperature Range | Tstg | -40~+125 | Ĉ | |

ELECTRICAL CHARACTERISTICS

(V⁺=5~15V, Ta=25℃)

(Ta=25℃)

| PARAMETER | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|----------------------------|-----------------|---|-------|-------|------|--------|
| Operating Voltage | V ⁺ | | 4.5 | | 16 | v |
| Operating Current (Note 1) | 1 _{cc} | $V^+=5V, R_L=\infty$ | - | 3.0 | 6.0 | mA |
| Operating Current (Note 1) | I _{CC} | $V^+=15V, R_L=\infty$ | - | 10 | 15 | mA |
| Timing Error (Note 2) | | | | | | 1 |
| Initial Accuracy | E | $Ta = -20 \sim 75^{\circ}C, V^{+} = 5 \sim 15V$ | - | 1.0 | · | % |
| Drift with Temperature | E, | $Ta = -20 \sim 75^{\circ}C, V^{+} = 5 \sim 15V$ | _ | 50 | | ppm/°C |
| Drift with Supply Voltage | E, | $Ta = -20 \sim 75^{\circ}C, V^{+} = 5 \sim 15V$ | - | 0.1 | | %/V |
| Threshold Voltage | V_{th} | | - | 2/3 | — | ×V* |
| Trigger Voltage | VT | V ⁺ =15V | _ | 5.0 | — . | v |
| Trigger Voltage | Vr | V*=5V | - | 1.67 | - | v |
| Trigger Current | I _T | | | 0.5 | - | μA |
| Reset Voltage | V _R | | 0.4 | 0.5 | 1.0 | v |
| Reset Current | I _R | | — | 0.1 | | mA |
| Threshold Current | I _{th} | | _ | 0.1 | 0.25 | μΑ |
| Control Voltage Level | V _{CL} | V ⁺ =15V | 9 | 10 | 11 | V |
| Control Voltage Level | V _{CL} | V ⁺ =5V | 2.6 | 3.33 | 4.0 | V |
| Output Voltage (Low) | V _{OL} | $V^+=15V$ Isink=10mA | - | 0.1 | 0.25 | V |
| Output Voltage (Low) | V _{OL} | V ⁺ =15V Isink=50mA | - | 0.4 | 0.75 | v v |
| Output Voltage (Low) | V _{OL} | $V^{+}=15V$ Isink=100mA (Note 3) | - | 2.0 | 2.5 | v |
| Output Voltage (Low) | V _{OL} | $V^{+}=15V$ Isink=200mA (Note 3) | 1 — | · 2.5 | · · | v |
| Output Voltage (Low) | VoL | $V^+=5V$ Isink=5mA | _ | 0.25 | 0.35 | v |
| Output Voltage (High) | Voii | V ⁺ =15V Isource=200mA (Note 3) | | 12.5 | - | V |
| Output Voltage (High) | VoH | $V^+=15V$ Isource=100mA (Note 3) | 12.75 | 13.3 | — · | V |
| Output Voltage (High) | V _{OH} | $V^+=15V$ Isource=40mA | | 13.5 | | v |
| Output Voltage (High) | Voit | $V^+=5V$ Isource=100mA | 2.75 | 3.3 | - | v |
| Rise Time of Output | t _r | No Loading | | 100 | | ns |
| Fall Time of Output | t _f | No Loading | - | 100 | - | ns |

Note 1: Low output condition (When the output is high, it is lower than the low output condition by ImA in the standard specification.) Note 2: R_A , $R_B=1k\sim 100k\Omega$, $C=0.1\mu$ F, $V^+=15V$ from 5V

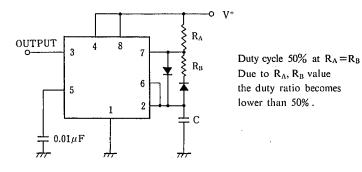
Note 3: Not specified for NJM555M/NJM555E

6

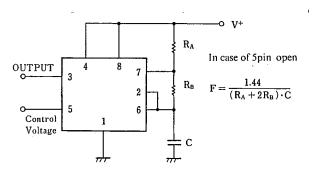
———— New Japan Radio Co.,Ltd.-

TYPICAL APPLICATION

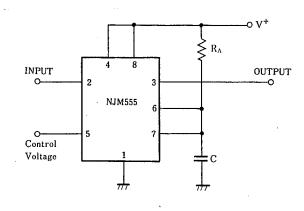
(1) 50% Duty Cycle Oscillator



(2) Oscillatoion frequency can be changed by changing the control voltage.



(3) Pulse Width Modulation

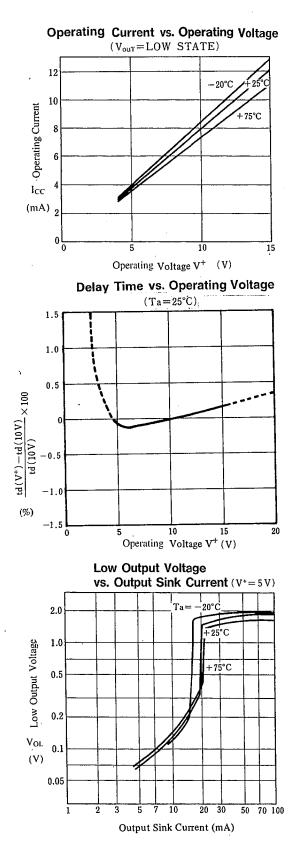


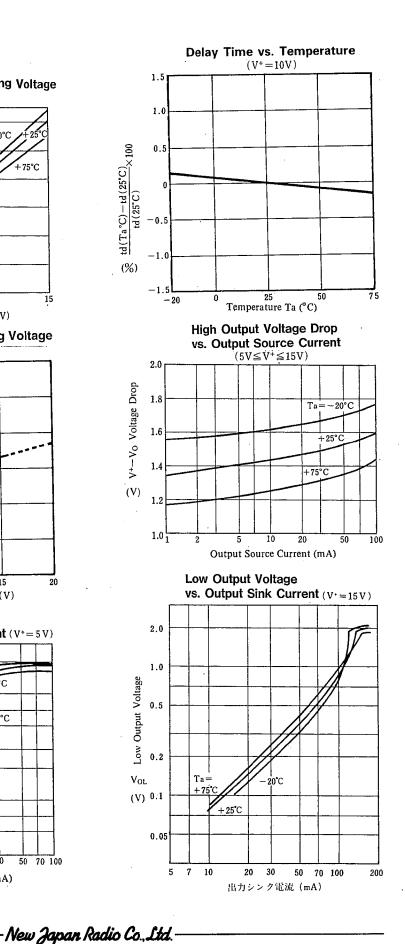
-New Japan Radio Co.,Ltd.

6-9

6

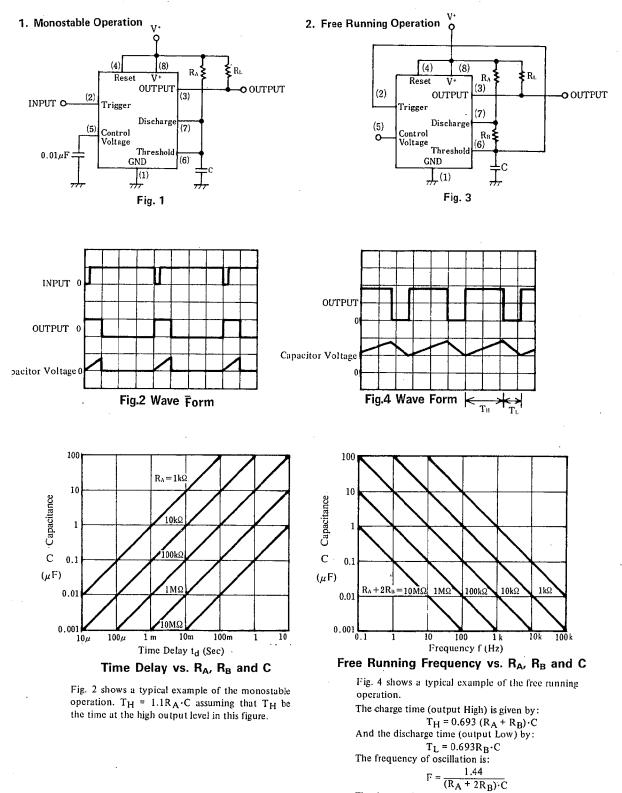
TYPICAL CHARACTERISTICS





6

TYPICAL CHARACTERISTICS



—— New Japan Radio Co.,Ltd.

The duty cycle is:

 $D = \frac{T_H}{T_H + T_L} = \frac{R_A + R_B}{R_A + 2R_B}$

MEMO

[CAUTION] The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative usages of the product and not intended for the guarantee or permission of any right including the industrial rights.

New Japan Radio Co., Ltd.