

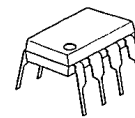
SINGLE-SUPPLY DUAL OPERATIONAL AMPLIFIER

■ GENERAL DESCRIPTION

The NJM2904 consists of two independent, high gain, internally frequency compensated operation amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks, and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the NJM2904 can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm 15V$ power supplies.

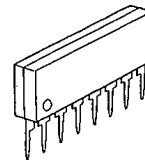
■ PACKAGE OUTLINE



NJM2904D



NJM2904M



NJM2904L



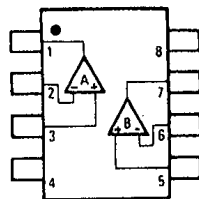
NJM2904V

■ FEATURES

- Single Supply
- Operating Voltage (+3V ~ +32V)
- Low Operating Current (0.7mA typ.)
- Slew Rate (0.5V/ μs typ.)
- Bipolar Technology
- Package Outline DIP8, DMP8, SIP8, SSOP8

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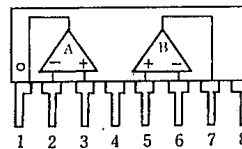
■ PIN CONFIGURATION



NJM2904D
NJM2904M
NJM2904V

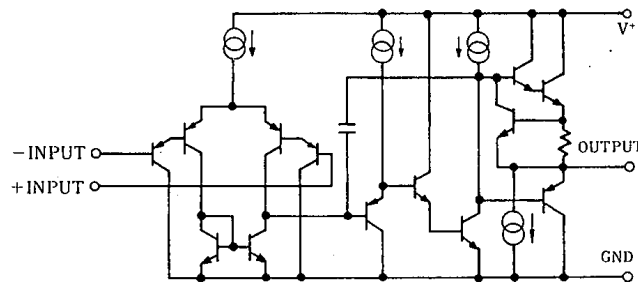
PIN FUNCTION

1. A OUTPUT
2. A-INPUT
3. A+INPUT
4. GND
5. B+INPUT
6. B-INPUT
7. B OUTPUT
8. V*



NJM2904L

■ EQUIVALENT CIRCUIT (1/2 Shown)



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺ (V ⁺ /V ⁻)	32(or ±16)	V
Differential Input Voltage	V _{ID}	32	V
Input Voltage	V _{IC}	-0.3~+32	V
Power Dissipation	P _D	(DIP8) 500	mW
		(DMP8) 300	mW
		(SSOP8) 300	mW
		(SIP8) 800	mW
Operating Temperature Range	T _{opr}	-40~+85	°C
Storage Temperature Range	T _{stg}	-50~+125	°C

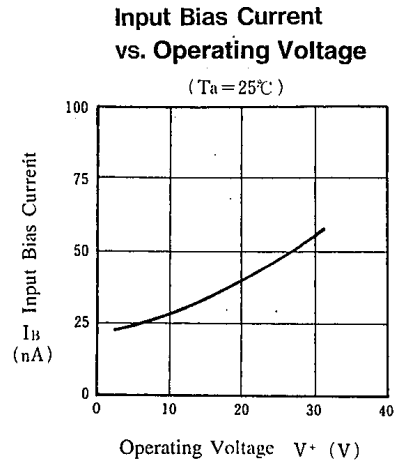
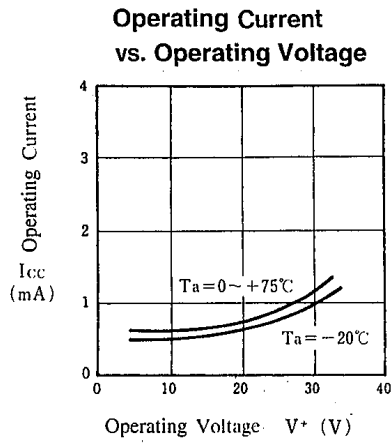
■ ELECTRICAL CHARACTERISTICS

(Ta=25°C V⁺=5V)

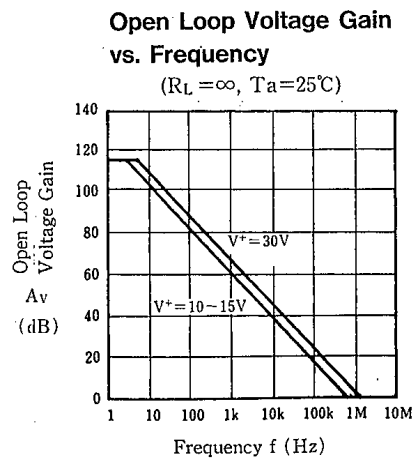
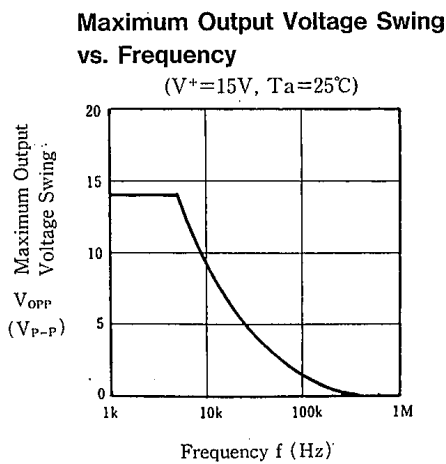
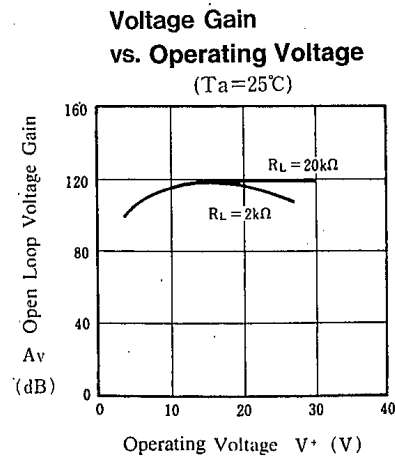
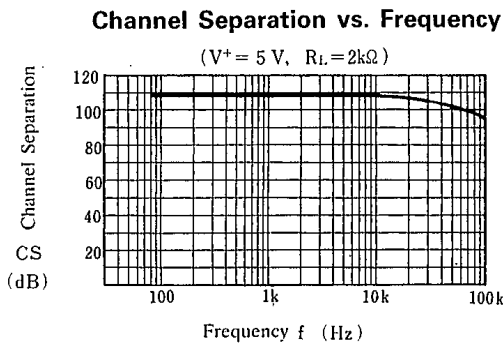
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V _{IO}	R _S =0Ω	—	2	7	mV
Input Offset Current	I _{IO}		—	5	50	nA
Input Bias Current	I _B		—	25	250	nA
Large Signal Voltage Gain	A _V	R _L ≥2kΩ	—	100	—	dB
Maximum Output Voltage Swing	V _{OM}	R _L =2kΩ	3.5	—	—	V
Input Common Mode Voltage Range	V _{ICM}		0~3.5	—	—	V
Common Mode Rejection Ratio	CMR		—	85	—	dB
Supply Voltage Rejection Ratio	SVR		—	100	—	dB
Output Source Current	I _{SOURCE}	V _{IN} ⁺ =1V, V _{IN} ⁻ =0V	20	30	—	mA
Output Sink Current	I _{SINK}	V _{IN} ⁺ =0V, V _{IN} ⁻ =1V	8	20	—	mA
Channel Separation	CS	f=1k~20kHz, Input Referred	—	120	—	dB
Operating Current	I _{CC}	R _L =∞	—	0.7	1.2	mA
Slew Rate	SR	V ⁺ /V ⁻ =±15V	—	0.5	—	V/μs
Unity Gain Bandwidth	f _T	V ⁺ /V ⁻ =±15V	—	0.2	—	MHz

4

TYPICAL CHARACTERISTICS

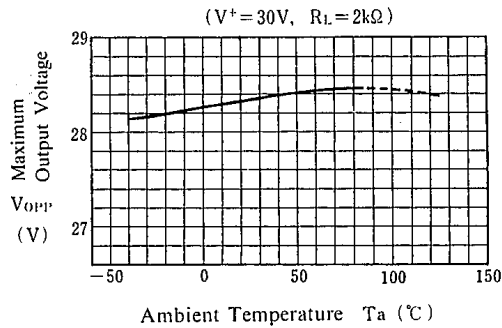


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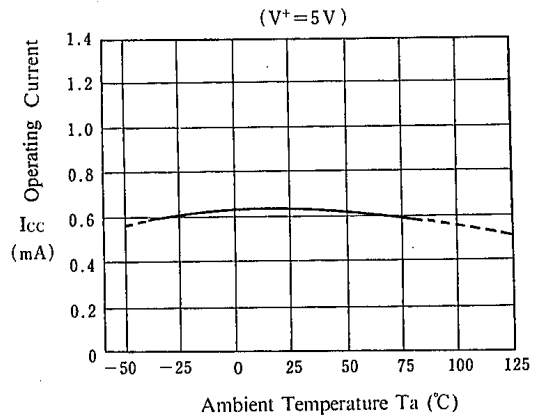


■ TYPICAL CHARACTERISTICS

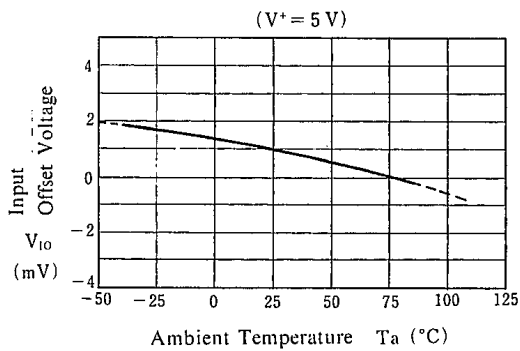
Maximum Output Voltage Swing vs. Temperature



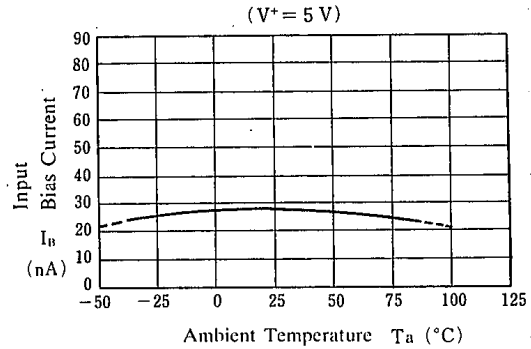
Operating Current vs. Temperature



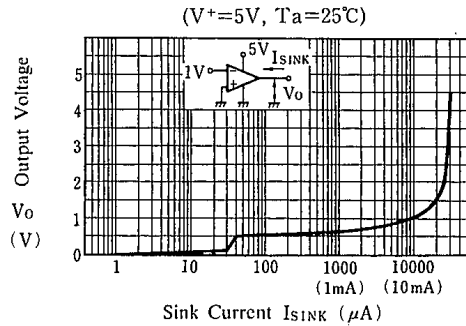
Input Offset Voltage vs. Temperature



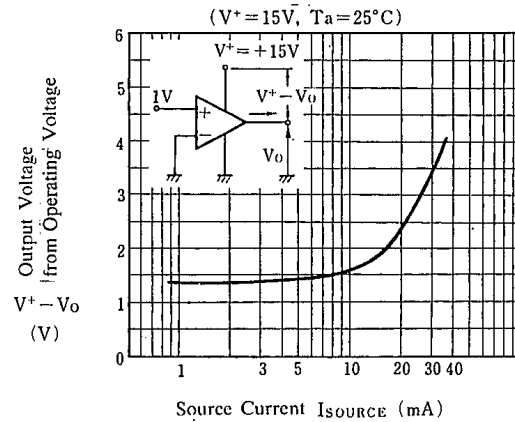
Input Bias Current vs. Temperature



Output Voltage vs. Sink Current



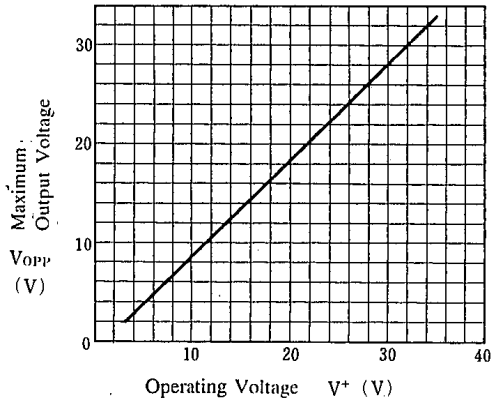
Source Current



■ TYPICAL CHARACTERISTICS

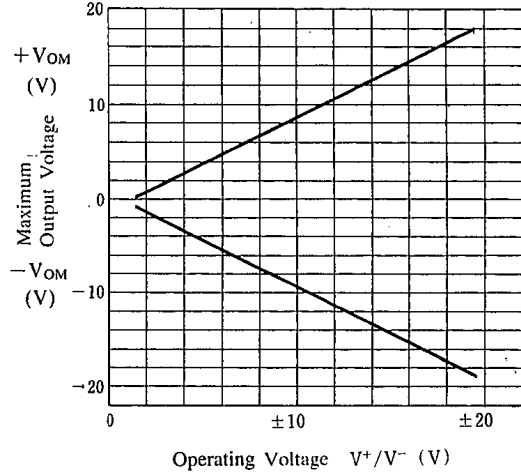
Maximum Output Voltage

($R_L = 2k\Omega$, $T_a = 25^\circ\text{C}$)



Maximum Output Voltage vs. Operating Voltage

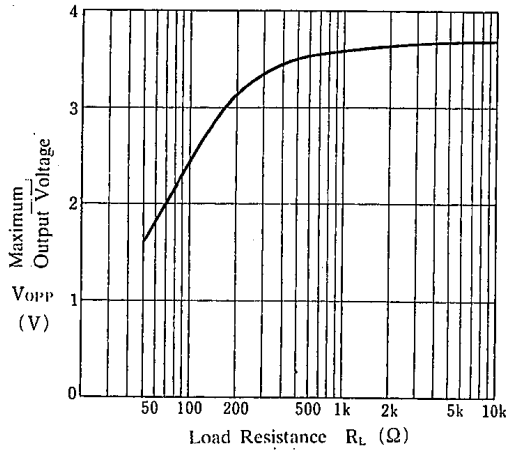
($R_L = 2k\Omega$, $T_a = 25^\circ\text{C}$)



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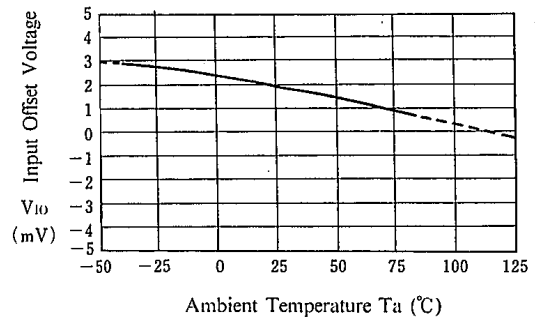
Maximum Output Voltage Swing vs. Load resistance

($V^+ = 5\text{V}$, $T_a = 25^\circ\text{C}$)



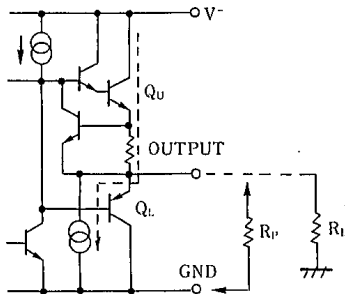
Input Offset Voltage vs. Temperature

($V^+ = 5\text{V}$)



■ APPLICATION

- Improvement of Cross-over Distortion
Equivalent circuit at the output stage

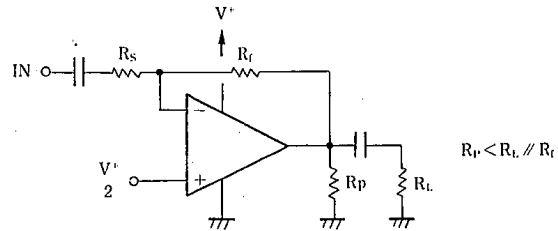
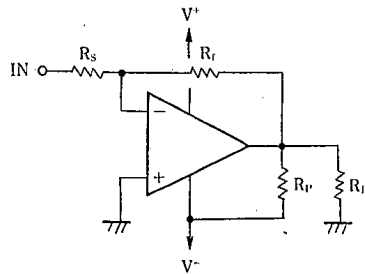


NJM2904, in its static state (No in and output condition) when design, Q_U being biased by constant current (break down beam) yet, Q_L stays OFF.

While using with both power source mode, the cross-over distortion might occur instantly when Q_L ON.

There might be cases when application for amplifier of audio signals, not only distortion but also the apparent frequency bandwidth being narrowed remarkably.

It is advisable especially when using both power source mode, constantly to use with higher current on Q_U than the load current (including feedback current), and then connect the pull-down resistor R_P at the part between output and GND pins.



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MEMO

[CAUTION]

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