

## High Dynamic Range Low Noise Amplifier 800 - 1000 MHz

**AM50-0003  
V4**

### Features

- Low Noise Figure: 1.2 dB
- High Input: +18 dBm at 8 V, 60 mA bias  
+6.5 dBm at 3 V, 20 mA bias
- High Gain: 15 dB
- Single Supply: +3 to +8 VDC
- Low Cost SOIC-8 Plastic Package
- Adjustable current: 20 to 80 mA with external resistor

### Description

M/A-COM's AM50-0003 is a high dynamic range, GaAs MMIC, low noise amplifier in a low cost, SOIC 8-Lead, surface mount, plastic package. It employs external input matching to obtain optimum noise figure performance and operating frequency flexibility. The AM50-0003 also features flexible biasing to control the current consumption vs. dynamic range trade-off. The AM50-0003 can operate from any supply voltage in the 3V to 8V range. Its current can be controlled over a range of 20 mA to 80 mA with an external resistor.

The AM50-0003 is ideally suited for use where low noise figure, high gain, high dynamic range, and low power consumption are required. Typical applications include receiver front ends in AMPS, GSM, and ETACS base stations. It is also useful as a gain block, buffer, driver, and IF amplifier in both fixed and portable cellular and 900 MHz ISM systems.

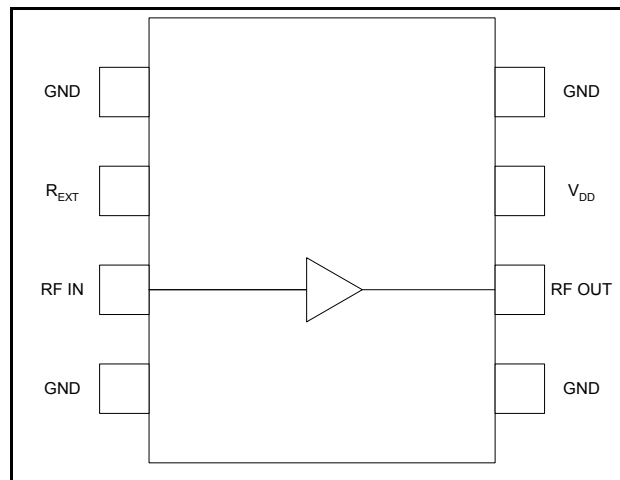
The AM50-0003 is fabricated using a low-cost 0.5-micron gate length GaAs process. The process features full passivation for increased performance reliability. The AM50-0003 is 100% RF tested to ensure performance specification compliance.

### Ordering Information

Part Number	Package
AM50-0003	Bulk Packaging
AM50-0003TR	1000 piece reel
AM50-0003SMB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

### Functional Block Diagram



### Pin Configuration

Pin No.	Pin Name	Description
1	GND	RF and DC Ground
2	R <sub>EXT</sub>	External Current Control (optional)
3	RF IN	RF Input of the amplifier
4	GND	RF and DC Ground
5	GND	RF and DC Ground
6	RF OUT	RF Output of the amplifier
7	V <sub>DD</sub>	Positive supply voltage
8	GND	RF and DC Ground

### Absolute Maximum Ratings <sup>1</sup>

Parameter	Absolute Maximum
V <sub>DD</sub>	+10 VDC
Input Power	+17 dBm
Current <sup>2</sup>	120 mA
Channel Temperature <sup>3</sup>	+150°C
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

1. Exceeding any one or combination of these limits may cause permanent damage.
2. When pin #2 is used to increase current. (see note 6)
3. Thermal resistance (θ<sub>jc</sub>) = +88°C/W

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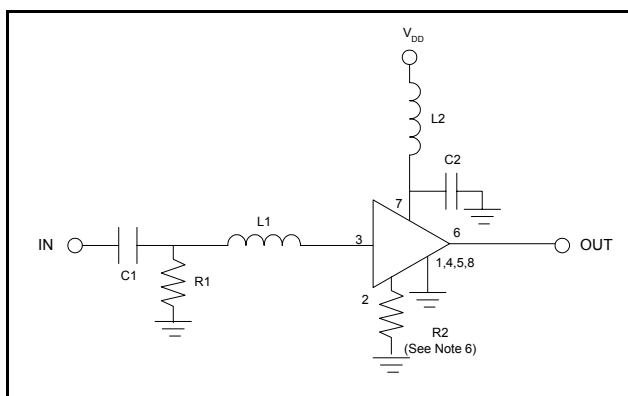
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**Electrical Specifications:  $T_A = +25^\circ\text{C}$ ,  $Z_0 = 50 \Omega$ ,  $F=900 \text{ MHz}$ ,  $P_{in} = -30 \text{ dBm}$**

Parameter	Test Conditions	Units	Min	Typ	Max
Gain	5 V, 60 mA <sup>4</sup>	dB	13	15	—
	3 V, 20 mA	dB	12	13.5	—
Noise Figure	5 V, 60 mA <sup>4</sup>	dB	—	1.20	1.50
	3 V, 20 mA	dB	—	1.25	1.50
Input VSWR	—	Ratio	—	2.0:1	—
Output VSWR	—	Ratio	—	1.5:1	—
Output 1 dB Compression	5 V, 60 mA <sup>4</sup>	dBm	—	16.5	—
	3 V, 20 mA	dBm	—	8.5	—
Input IP3	5 V, 60 mA <sup>4</sup>	dBm	12.5	15	—
	3 V, 20 mA	dBm	4.0	6.5	—
Reverse Location	—	dB	—	25	—

4. Using external 15 Ohm resistor. See functional block diagram herein.

**Functional Schematic**



**Component List <sup>5</sup>**

Part	Value	Case Size	Manufacturer	Purpose
C1	47 pF	0603	Murata	DC Block
C2	470 pF	0603	Murata	By-Pass
L1	12 nH	0603	Coilcraft	Tuning
L2	12 nH	0603	Coilcraft	RF Choke
R1	5.1K Ohms	0603	Panasonic	DC Return
R2	See Note 6	0603	Panasonic	Optional current control

5. All External circuitry parts are readily available, low cost surface mount components (.060 in. x .030 in. or .080 in. x .050 in.)
6. Pin 2 allows use of an external resistor to ground for optional, higher current. For 20 mA operation, no resistor is used.

For  $I_{DD} \sim 40 \text{ mA}$ ,  $R_2 = 43 \text{ ohms}$ ;  
 $I_{DD} \sim 60 \text{ mA}$ ,  $R_2 = 15 \text{ ohms}$ ;  
 $I_{DD} \sim 80 \text{ mA}$ ,  $R_2 = 10 \text{ ohms}$ .

**Handling Procedures**

Please observe the following precautions to avoid damage:

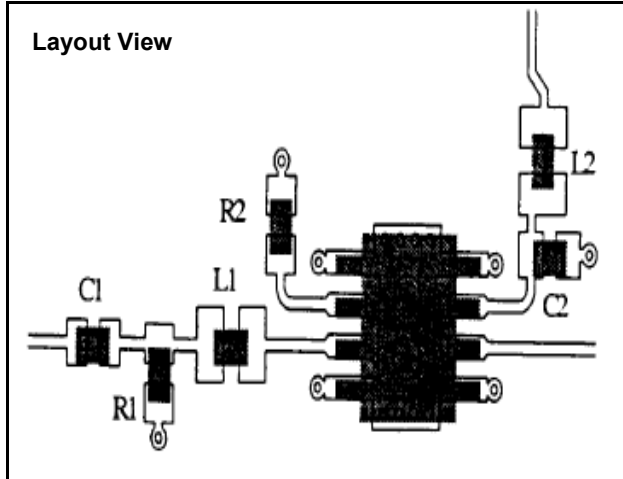
**Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

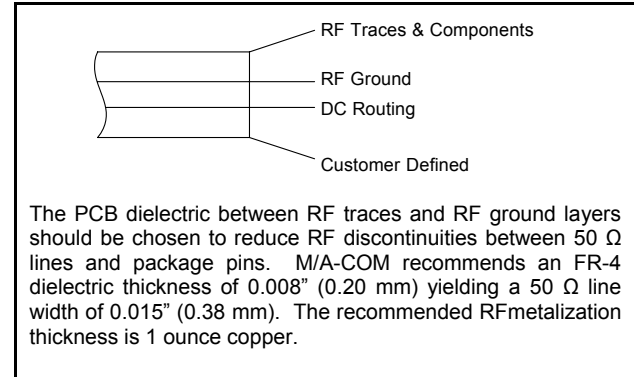
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**Recommended PCB Configuration**



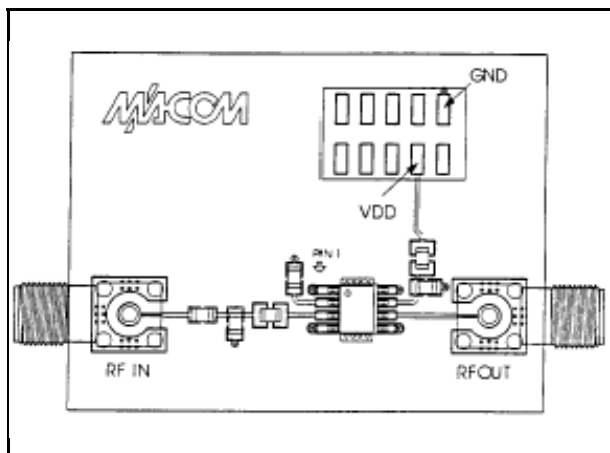
**Cross Section View**



**Designer's Kit AM50-0003SMB**

The AM50-0003SMB Designer's Kit allows for immediate evaluation of M/A-COM's AM50-0003. The Designer's Kit includes an evaluation board with mounted AM50-0003 and five loose AM50-0003's. The evaluation board consists of the recommended external surface mount circuitry, RF connectors, and a DC multi-pin connector, all mounted to a multi-layer FR-4 PCB. The AM50-0003SMB evaluation PCB is illustrated below with all functional ports labeled.

**AM50-0003 Evaluation Board**



**Evaluation PCB & RF Connector Losses**

Port Reference	Approximate RF Loss
LNA Input	0.1 dB @ 900 MHz
LNA Output	0.1 dB @ 900 MHz

The DC connector on the Designer's Kit PCB allows convenient DC line access. This is accomplished by the one or more of the following methods.

7. A mating female multi-pin connector (newark Electronics Stock # 46F-4658, not included).
8. Wires soldered to the necessary pins (not included).
9. Clip leads (not included).

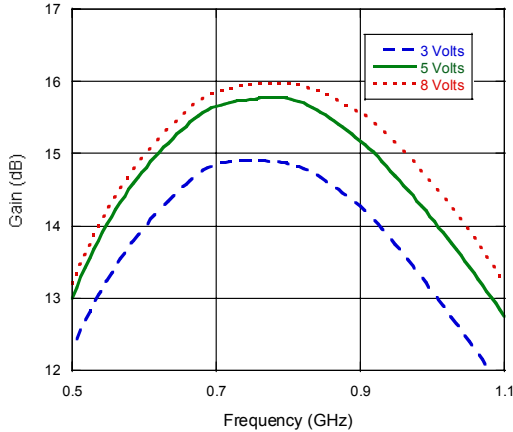
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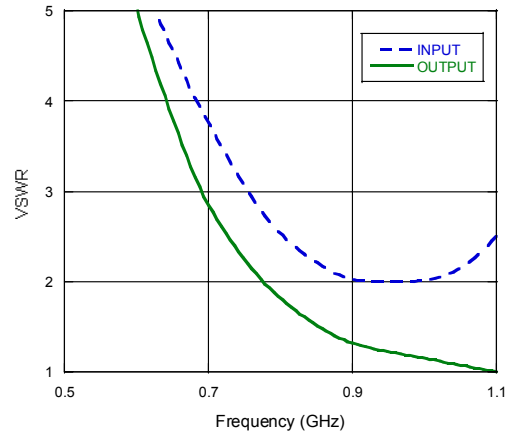
**Typical Performance Curves**

**Test Conditions:  $T_A = +25^\circ\text{C}$ ,  $Z_0 = 50 \Omega$ ,  $V_{DD} = 5 \text{ V}$ ,  $I_{DD} = 60 \text{ mA}$  unless otherwise specified**

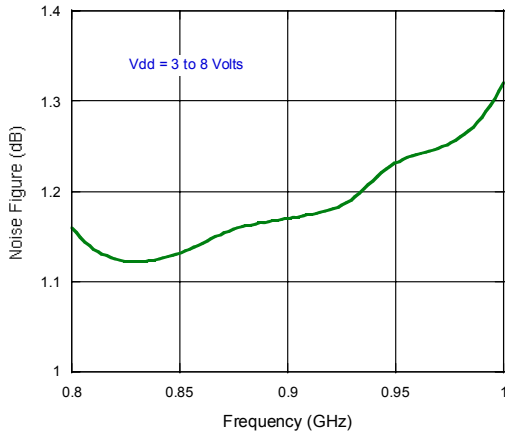
**Gain vs. Frequency**



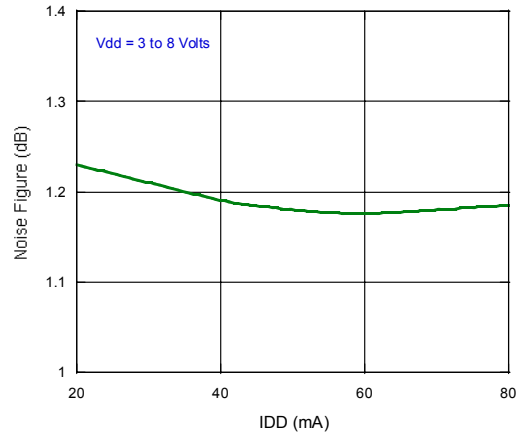
**VSWR vs. Frequency**



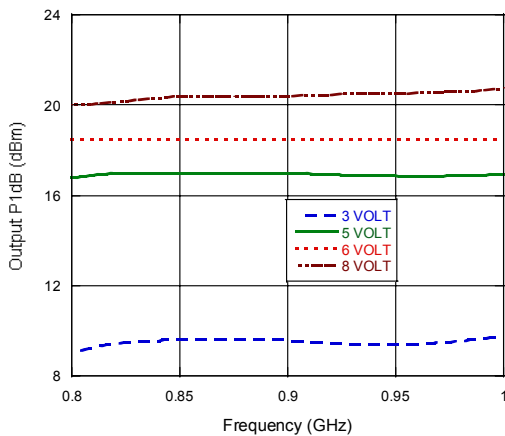
**Noise Figure vs. Frequency**



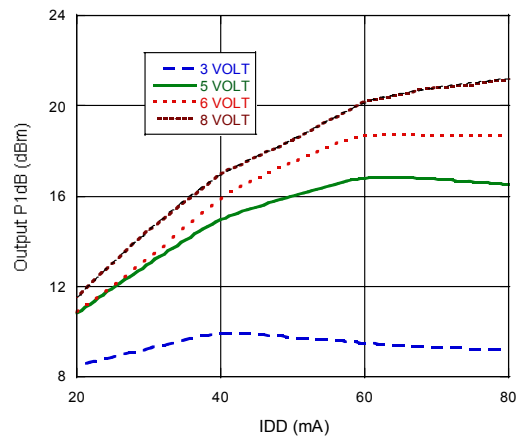
**Noise Figure vs. Current,  $F = 900 \text{ MHz}$**



**Output P1 dB vs. Frequency**



**Output P1 dB vs. Current,  $F = 900 \text{ MHz}$**



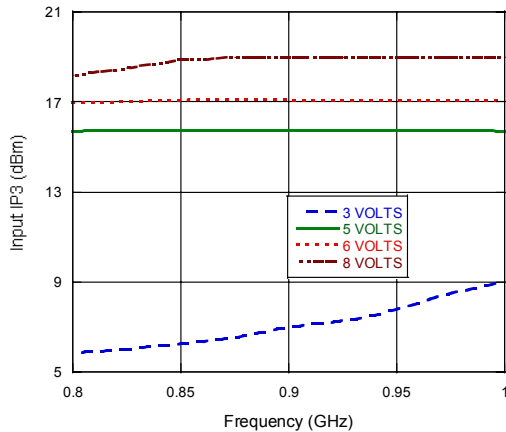
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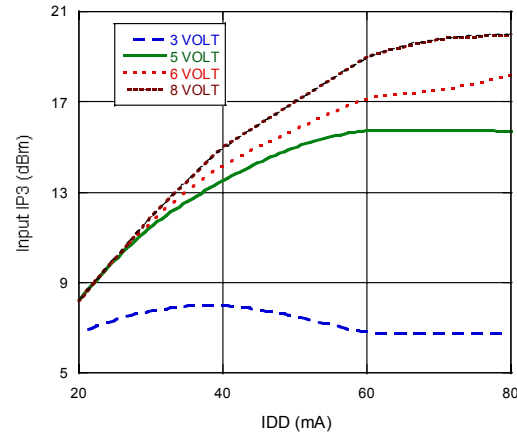
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**Input IP3 vs. Frequency**



**Input IP3 vs. Current,  $F = 900 \text{ MHz}$**



**SOIC-8**

The drawing shows the top and side views of the SOIC-8 package. Key features include:
 

- Top View:** Shows the part number (XX-XXX), date/lot code (YYWWXXXX), company logo (MACOM), and orientation mark. Dimensions include a width of .2284/.2440 (5.80/6.20) and a height of .1497/.1574 (3.80/4.00).
- Side View:** Shows the package height of .0075/.0098 (.19/.25) and the lead length of .016/.050 (.40/1.27).
- Bottom View:** Shows the lead pitch of .0532/.0688 (1.35/1.75), a lead width of .050 (1.27), and a lead length of .013/.020 (.33/.51).
- Dimensions:** A total length of .1890/.1968 (4.80/5.00) and a lead spacing of .0040/.0098 (.10/.25) are also indicated.

**NOTES:**

1. REFERENCE JEDEC MS-012-AA, FOR ADDITIONAL DIMENSIONAL AND TOLERANCE INFORMATION.
2. REFERENCE M538 APPLICATION NOTE FOR FOOTPRINT INFORMATION.
3. ALL DIMENSIONS SHOWN AS INCHES/MM.

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