

For LF Power Amplifier, 50W Output Large Power Switching Applications

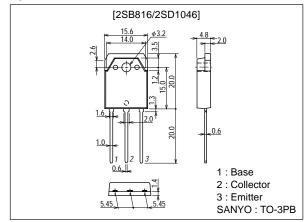
Features

- · Capable of being mounted easily because of onepoint fixing type plastic molded package (Interchangeable with TO-3).
- · Wide ASO because of built-in ballast resistance.
- \cdot Goode dependence of f_{T} on current and good HF characteristic.

Package Dimensions

unit:mm

2022A



():2SB816

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		(–)150	V
Collector-to-Emitter Voltage	VCEO		(-)120	V
Emitter-to-Base Voltage	V _{EBO}		(–)6	V
Collector Current	IС		(–)8	Α
Collector Current (Pulse)	lCP		(-)12	Α
Collector Dissipation	PC	Tc=25°C	80	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-40 to +150	°C

Electrical Characteristics at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offit
Collector Cutoff Current	ICBO	V _{CB} =(-)80V, I _E =0			(-)0.1	mA
Emitter Cutoff Current	I _{EBO}	V _{EB} =(-)4V, I _C =0			(-)0.1	mA
DC Current Gain	h _{FE} 1	V _{CE} =(-)5V, I _C =(-)1A	60*		200*	
	h _{FE} 2	V _{CE} =(-)5V, I _C =(-)5A	20			
Gain-Bandwidth Product	fT	V _{CE} =(-)5V, I _C =(-)1A		15		MHz
Output Capacitance	C _{ob}	V _{CB} =(-)10V, f=1MHz		(220)		pF
				160		pF

^{*:} The 2SB816/2SD1046 are classified by 1A h_{FE} as follows:

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Rank	D	Е
h _{FE}	60 to 120	100 to 200

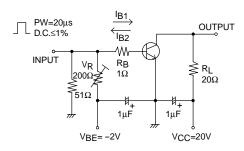
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SANYO Electric Co.,Ltd. Semiconductor Company
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110-8534 JAPAN

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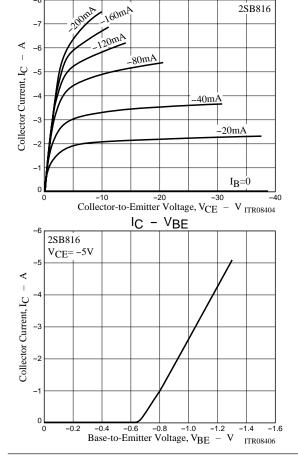
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Base-to-Emitter Voltage	V _{BE}	V _{CE} =(-)5V, I _C =(-)1A			1.5	٧
Collector-to-Emitter Saturation Voltage	VCE(sat)	I _C =(-)5A, I _B =(-)0.5A		1.0	2.0	V
Collector-to-Base Breakdown Voltage	V _(BR) CBO	I _C =(-)5mA, I _E =0	(–)150			٧
Collector-to-Emitter Breakdown Voltage	V _(BR) CEO	I _C =(−)5mA, R _{BE} =∞	(-)120			V
		I _C =(-)50mA, R _{BE} =∞	(-)120			V
Emitter-to-Base Breakdown Voltage	V _{(BR)EBO}	I _E =(-)5mA, I _C =0	(–)6			V
Turn-ON Time	ton	See specified test circuit.		(0.22)		μs
				0.22		μs
Fall Time	+.	See specified test circuit.		(0.37)		μs
	t _f			1.02		μs
Storage Time		See specified test circuit.		(0.93)		μs
	tstg t			6.66		μs

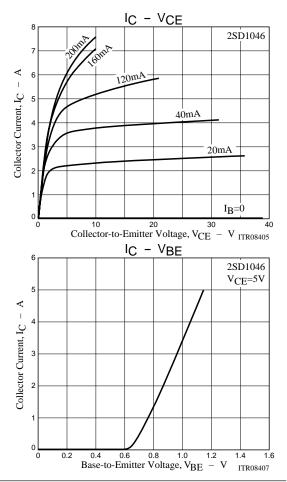
Swicthing Time Test Circuit

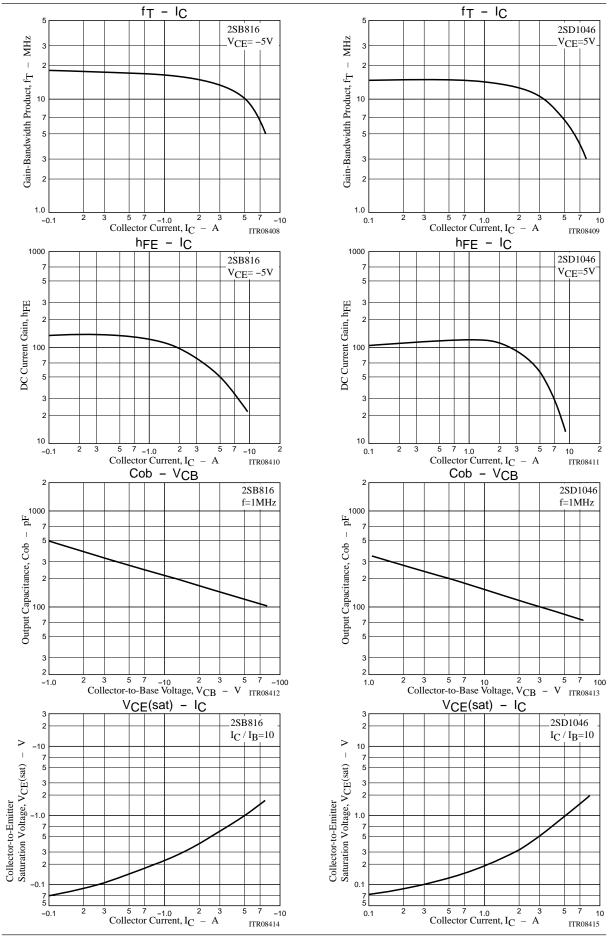


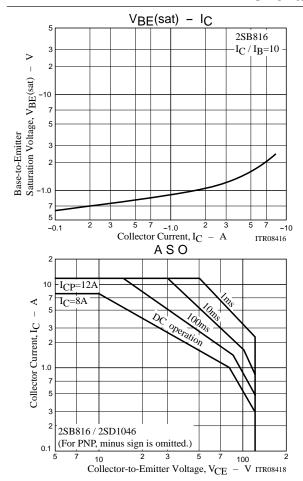
IC - VCE

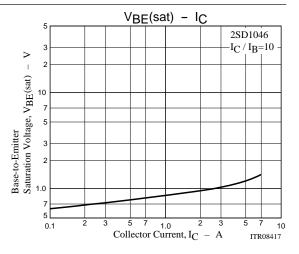
 $I_{C}=10I_{B1}=-10I_{B2}=1A$ (For PNP, the polarity is reversed.)











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