Advanced Specification 50A - 60A DC/DC Power Module 48V Input, 3.3V Output

- High Efficiency, 92% Typ
- High Power Density, 68 W/in³ (3.3V @ 60A)
- Fast Dynamic Response, 200 ms, ±250 mVpeak Typ.
- Low Output Ripple, 70mVp-p Typ.
- Wide input voltage range (36-75V)
- 1,500 Vdc isolation voltage
- Max. case temperature +100°C
- UL 1950/UL_C 1950 Recognized
- TUV to EN 60950 Type Approved
- Demonstrated compliance with isolation requirements equivalent to Basic Insulation per UL 60950
- Patent Pending Paralleling Feature
- Heatsinks available as an option for extended operation





The PKL 4000 series represents another one of Ericsson's "industry first" achievements in the continuing development of our "Third Generation" of high-density, high-efficiency power modules. The PKL 4110A PI module packs 68W/in³ at 92% efficiency (3.3V @ 60A) in an half-brick footprint that has been enhanced to include two additional output pins for motherboard connection reliability at this high power.

These breakthrough features come from using the most advanced patented topology utilizing integrated magnetics and synchronous rectification on a low-resistivity multilayer PCB.

This product features fast dynamic response times and low output ripple, which are important parameters when supplying low-voltage logics.



The PKL 4000 PI Series also is especially well suited for limited board space and high dynamic load applications.

Ericsson's PKL 4000 Series Power Modules address the converging "New Telecom" market, by specifying the input voltage range in accor-dance with ETSI specifications. The PKL 4000 Series also offers overvoltage protection, under-voltage protection, overtemperature protection, soft-start, paralleling, and short circuit protection.

These modules are manufactured on highly automated manufacturing lines. Ericsson's world-class quality commitment is reflected in our standard five year warranty. Ericsson Inc., Power Modules has been an ISO 9001 certified Suplier since 1991.

For a complete product program, please reference the back page.

General

Connections

Pin	Designation	Function
1	-INPUT	Negative input
2	CASE	Connected to base plate
3	REMOTE ON/OFF	Remote control (primary) to turn-on and turn-off the output
4	+INPUT	Positive input.
5, 10	-OUTPUT	Negative output (two pins)
6	-SENSE	Negative remote sense
7	TRIM	Output voltage adjust
8	+SENSE	Positive remote sense
9, 11	+OUTPUT	Positive output (two pins)

Note: If the remote sense is not needed the "-Sense" should be connected to "-Out" and the "+Sense" should be connected to "+Out."

Weight

110 grams

Case

Aluminum base plate with metal standoffs.

Pins

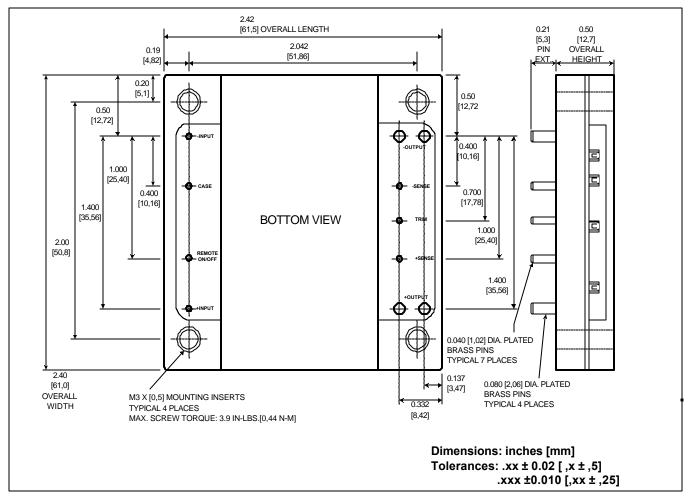
Pin Material: Brass

Pin Plating: Tin/Lead over Nickel

Input $T_C < T_{Cmax}$ unless otherwise specified

Characteristics		Conditions	min	typ	max	Units
Vi	Input voltage range		36		75	V _{dc}
V _l off	Turn-off input voltage	Ramping from higher voltage	31	33		V _{dc}
V _I on	Turn-on input voltage	Ramping from lower voltage		34	36	V _{dc}
I _I max	Max. Input Current	$V_i = V_i min = 36 \text{ V}$	PKL 41		6.5 5.5	A _{dc}
l rush	Inrush Current	Except Charging of C			1	A _{dc}
Cı	Input capacitance			3.5		μF
Pli	Input idling power	I _O =0, T _C =-30+95°C		6		w
P _{RC}	Input stand-by power	T _C =-30+95°C, RC Open		0.4	0.6	W

Mechanical Data



PKL 4110A PI / PKL 4110 PI

 $T_C = -40...+100$ °C, $V_I = 36...75V$ unless otherwise specified.

Output

Characteristics		Conditions	Device	Output			Unit
		Conditions		min	typ	max	
Voi	Output voltage initial setting and accuracy	T _C =+25 °C, V _I = 53 V, b =I _{Omax}	All	3.25	3.3	3.35	V
•01	Output adjust range	I _O =I _{Omax}	All	2.64		3.63	V
Vo	Output voltage tolerance band	I _O =0 to I _O max	All	3.2		3.4	V
	Line regulation	I _O =I _O max	All		5	15	mV
	Load regulation	V _I = 53V, I _O =0 to I _O max,	All		5	15	mV
V _t r	Load transient voltage deviation	Load step = 0.25 x I _{Omax} di/dt = 1A/us	All		+/-250		mV
t _{t r}	Load transient recovery time		All		200		μs
ts	Start-up time	From VI connection to $V_O = 0.9 \times V_{Onom}$	All		10	15	ms
lo	Output current		PKL 4110A PI PKL 4110 PI	0		60 50	А
P _{Omax}	Max output power	At V _O = V _{Onom}	PKL 4110A PI PKL 4110 PI			198 165	W
l _{lim}	Current limit threshold	V _O = 0.96 V _{Onom} @ T _C <100°C	PKL 4110A PI PKL 4110 PI	61.0 51.0	64.8 54.5	72.0 62.0	А
I _{sc}	Short circuit current		PKL 4110A PI PKL 4110 PI		65 55	74 64	А
Voac	Output ripple & noise	I _O =I _O max, f < 20 MHz	All		70	150	mV _{p-p}
SVR	Supply voltage rejection (ac)	f < 1kHz	All	-50			dB
OVP	Over voltage protection	V _I = 53V,	All	3.9	4.4	5.0	V

Miscellaneous

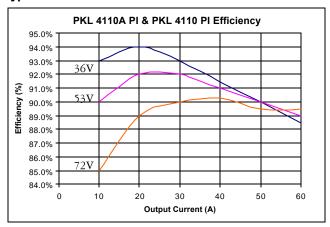
Characteristics		Conditions	Device min typ max		Unit
η	Efficiency	$I_{o} = I_{omax}, V_{i} = 53V, T_{c} = +25^{\circ}C$	All	92	%
P,	Power dissapation.	$I_{o} = I_{omax}, V_{i} = 53V, T_{c} = +25^{\circ}C$	PKL 4110A PI	17.2	W
ď		O Omax 1 · C	PKL 4110 PI	14.3	
fs	Switching frequency	I _o = 01.0 x I _{Omax}	All	150	kHz

Absolute Maximum Ratings

Characteristics		min	max	Unit
T _C	Maximum Operating Case Temperature	-40	+100	°C
Ts	Storage temperature	-40	+125	°C
VI	Input voltage: Continuous	- 0.5	+80	V dc
	Transient (100ms)		+100	V dc
V _{ISO}	Isolation voltage (input to output test voltage)	1,500		V dc
V _{RC}	Remote control voltage		12	Vdc
I ² t	Inrush transient		1	A ² s

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as "no destruction limits," are normally tested with one parameter at a time exceeding the limits of output data or electrical characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Typical Characteristics



Product Program

Vi	V _o /I _o	P _o max	Ordering Number
48/60V	3.3V/60A	198W	PKL 4110A PI
48/60V	3.3V/50A	165W	PKL 4110 PI

The PKL 4000 DC/DC power modules will be available with the different options listed in the Product Options Table

Please check with the factory for availability.

Product Options

Option	Suffix	Example
Negative remote on/off logic	-	PKL 4110A PI
Industry Standard Trim (i.e. V _o Adjust)	Т	PKL 4110A PIT
Positive remote on/off logic	Р	PKL 4110A PIP
Lead length 0.145" ± 0.010"	LA	PKL 4110A PILA

Information given in this Advanced Specification is believed to be accurate and reliable. No responsibility is assumed for the consequences of its use for any infringement of patents or other rights of third parties that may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Ericsson Power Modules. These products are sold only according to Ericsson Power Modules' general conditions of sale, unless otherwise confirmed in writing. Specifications are subject to change without notice.

Ericsson Inc., Power Modules

1700 International Pkwy. Richardson, Texas 75081 Phone: 877-374-2642 www.ericsson.com/powermoduless The latest and most complete information can be found on our website!

Advanced Specification

EN/LZT 108 5167 R1b © Ericsson Inc., Power Modules, November, 2002