

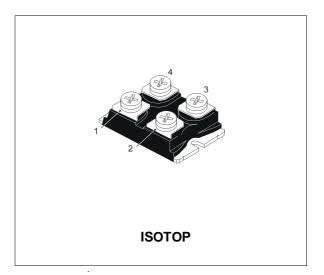
ESM3045DV

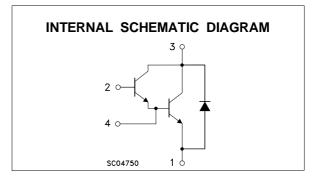
NPN DARLINGTON POWER MODULE

- HIGH CURRENT POWER BIPOLAR MODULE
- VERY LOW Rth JUNCTION CASE
- SPECIFIED ACCIDENTAL OVERLOAD AREAS
- ULTRAFAST FREEWHEELING DIODE
- FULLY INSULATED PACKAGE (UL COMPLIANT)
- EASY TO MOUNT
- LOW INTERNAL PARASITIC INDUCTANCE

INDUSTRIAL APPLICATIONS:

- MOTOR CONTROL
- SMPS & UPS
- DC/DC & DC/AC CONVERTERS
- WELDING EQUIPMENT





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VCEV	Collector-Emitter Voltage (V _{BE} = -5 V)	600	V
$V_{\text{CEO}(\text{sus})}$	Collector-Emitter Voltage $(I_B = 0)$	450	V
V_{EBO}	Emitter-Base Voltage ($I_C = 0$)	7	V
lc	Collector Current	24	А
Ісм	Collector Peak Current (t _p = 10 ms)	36	A
IB	Base Current	2.5	А
I _{BM}	Base Peak Current (t _p = 10 ms)	5	A
Ptot	Total Dissipation at $T_c = 25$ °C	125	W
Visol	Insulation Withstand Voltage (RMS) from All Four Terminals to Exernal Heatsink	2500	V
T _{stg}	Storage Temperature	-55 to 150	°C
Tj	Max. Operating Junction Temperature	150	°C

THERMAL DATA

R _{thj-case}	Thermal Resistance Junction-case (transistor)	Max	1	°C/W
R _{thj} -case	Thermal Resistance Junction-case (diode)	Max	2	°C/W
R _{thc-h}	Thermal Resistance Case-heatsink With Conductive			
	Grease Applied	Max	0.05	°C/W

ELECTRICAL CHARACTERISTICS ($T_{case} = 25 \ ^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{CER} #	Collector Cut-off Current ($R_{BE} = 5 \Omega$)	$V_{CE} = V_{CEV}$ $V_{CE} = V_{CEV}$ $T_j = 100 \ ^{\circ}C$			1.5 17	mA mA
I _{CEV} #	Collector Cut-off Current (V _{BE} = -5)	$V_{CE} = V_{CEV}$ $V_{CE} = V_{CEV}$ $T_j = 100 \ ^{\circ}C$			1 12	mA mA
I _{EBO} #	Emitter Cut-off Current $(I_C = 0)$	$V_{EB} = 5 V$			1	mA
Vceo(sus)*	Collector-Emitter Sustaining Voltage (I _B = 0)	$ I_C = 0.2 A \qquad L = 25 \text{ mH} $ $ V_{clamp} = 450 \text{ V} $	450			V
h _{FE} *	DC Current Gain	I _C = 20 A V _{CE} = 5 V		120		
V _{CE(sat)} *	Collector-Emitter Saturation Voltage			1.2 1.3 1.4 1.6	2 2	V V V V
V _{BE(sat)} *	Base-Emitter Saturation Voltage	$ I_{C} = 20 \text{ A} I_{B} = 1.2 \text{ A} \\ I_{C} = 20 \text{ A} I_{B} = 1.2 \text{ A} T_{j} = 100 ^{\circ}\text{C} $		2.1 2.1	3	V V
di _C /dt	Rate of Rise of On-state Collector		125	160		A/µs
V _{CE} (3 µs)⊷	Collector-Emitter Dynamic Voltage			4.5	8	V
V _{CE} (5 μs)⊷	Collector-Emitter Dynamic Voltage			2.5	4.5	V
ts tf tc	Storage Time Fall Time Cross-over Time	$ I_C = 15 \ A V_{CC} = 50 \ V \\ V_{BB} = -5 \ V R_{BB} = 0.6 \ \Omega \\ V_{clamp} = 450 \ V I_{B1} = 0.3 \ A \\ L = 0.17 \ mH T_j = 100 \ ^oC $		2.1 0.15 0.5	4 0.4 1.2	μs μs μs
V _{CEW}	Maximum Collector Emitter Voltage Without Snubber	$\begin{split} I_{CWoff} &= 24 \ A I_{B1} = 1.2 \ A \\ V_{BB} &= -5 \ V V_{CC} = 50 \ V \\ L &= 0.1 \ mH R_{BB} = 0.6 \ \Omega \\ T_j &= 125 \ ^{\circ}C \end{split}$	450			V
V _F *	Diode Forward Voltage	$I_F = 20 \text{ A}$ $T_j = 100 ^{\circ}\text{C}$		1.7	2	V
I _{RM}	Reverse Recovery Current	V _{CC} = 200 V I _F = 20 A di _F /dt = -125 A/μs L < 0.05 μH T _j = 100 °C		11	14	A

 \ast Pulsed: Pulse duration = 300 $\mu s,$ duty cycle 1.5 %

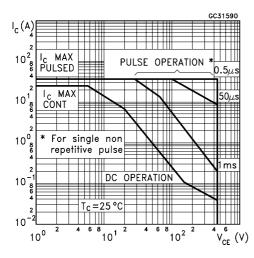
See test circuits in databook introduction

To evaluate the conduction losses of the diode use the following equations:

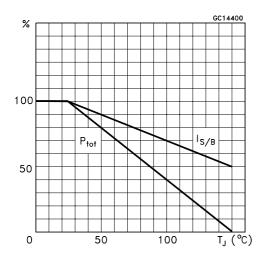
 $V_F = 1.47 + 0.0026 \ I_F \qquad P = 1.47 \ I_{F(AV)} + 0.0026 \ I^2_{F(RMS)}$

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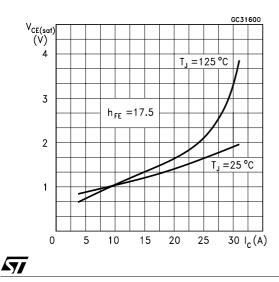
Safe Operating Areas



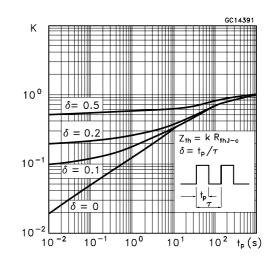
Derating Curve



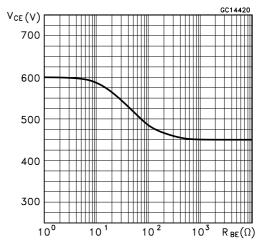
Collector Emitter Saturation Voltage



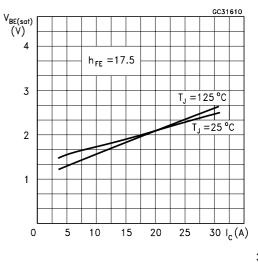
Thermal Impedance



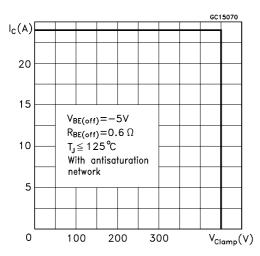
Collector-emitter Voltage Versus base-emitter Resistance



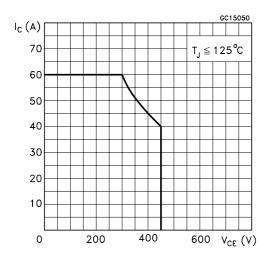




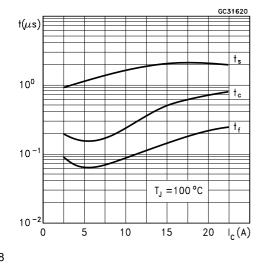
Reverse Biased SOA



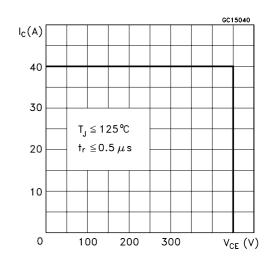
Reverse Biased AOA



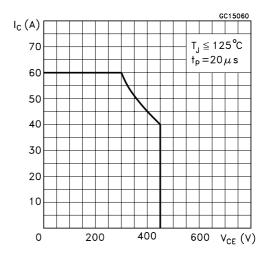
Switching Times Inductive Load



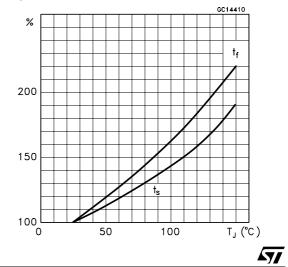
Foward Biased SOA



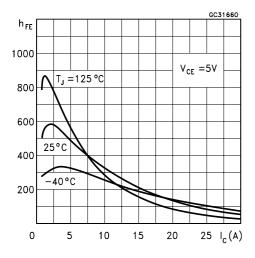
Forward Biased AOA



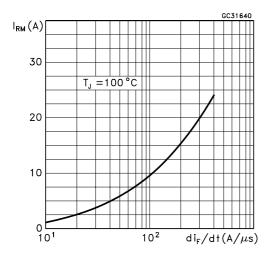
Switching Times Inductive Load Versus Temperature



Dc Current Gain

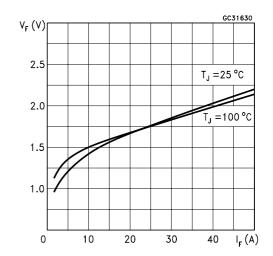


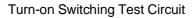
Peak Reverse Current Versus diF/dt

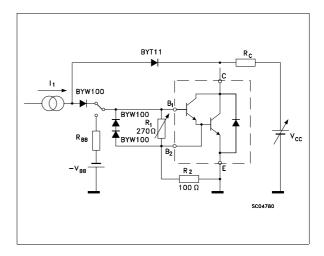


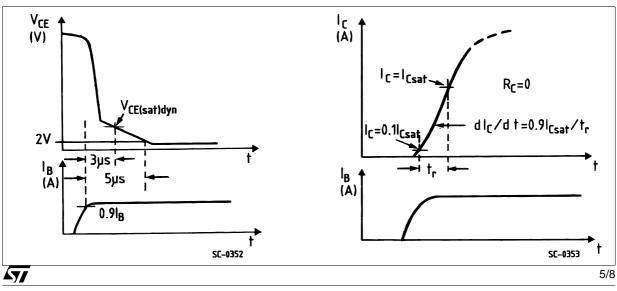
Turn-on Switching Waveforms

Typical V_F Versus I_F

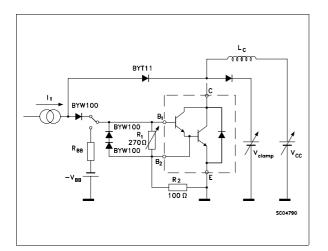




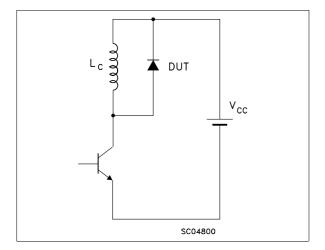




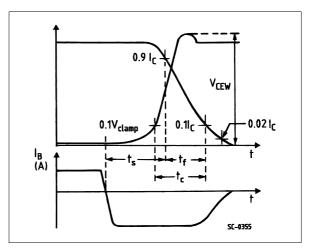
Turn-on Switching Test Circuit



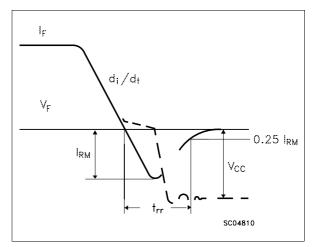
Turn-off Switching Test Circuit of Diode



Turn-off Switching Waveforms



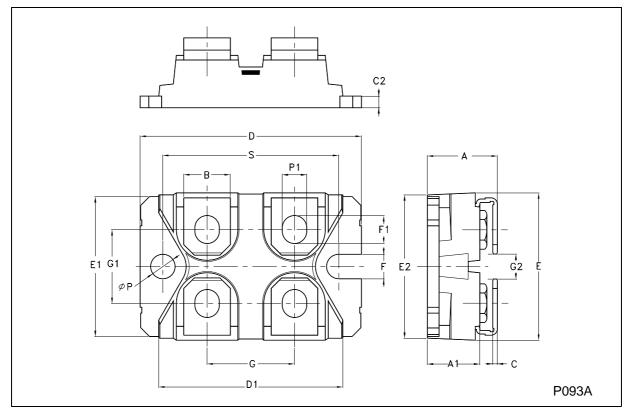
Turn-off Switching Waveform of Diode



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. TYP.	MAX. 12.2 9.1 8.2 0.85 2.05	MIN. 0.465 0.350 0.307 0.029 0.076	TYP.	0.358 0.322
; ;	9.1 8.2 0.85 2.05	0.350 0.307 0.029		
; ; ;	8.2 0.85 2.05	0.307 0.029		0.322
;	0.85 2.05	0.029		0.322 0.033
5	2.05			0.033
		0.076		
3				0.080
	38.2	1.488		1.503
5	31.7	1.240		1.248
5	25.5	0.990		1.003
5	24.15	0.938		0.950
24.8			0.976	
)	15.1	0.586		0.594
5	12.8	0.496		0.503
	4.3	0.137		1.169
	4.3	0.161		0.169
	5	0.181		0.196
	4.3	0.157		0.169
	4.4	0.157		0.173
	5 24.8 9 3 9 4 9 4 9 4 9 4 9 4 9 4 9 4 9 4	24.8 15.1 12.8 4.3 4.3 5 4.3 4.3 4.4	24.8 15.1 0.586 0 12.8 0.496 4.3 0.137 4.3 0.161 5 0.181 4.3 0.157 4.4 0.157	24.8 0.976 0 15.1 0.586 3 12.8 0.496 4.3 0.137 4.3 0.161 5 0.181 4.3 0.157 4.4 0.157





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