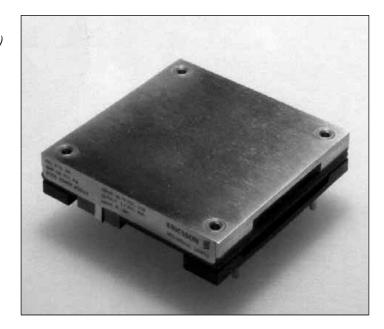
# Advanced Specification 50-60A DC/DC Power Modules 48V Input, 2.5V Output

- High efficiency 88% Typ (50A) at full load
- High power density, 51.7 W/in³, (2.5V @ 60A)
- Fast dynamic response, 200µs, ± 200 mVpeak Typ
- Low output ripple, 80 mVp-p Typ
- Parallelable with no external components
- Wide input voltage range (36-75V)
- 1,500Vdc isolation voltage
- Max case temperature +100℃
- Designed to meet UL 1950 and EN 60950



The PKL series represents another one of Ericsson's "industry first" achievements in the continued development of our "Third Generation" of high-density, high-efficiency power modules. This module packs 51.7 W/in<sup>3</sup> at 87% efficiencies (2.5V @ 60A) in an industry standard footprint that has been enhanced to include two additional output pins for motherboard connection reliability. These breakthrough features come from using the most advanced patented topology utilizing integrated magnetics and synchronous rectification on a low-resistivity multilayer PCB.

This product features fast dynamic response times and low output ripple, which are important parameters when supplying low-voltage logics. The PKL series also is especially suited for limited board space and high dynamic load applications.

Ericsson's PKL Power Module has been designed with the converging "New Telecoms" market in mind, by specifying the input voltage range in accordance with ETSI specifications. The PKL series also offers over-voltage protection, under-voltage protection, over-temperature protection, soft-start, and is short circuit proof.

These modules are manufactured on highly automated manufacturing lines. Ericsson's world-class quality commitment is reflected in our standard five-year warranty. Ericsson Microelectronics has been an ISO 9001 certified supplier since 1991.

For a complete product program, please reference the back cover.



## General

## Connections

Designation	Function
-INPUT	Negative input. Connected to base plate
CASE REMOTE	Remote control (primary).
ON/OFF	To turn-on and turn-off the output
+INPUT	Positive input
-OUTPUT	Negative output, (two pins)
-SENSE	Negative remote sense
TRIM	Output voltage adjust
+SENSE	Positive remote sense
+OUTPUT	Positive output, (two pins)

Note: If the remote sense is not needed the -Sen should be connected to -Out and +Sen should be connected to +Out.

### Weight

100 grams

#### Case

Aluminum baseplate with metal standoffs.

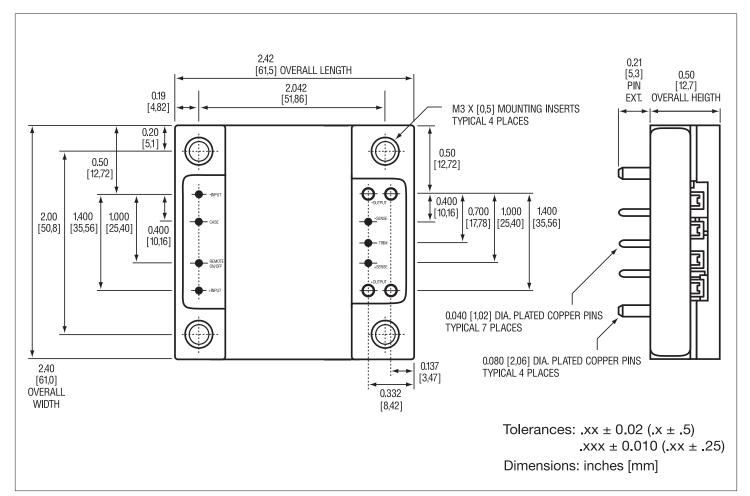
#### Pins

Pin material: Copper Alloy Pin plating: Tin/Lead over Nickel.

## Input $T_C < T_{Cmax}$

Chara	acteristics	Conditions		min	typ	max	Unit
VI	Input voltage range			36		75	Vdc
V <sub>loff</sub>	Turn-off input voltage	Ramping from higher voltage		31	33		Vdc
V <sub>Ion</sub>	Turn-on input voltage	Ramping from lower voltage			34	36	Vdc
Cl	Input capacitance			3.5			μF
I <sub>I</sub> max	Maximum input current	$V_I = V_I \text{ min}$	125 W 150 W			5.5 6.5	A
P <sub>li</sub>	Input idling power		I <sub>O</sub> = 0		6		W
P <sub>RC</sub>	Input stand-by power (turned off with RC)	V <sub>I</sub> = 50V	RC open		0.6		W
TRIM	Maximum input voltage on trim pin					6	Vdc

### **Mechanical Data**



## PKL 4119A PI/PKL 4119 PI $T_C$ = -40...+100°C, $V_I$ = 36...75 V dc unless otherwise specified.

## Output

Characteristics		Conditions	Device	e Output			
				min	typ	max	Unit
V <sub>Oi</sub>	Output voltage initial setting and accuracy	$T_C = +25^{\circ}C$ , $V_I = 53V$ , $I_O = I_{Omax}$	All	2.45	2.50	2.55	V
	Output adjust range	I <sub>O</sub> = 0 to I <sub>O</sub> max	All	2.0		2.75	V
IO	Output current		PKL 4119A PI PKL 4119 PI	0		60 50	А
VO	Output voltage tolerance band	I <sub>O</sub> = 0 to I <sub>O</sub> max	All	2.38		2.63	V
	Line regulation	I <sub>O</sub> = I <sub>O</sub> max	All		5	15	mV
	Load regulation	V <sub>I</sub> = 53V, I <sub>O</sub> = 0 to I <sub>O</sub> max	All		5	15	mV
V <sub>tr</sub>	Load transient voltage deviation	Load step = 0.25 x I <sub>O</sub> max dI/dt = 1A/µs	All		±200		mV <sub>peak</sub>
t <sub>tr</sub>	Load transient recovery time		All		200		μs
t <sub>s</sub>	Start-up time	From $V_I$ connection to $V_O = 0.9 \times V_{O^{nom}}$	All		20	30	ms
l <sub>lim</sub>	Current limit threshold	V <sub>O</sub> = 0.96 V <sub>Onom</sub> @ T <sub>C</sub> <100°C	PKL 4119A PI PKL 4119 PI	61 51	64.5 54.5	72 62	А
I <sub>SC</sub>	Short circuit current		PKL 4119A PI PKL 4119 PI		65 55	79 69	А
V <sub>Oac</sub>	Output ripple and noise	I <sub>O</sub> = I <sub>Omax</sub> f ≤ 20 MHz	All		80	150	mVp-p
SVR	Supply voltage rejection (ac)	f<1kHz	All	-50			dB
OVP	Over voltage protection	Vin = 50V	All	3.0	3.5	3.9	V

### Miscellaneous

Char	acteristics	Conditions	Device	min	typ	max	Unit
η	Efficiency	$T_A = +25^{\circ}C$ , $V_I = 53V$ , $I_O = I_{O}$ max	PKL 4119A PI PKL 4119 PI		88 89		%
P <sub>d</sub>	Power dissipation	$I_O = I_{Omax}$ , $V_I = 53V$	PKL 4119A PI PKL 4119 PI		20.5 15.4		W

## **Absolute Maximum Ratings**

Cha	racteristics	min	max	Unit
T <sub>C</sub>	Case temperature @ max output power	-40	+100	°C
T <sub>S</sub>	Storage temperature	-40	+125	°C
VI	Continuous input voltage	-0.5	+80	Vdc
V <sub>ISO</sub>	Isolation voltage (input to output test voltage)	1,500		Vdc
$v_{RC}$	Remote control voltage		12	Vdc
ı²t	Inrush transient		1	A <sup>2</sup> s

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as "no destruction limits," are normally tested with one parameter at a time exceeding the limits of output data or electrical characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

## **Product Program**

VI	V <sub>O</sub> /I <sub>O</sub>	POmax	Ordering Number
48/60 V	2.5V/60A	150W	PKL 4119A PI
48/60 V	2.5V/50A	125W	PKL 4119 PI

The PKL 4000 DC/DC power modules will be available with the different options listed in the Product Options table.

Please check with the factory for availability.

### **Product Options**

Option	Suffix	Example
Negative remote on/off logic Ericsson Unique Trim, (i.e. V <sub>out</sub> Adjust)	-	PKL 4119A PI
Positive remote on/off logic	Р	PKL 4119A PIP
Lead length of 0.145" ± 0.010"	LA	PKL 4119A PILA

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Microelectronics

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