

DUAL 2-WIDE 2-INPUT/3-INPUT AND-OR-INVERT GATE**DESCRIPTION**

The M74LS51P is a semiconductor integrated circuit containing dual 2-wide 2-input/3-input AND-OR-INVERT gates.

FEATURES

- High breakdown input voltage ($V_I \geq 15V$)
- Low power dissipation ($P_d=5.5mW$ typical)
- High speed ($t_{pd}=7ns$ typical)
- Low output impedance
- Wide operating temperature range ($T_a=-20\sim+75^{\circ}C$)

APPLICATION

General purpose, for use in industrial and consumer equipment.

FUNCTIONAL DESCRIPTION

Schottky TTL technology enables input high breakdown voltage, high speed, low power dissipation and high fan-out.

This device consists of a NOR gate with two 2-input AND gates as the inputs and a NOR gate with two 3-input AND gates as the inputs, and the following logical expressions are yielded:

$$1Y = 1A \cdot 1B \cdot 1C + 1D \cdot 1E \cdot 1F$$

$$2Y = 2A \cdot 2B + 2C \cdot 2D$$

FUNCTION TABLE

M	N	Y
L	L	H
H	L	L
L	H	L
H	H	L

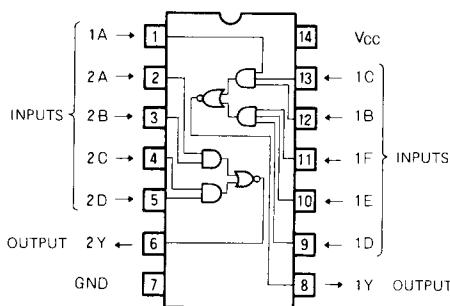
$$M = 1A \cdot 1B \cdot 1C$$

$$N = 1D \cdot 1E \cdot 1F$$

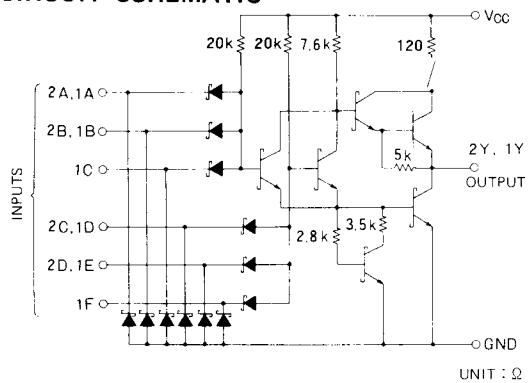
AND-OR

$$M = 2A \cdot 2B$$

$$N = 2C \cdot 2D$$

PIN CONFIGURATION (TOP VIEW)

Outline 14P4

CIRCUIT SCHEMATIC**ABSOLUTE MAXIMUM RATINGS** ($T_a = -20 \sim +75^{\circ}C$, unless otherwise noted)

Symbol	Parameter	Conditions	Limits	Unit
V _{CC}	Supply voltage		-0.5 ~ +7	V
V _I	Input voltage		-0.5 ~ +15	V
V _O	Output voltage	High-level state	-0.5 ~ V _{CC}	V
T _{opr}	Operating free-air ambient temperature range		-20 ~ +75	°C
T _{stg}	Storage temperature range		-65 ~ +150	°C

DUAL 2-WIDE 2-INPUT/3-INPUT AND-OR-INVERT GATE

RECOMMENDED OPERATING CONDITIONS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Limits			Unit
		Min	Typ	Max	
V _{CC}	Supply voltage	4.75	5	5.25	V
I _{OH}	High-level output current	0		-400	μA
I _{OL}	Low-level output current	V _{OL} \leq 0.4V V _{OL} \leq 0.5V	0	4 8	mA

ELECTRICAL CHARACTERISTICS ($T_a = -20 \sim +75^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit	
			Min	Typ *	Max		
V _{IH}	High-level input voltage			2		V	
V _{IL}	Low-level input voltage				0.8	V	
V _{IC}	Input clamp voltage	V _{CC} = 4.75V, I _{IC} = -18mA			-1.5	V	
V _{OH}	High-level output voltage	V _{CC} = 4.75V, V _I = 0.8V I _{OH} = -400 μA	2.7	3.4		V	
V _{OL}	Low-level output voltage	V _{CC} = 4.75V V _I = 2V	I _{OL} = 4mA I _{OL} = 8mA	0.25 0.35	0.4 0.5	V	
I _{IH}	High-level input current	V _{CC} = 5.25V, V _I = 2.7V V _{CC} = 5.25V, V _I = 10V			20	μA	
I _{IL}	Low-level input current	V _{CC} = 5.25V, V _I = 0.4V			0.1	mA	
I _{OS}	Short-circuit output current (Note 1)	V _{CC} = 5.25V, V _O = 0V		-20	-100	mA	
I _{ICCH}	Supply current, all outputs high	V _{CC} = 5.25V, V _I = 0V			0.8	1.6	mA
I _{ICCL}	Supply current, all outputs low	V _{CC} = 5.25V, V _I = 4.5V			1.4	2.8	mA

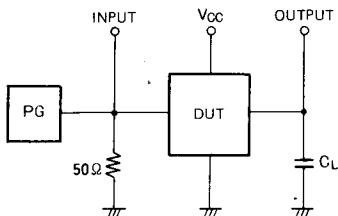
* : All typical values are at $V_{CC} = 5\text{V}$, $T_a = 25^\circ\text{C}$.

Note 1: All measurements must be done quickly and not more than one output should be shorted at a time.

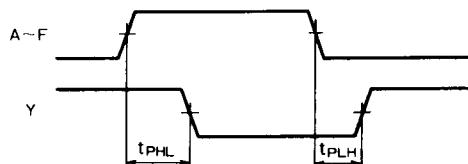
SWITCHING CHARACTERISTICS ($V_{CC} = 5\text{V}$, $T_a = 25^\circ\text{C}$, unless otherwise noted)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
t _{PLH}	Low-to-high-level output propagation time	C _L = 15 pF		6	20	ns
t _{PHL}	High-to-low-level output propagation time	(Note 2)		8	20	ns

Note 2: Measurement circuit



TIMING DIAGRAM (Reference level = 1.3V)



- (1) The pulse generator (PG) has the following characteristics:
PRR = 1MHz, t_r = 6ns, t_w = 500ns, V_p = 3Vp-p, Z₀ = 50Ω.
- (2) C_L includes probe and jig capacitance.

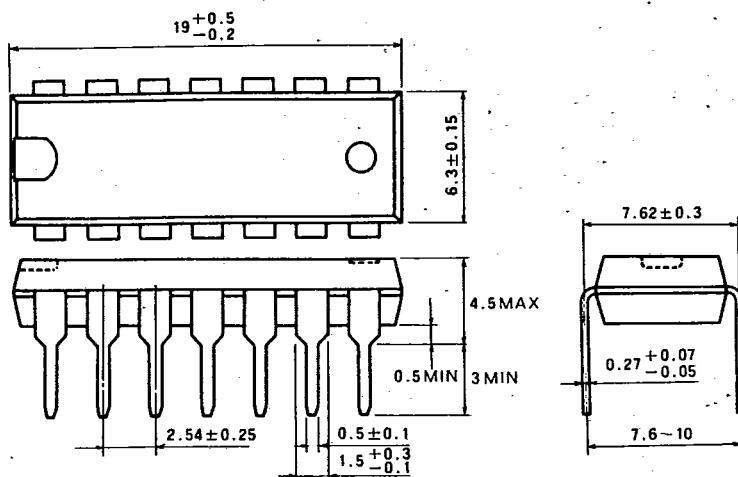
MITSUBISHI LSTTLs
PACKAGE OUTLINES

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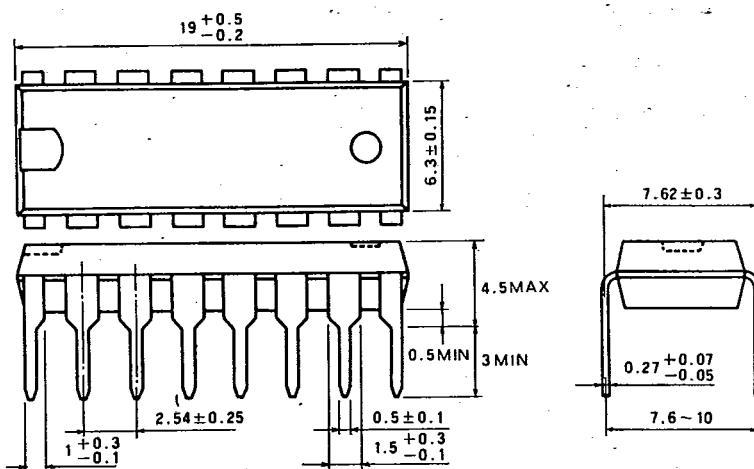
TYPE 14P4 14-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 16P4 16-PIN MOLDED PLASTIC DIL

Dimension in mm



TYPE 20P4 20-PIN MOLDED PLASTIC DIL

Dimension in mm

