# **CXA1355L**

# All Band TV Tuner IC (VHF-CATV-UHF)

## Description

The CXA1355L is a single chip tuner IC which performs as an oscillator mixer for VHF/CATV bands and UHF bands. An IF amplifier is also provided.

Use of this IC achieves a large reduction of external parts in addition to miniaturizing the tuner and increasing manufacturing productivity, reliability, and ease of design.

#### **Features**

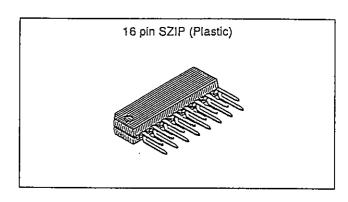
- On-chip UHF band OSC and MIX
- Low noise figure
- Reduced spurious interference
- Superior cross modulation distortion
- Ultra small package ensures tuner miniaturization

# **Applications**

- CTV tuner
- CATV UP-DOWN converter
- FM detector for 2IF satellite broadcasts

### Structure

Bipolar silicon monolithic IC



# Absolute Maximum Ratings (Ta=25 °C)

<ul> <li>Supply voltage</li> </ul>	Vcc	12	٧
• Storage temperature	Tstg	-55 to +150	$^{\circ}$ C
. 40 11 0			

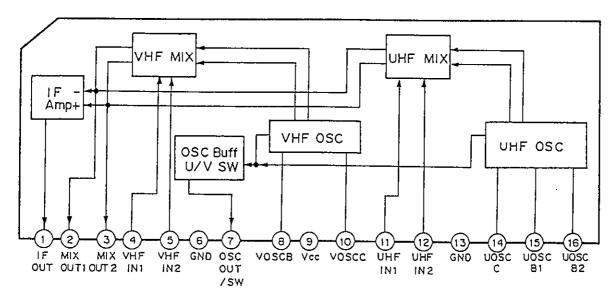
Allowable power dissipation

P<sub>D</sub> 800 mW

# **Recommended Operating Conditions**

Supply voltage
 Vcc
 8.1 to 9.9
 V
 Operating temperature
 Topr
 −20 to +75
 C

# Block Diagram and Pin Configuration



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# Pin Description

Pin No.	Symbol	Typical voltage (V)	Equivalent circuit	Description
1	IF OUT	Under VHF operation: 5.4  Under UHF operation: 5.4	Vcc	IF output.
2	MIX OUT1	7.8	300 2 × × × × × × × × × × × × × × × × × × ×	Doubles as mixer output
3	MIX OUT2	7.8 7.7		and IF Amp input.
4	VHF IN1	2.8 3.2		VHF input; normally a condenser is connected at
5	VHF IN2	2.8	4 \$3k \$3k \$3k	Pin 4 to GND and Pin 5 is used for input.
6	GND	0		GND
7	OSC OUT/ SW	3V or more	7 70k \$ \$40k	Doubles as output pin for OSC monitor and U/V switch pin. Connect 9V source through about a 5kΩ resistor for VHF reception; leave OPEN (0V) for UHF.

Pin No.	Symbol	Typical voltage (V)	Equivalent circuit	Description
8	V OSCB	2.8	10 8	VHF oscillator. Since Pin 10 is an open collector connect power supply through a
10	v oscc	9.0	3 k 3 k ₹	choke coil or a resistor of about 500Ω.
9	Vcc	9.0		Supply pin (9V).
11	UHF IN1	2.8	11	UHF input. The differential signal input to Pin 11 and
12	UHF IN2	3.2		12.
13	GND	0		GND
14	U OSCC	9.0	14	
15	U OSCB1	2.8	15 16 16	UHF oscillator. Since Pin 14 is an open collector connect power supply through a choke coil or a resistor of about $500\Omega$ .
16	U OSCB2	2.8		

# **Electrical Characteristics**

See Electrical Characteristics Test Circuit (Ta=25 °C, Vcc=9V)

Item	Symbol	Test co	ondition	Min.	Typ.	Max.	Unit
	IccV	VHF operation;	No signal	35.0	45.0	56.0	mA
Supply current	lccU	UHF operation;	No signal	36.0	46.0	57.0	mA
	CG1	VHF operation;	fnr=55MHz	23.0	26.0	29.0	dΒ
Conversion gain * 1	CG2	VHF operation;	fnr=470MHz	23.0	26.0	29.0	dB
Conversion gain .	CG3	UHF operation;	fr=470MHz	30.0	33.0	36.0	dB
	CG4	UHF operation;	fr=890MHz	26.0	29.0	32.0	dB
	NF1	VHF operation;	fr=55MHz		12.0	14.0	dB
Noise figure * 1	NF2	VHF operation;	far=470MHz		13.5	15.5	dB
Noise ligure	NF3	UHF operation;	far=470MHz		8.5	10.5	dB
	NF4	UHF operation;	far=890MHz		10.5	13.0	dB
	CM1	VHF operation;	fo=55MHz fuo= ± 12MHz	92.0	95.0		dΒμ
1% Cross modulation	CM2	VHF operation;	fo=470MHz fuo= ± 12MHz	88.0	91.0		dΒμ
*2	СМЗ	UHF operation;	fo=470MHz fuo= ± 12MHz	85.0	88.0		dΒμ
	CM4	UHF operation;	fo=890MHz fup= ± 12MHz	85.0	88.0		dΒμ
Max. output power	Pomax (sat)	50Ω load		+8.0	+12.0		dBm
	PoscV	VHF operation; $50\Omega$ load	fosc=515MHz	-19	-16	-	dBm
OSC output	PoscU	UHF operation; $50\Omega$ load	fosc=935MHz	-25	-22		dBm

<sup>\*1)</sup> Measured value for untuned inputs.

Matching NF (noise figure) results in approximately the below values.

NF2 (fre=470MHz): 12dB typ. NF4 (fre=890MHz): 10dB typ.

For VHF the input is unbalanced and for UHF input is balanced.

# \*2) Optimal reception frequency (fb) input level is -33dBm.

Interrupt frequency (fuo) is 100kHz at 40% AM; interrupt frequency level when measured with spectrum analyzer at S/I=46dB.

47p U 0SCB1 47p U OSCC GND UHF IN2 Ş F.F. 56k 17363 V OSCC -₽ ŧ 1T363 Z CC 41.362 -}} -1 V OSCB ķặ ∰ ‡ # 2,2 ¢ 2.5T GND VHF IN2 <u>-</u> 3.4 ¢ 3.5T in HN EN MIX OUT 2 MIX OUT1 <u> පූ</u> **Electrical Characteristics Test Circuit** 3.2 ¢ 6.5T ٦. T 함 100 k 3.3 k ₩<u>₩</u> 0.68 5.1k Ř₹ WS V/U BAND SW SC OUT IF OUT

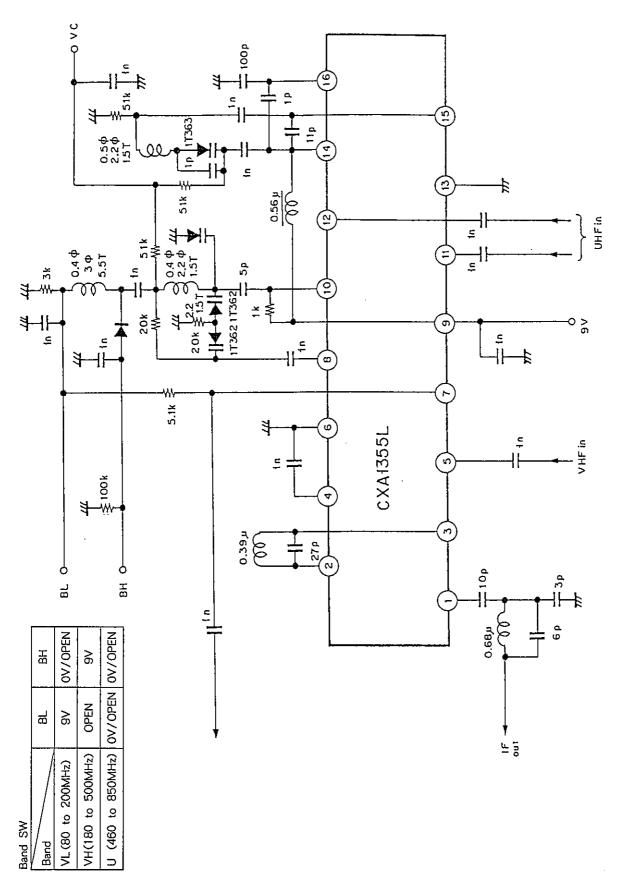
U OSCB2

Note) The UHF input balloon is inserted for measuring with a balanced input.

F IN

¥ C

Application Circuit (U/V 3 Band Tuner)



# Description of Operation (See Electrical Characteristics Test Circuit Application Circuit)

### (1) VHF Oscillator Circuit

Differential amplifier type oscillator circuit with output at Pin 10 and input at Pin 8.

Provide oscillation through applying positive feedback by inputting Pin 10 output through a combination capacitor followed by an LC resonator circuit and a feedback capacitor ro Pin 8.

Since Pin 10 is an open collector, connect the power supply through a choke coil or a resistor rated between 510 and  $1k\Omega$ .

### (2) VHF Mixer Circuit

The mixer circuit employs a double balance mixer for reduce local oscillator signal leakage. The RF is input to Pins 4 and 5. For normal use one pin is grounded and the other pin is the RF input. The RF signal is converted to IF signal with the oscillator signal. The input to the IF amplifier is output to Pins 2 and 3 simultaneously.

### (3) UHF Oscillator Circuit

UHF oscillator is formed from a differential amplifier just like the VHF oscillator.

Oscillation should be provided at the point between the transistor base and collector at the differential input as shown in the Application Circuit.

Since Pin 14 is an open collector, connect the power supply through a choke coil or a resistor rated between 510 and  $1k\Omega$ .

### (4) UHF Mixer Circuit

Double balance mixer is same as VHF mixer. The RF signal is input to Pins 11 and 12. The recommended input method is a balanced differential input from pre-stage double tune circuit.

Otherwise, the conditions and usage are the same as for the VHF Mixer Circuit.

### (5) IF Amplifier Circuit

The mixer output signal is amplified by the IF amplifier and output to Pin 1. The output impedance is nealy equal  $50\Omega$ .

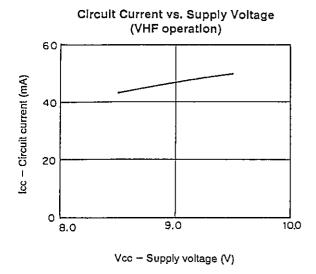
### (6) U/V Switch Circuit

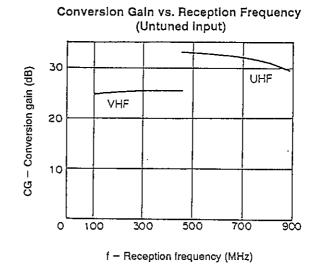
U/V mode is selected by the DC voltage at Pin 7. UHF operation is chosen by OPEN (0V) input and VHF operation by an input of 3V or more. Pin 7 also functions as an oscillator monitor output. Normally the U/V switch is implemented through the combination of a  $5.1k\Omega$  resistor switching from open /9V and a capacitor for filtering out the oscillator signal.

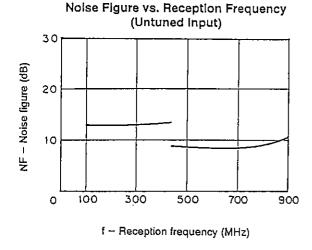
# Notes on Usage

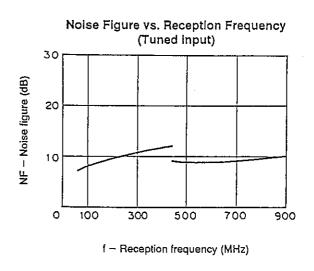
Care should be taken in placing external parts because high frequencies are present. Adjust accordingly to prevent heat problems with special care for Pins 6 and 13 (GND) whose heat dissipations accumulate.

# **Example of Representative Characteristics**

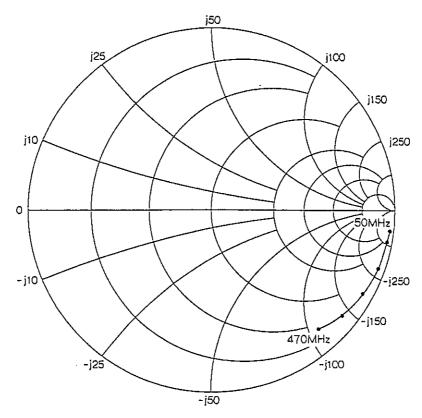




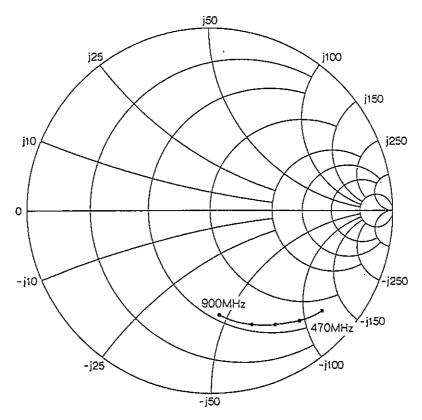




# VHF Input Impedance



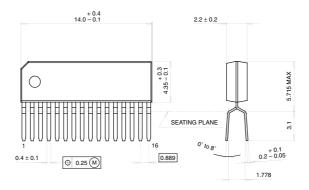
# UHF Input Impedance



# Package Outline Unit: mm

CXA1355L

### 16PIN SZIP (PLASTIC)



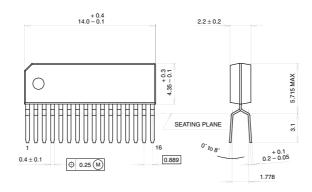


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#### PACKAGE STRUCTURE

PACKAGE MATERIAL	EPOXY RESIN
LEAD TREATMENT	SOLDER PLATING
LEAD MATERIAL	COPPER ALLOY
PACKAGE MASS	0.3g

### 16PIN SZIP (PLASTIC)





### PACKAGE STRUCTURE

SONY CODE	SZIP-16P-01
EIAJ CODE	SZIP016-P-0225
JEDEC CODE	

PACKAGE MATERIAL	EPOXY RESIN
LEAD TREATMENT	SOLDER PLATING
LEAD MATERIAL	COPPER ALLOY
PACKAGE MASS	0.3g

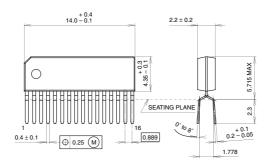
### LEAD PLATING SPECIFICATIONS

ITEM	SPEC.
LEAD MATERIAL	COPPER ALLOY
SOLDER COMPOSITION	Sn-Bi Bi:1-4wt%
PLATING THICKNESS	5-18um

# Package Outline Unit: mm

# CXA1355L-S

### 16PIN SZIP (PLASTIC)



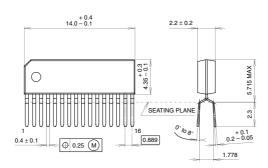


### PACKAGE STRUCTURE

SONY CODE	SZIP-16P-02
EIAJ CODE	SZIP016-P-0225
JEDEC CODE	

PACKAGE MATERIAL	EPOXY RESIN
LEAD TREATMENT	SOLDER PLATING
LEAD MATERIAL	COPPER ALLOY
PACKAGE MASS	0.3g

### 16PIN SZIP (PLASTIC)





### PACKAGE STRUCTURE

SONY CODE	SZIP-16P-02
EIAJ CODE	SZIP016-P-0225
JEDEC CODE	

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LEAD MATERIAL	COPPER ALLOY
PACKAGE MASS	0.3g

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ITEM	SPEC.
LEAD MATERIAL	COPPER ALLOY
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PLATING THICKNESS	5-18μm