

CMOS ST-BUS™ FAMILY **MT8930B**

Subscriber Network Interface Circuit

ISSUE 2

Preliminary Information

August 1993

Features

- CCITT I.430 and ANSI T1.605 S/T interface
- Full-duplex 2B+D, 192 kbit/s transmission
- Link activation/deactivation
- D-channel access contention resolution
- Point-to-point, point-to-multipoint and star configurations
- Master (NT)/Slave (TE) modes of operation
- Exceeds loop length requirements
- Complete loopback testing capabilities
- On chip HDLC D-channel protocoller
- 8 bit Motorola/Intel microprocessor interface
- Controllerless or microprocessor-controlled operation

factory

- Mitel ST-BUS interface
- Low power CMOS technolog
- Single 5 volt power supply

Applications

- ISDN NT1
- ISDN S or T interface
- ISDN Terminal Adaptor (TA)
- Digital sets (TE1) -4 wire ISDN interface
- Digital PABXs, Digital Line Cards (NT2)

Ordering Information MT8930BC 28 Pin Ceramic DIP MT8930BE 28 Pin Plastic DIP MT8930BP 44 Pin PLCC -40°C to +85°C

Description

The MT8930B Subscriber Network Interface Circuit (SNIC) implements the CCITT I.430 and ANSI T1.605 Recommendations for the ISDN S and T reference points. Providing point-to-point and point-to-multipoint digital transmission, the SNIC may be used at either end of the subscriber line (NT or TE).

An HDLC D-channel protocoller is included and controlled through a Motorola/Intel microprocessor port. A controllerless mode allows the SNIC to operate without a microprocessor.

The MT8930B is fabricated in Mitel's CMOS process.

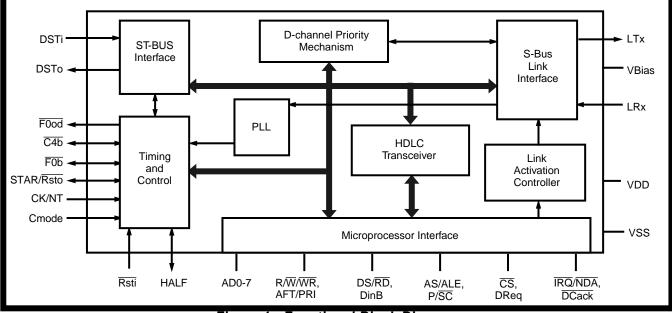


Figure 1 - Functional Block Diagram

Notes:



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