

# JW050M, JW075M, JW100M, JW150M Power Modules: dc-dc Converters; 36 to 75 Vdc Input, 1.5 Vdc Output; 15 W to 45 W



The JW-Series Power Modules use advanced, surface-mount technology and deliver high-quality, efficient, and compact dc-dc conversion.

## **Applications**

- n Distributed power architectures
- Workstations
- n Computer equipment
- n Communications equipment

## **Options**

- n Heat sinks available for extended operation
- n Choice of remote on/off logic configuration

#### **Features**

- Small size: 61.0 mm x 57.9 mm x 12.7 mm (2.40 in. x 2.28 in. x 0.50 in.)
- n High power density
- <sub>n</sub> High efficiency: 67% typical
- n Low output noise
- n Constant frequency
- n Industry-standard pinout
- n Metal baseplate
- <sub>n</sub> 2:1 input voltage range
- o Overtemperature protection (30 W and 45 W only)
- n Overcurrent and overvoltage protection
- n Remote sense
- n Remote on/off
- n Adjustable output voltage
- n Case ground pin
- <sub>n</sub> /SO\* 9001 Certified manufacturing facilities
- UL<sup>†</sup>1950 Recognized, CSA<sup>‡</sup> C22.2 No. 950-95
   Certified, and VDE 0805 (EN60950, IEC950)
   Licensed
- n CE mark meets 73/23/EEC and 93/68/EEC directives§
- \* ISO is a registered trademark of the International Organization for Standardization.
- † *UL* is a registered trademark of Underwriters Laboratories, Inc.
- $\mathop{\sharp} \textit{CSA}$  is a registered trademark of Canadian Standards Assn.
- § This product is intended for integration into end-use equipment. All the required procedures for CE marking of end-use equipment should be followed. (The CE mark is placed on selected products.)

## **Description**

The JW050M, JW075M, JW100M, and JW150M Power Modules are dc-dc converters that operate over an input voltage range of 36 Vdc to 75 Vdc and provide a precisely regulated dc output. The outputs are fully isolated from the inputs, allowing versatile polarity configurations and grounding connections. The modules have maximum power ratings from 15 W to 45 W at a typical full-load efficiency of 67%.

The sealed modules offer a metal baseplate for excellent thermal performance. Threaded-through holes are provided to allow easy mounting or addition of a heat sink for high-temperature applications. The standard feature set includes remote sensing, output trim, and remote on/off for convenient flexibility in distributed power applications.

## **Absolute Maximum Ratings**

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage:				
Continuous:				
JW050M, JW075M	Vı	_	75	Vdc
JW100M, JW150M	Vı	_	80	Vdc
Transient (100 ms; JW100M, JW150M only)	VI, trans	_	100	V
I/O Isolation Voltage (for 1 minute)	_	_	1500	Vdc
Operating Case Temperature (See Thermal Considerations section.)	Tc	-40	100	°C
Storage Temperature	Tstg	<b>–</b> 55	125	°C

## **Electrical Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions.

**Table 1. Input Specifications** 

Parameter	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	Vı	36	48	75	Vdc
Maximum Input Current (VI = 0 V to 75 V; Io = Io, max):					
JW050M	II, max		—	0.9	Α
JW075M	II, max	_	_	1.3	Α
JW100M (See Figure 1.)	II, max	_	_	1.7	Α
JW150M	II, max	_	<del>-</del>	2.6	Α
Maximum Input Current (VI = 36 V to 75 V; Io = Io, max):					
JW050M	II, max	_	_	0.7	Α
JW075M	II, max	_	_	1.0	Α
JW100M (See Figure 1.)	II, max	_	_	1.3	Α
JW150M	II, max	_	_	1.9	Α
Inrush Transient	i <sup>2</sup> t	_	_	1.0	A <sup>2</sup> s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 µH source impedance; see Figure 5.)	lı .	_	5	_	mAp-p
Input Ripple Rejection (120 Hz)	_		60	_	dB

## **Fusing Considerations**

#### CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow fuse with a maximum rating of 20 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.

## **Electrical Specifications** (continued)

**Table 2. Output Specifications** 

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Voltage Set Point (VI = 48 V; Io = Io, max; Tc = 25 °C)	All	Vo, set	1.47	1.5	1.53	Vdc
Output Voltage (Over all operating input voltage, resistive load, and temperature conditions until end of life. See Figure 7.)	All	Vo	1.45	_	1.55	Vdc
Output Regulation: Line (VI = 36 V to 75 V) Load (Io = Io, min to Io, max) Temperature (Tc = -40 °C to +100 °C)	All All All	_ _ _	_ _ _	0.01 0.05 10	0.1 0.2 30	%Vo %Vo mV
Output Ripple and Noise Voltage (See Figure 6.): RMS Peak-to-peak (5 Hz to 20 MHz)	All All	_ _	_ _	_	35 100	mVrms mVp-p
External Load Capacitance (electrolytic)	All	_	0	_	*	μF
Output Current (At Io < Io, min, the module may exceed output ripple specifications.)	JW050M JW075M JW100M JW150M	lo lo lo lo	0.5 0.5 0.5 0.5	_ _ _ _	10 15 20 30	A A A
Output Current-limit Inception (Vo = 90% of Vo, nom)	JW050M JW075M JW100M JW150M	IO, cli IO, cli IO, cli IO, cli	_ _ _	11.5 19.3 23.0 34.5	15 <sup>†</sup> 19.5 <sup>†</sup> 26 <sup>†</sup> 39 <sup>†</sup>	A A A
Output Short-circuit Current (Vo = 250 mV)	All	_	_	170	_	%IO, max
Efficiency (VI = 48 V; Io = Io, max; Tc = 70 °C)	JW050M JW075M JW100M JW150M	η η η	_ _ _ _	68 66 66 65	_ _ _ _	% % %
Switching Frequency	All	_	_	500	_	kHz
Dynamic Response (ýlo/ýt = 1 A/10 μs, V <sub>I</sub> = 48 V, T <sub>C</sub> = 25 °C; tested with a 10 μF aluminum and a 1.0 μF ceramic capacitor across the load): Load Change from I <sub>O</sub> = 50% to 75% of I <sub>O</sub> , max: Peak Deviation Settling Time (V <sub>O</sub> < 10% of peak deviation)	All All	_	_	100 300	_	mV μs
Load Change from Io = 50% to 25% of Io, max: Peak Deviation Settling Time (Vo < 10% of peak deviation)	All All	_ _ _	_ _ _	100 300	_ _ _	μs mV μs

<sup>\*</sup> Consult your sales representative or the factory. † These are manufacturing test limits. In some situations, results may differ.

## **Electrical Specifications** (continued)

**Table 3. Isolation Specifications** 

Parameter	Min	Тур	Max	Unit
Isolation Capacitance	_	2500	_	pF
Isolation Resistance	10	_	_	M3/4

## **General Specifications**

Parameter	Min	Тур	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C)		2,600,000		hr.
Weight	_	_	100 (3.5)	g (oz.)

## **Feature Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions for additional information.

Parameter	Symbol	Min	Тур	Max	Unit
Remote On/Off Signal Interface					
(V <sub>I</sub> = 0 V to 75 V; open collector or equivalent compatible;					
signal referenced to V <sub>I</sub> (–) terminal; see Figure 8 and					
Feature Descriptions.):					
JWxxxM1 Preferred Logic:					
Logic Low—Module On					
Logic High—Module Off					
JWxxxM Optional Logic:					
Logic Low—Module Off					
Logic High—Module On					
Logic Low:					
At Ion/off = 1.0 mA	Von/off	0	_	1.2	V
At $V_{on/off} = 0.0 V$	Ion/off	_	_	1.0	mA
Logic High:					
At $I_{on/off} = 0.0 \mu A$	Von/off	_	_	15	V
Leakage Current	Ion/off	_	_	50	μA
Turn-on Time		_	20	35	ms
(Io = 80% of Io, max; Vo within $\pm$ 1% of steady state)					
Output Voltage Adjustment (See Feature Descriptions.):					
Output Voltage Remote-sense Range		_	_	0.5	V
Output Voltage Set-point Adjustment Range (trim)	_	67	_	115	%Vo, nom
Output Overvoltage Protection	Vo, clamp	1.8*	_	2.3*	V
Overtemperature Protection	Tc	_	105	_	°C
(66 W and 99 W only; see Feature Descriptions.)					

<sup>\*</sup> These are manufacturing test limits. In some situations, results may differ.

## Solder, Cleaning, and Drying Considerations

Post solder cleaning is usually the final circuit-board assembly process prior to electrical testing. The result of inadequate circuit-board cleaning and drying can affect both the reliability of a power module and the testability of the finished circuit-board assembly. For guidance on appropriate soldering, cleaning, and drying procedures, refer to the *Board-Mounted Power Modules Soldering and Cleaning* Application Note (AP97-021EPS).

#### **Characteristic Curves**

The following figures provide typical characteristics for the power modules. The figures are identical for both on/off configurations.

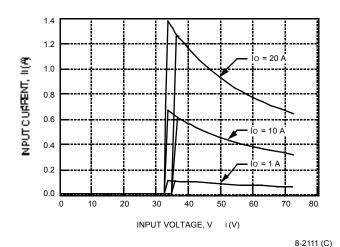


Figure 1. Typical JW100M Input Characteristics at Room Temperature

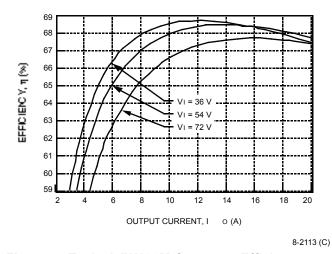


Figure 3. Typical JW100M Converter Efficiency vs.
Output Current at Room Temperature

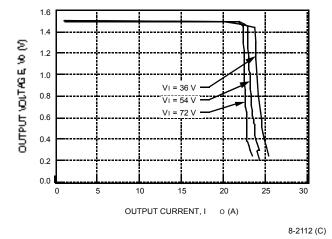
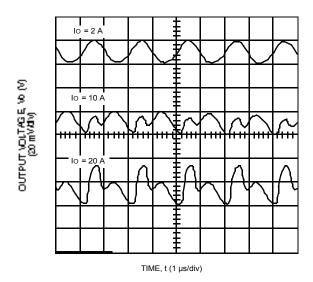


Figure 2. Typical JW100M Output Characteristics at Room Temperature

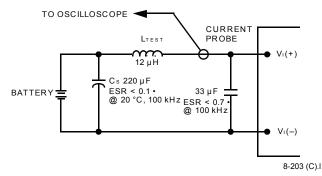


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Note: See Figure 9 for test conditions.

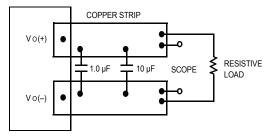
Figure 4. Typical JW100M Output Ripple Voltage at Room Temperature, 48 V Input

## **Test Configurations**



Note: Measure input reflected-ripple current with a simulated source inductance (LTEST) of 12 µH. Capacitor Cs offsets possible battery impedance. Measure current as shown above.

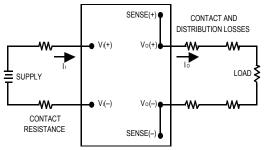
Figure 5. Input Reflected-Ripple Test Setup



8-513 (C).d

Note: Use a 1.0 µF ceramic capacitor and a 10 µF aluminum or tantalum capacitor. Scope measurement should be made using a BNC socket. Position the load between 51 mm and 76 mm (2 in. and 3 in.) from the module.

Figure 6. Peak-to-Peak Output Noise Measurement Test Setup



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Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance.

$$\eta = \left(\frac{[Vo(+) - Vo(-)]Io}{[VI(+) - VI(-)]II}\right) \times 100$$
 %

Figure 7. Output Voltage and Efficiency Measurement Test Setup

#### **Design Considerations**

## Input Source Impedance

The power module should be connected to a low ac-impedance input source. Highly inductive source impedances can affect the stability of the power module. For the test configuration in Figure 5, a 33  $\mu\text{F}$  electrolytic capacitor (ESR < 0.7  $^3\!\!/_4$  at 100 kHz) mounted close to the power module helps ensure stability of the unit. For other highly inductive source impedances, consult the factory for further application guidelines.

## **Safety Considerations**

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL*1950, *CSA* C22.2 No. 950-95, and VDE 0805 (EN60950, IEC950).

If the input source is non-SELV (ELV or a hazardous voltage greater than 60 Vdc and less than or equal to 75 Vdc), for the module's output to be considered meeting the requirements of safety extra-low voltage (SELV), all of the following must be true:

- The input source is to be provided with reinforced insulation from any hazardous voltages, including the ac mains.
- One V<sub>I</sub> pin and one V<sub>O</sub> pin are to be grounded or both the input and output pins are to be kept floating.
- The input pins of the module are not operator accessible.
- Another SELV reliability test is conducted on the whole system, as required by the safety agencies, on the combination of supply source and the subject module to verify that under a single fault, hazardous voltages do not appear at the module's output.

**Note:** Do not ground either of the input pins of the module without grounding one of the output pins. This may allow a non-SELV voltage to appear between the output pin and ground.

The power module has extra-low voltage (ELV) outputs when all inputs are ELV.

The input to these units is to be provided with a maximum 20 A normal-blow fuse in the ungrounded lead.

## **Feature Descriptions**

#### **Overcurrent Protection**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

#### Remote On/Off

Two remote on/off options are available. Positive logic remote on/off turns the module on during a logic-high voltage on the ON/OFF pin, and off during a logic low. Negative logic remote on/off turns the module off during a logic high and on during a logic low. Negative logic (code suffix "1") is the factory-preferred configuration.

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the V<sub>I</sub>(–) terminal (V<sub>on/off</sub>). The switch can be an open collector or equivalent (see Figure 8). A logic low is V<sub>on/off</sub> = 0 V to 1.2 V. The maximum l<sub>on/off</sub> during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum  $V_{on/off}$  generated by the power module is 15 V. The maximum allowable leakage current of the switch at  $V_{on/off}$  = 15 V is 50  $\mu$ A.

If not using the remote on/off feature, do one of the following:

- For negative logic, short ON/OFF pin to V<sub>I</sub>(–).
- n For positive logic, leave ON/OFF pin open.

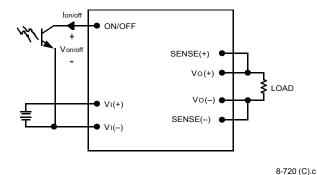


Figure 8. Remote On/Off Implementation Lineage Power

#### Remote Sense

Remote sense minimizes the effects of distribution losses by regulating the voltage at the remote-sense connections. The voltage between the remote-sense pins and the output terminals must not exceed the output voltage sense range given in the Feature Specifications table, i.e.:

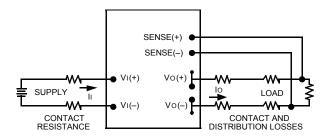
$$[Vo(+) - Vo(-)] - [SENSE(+) - SENSE(-)] \delta 0.5 V$$

The voltage between the Vo(+) and Vo(-) terminals must not exceed 3.8 V. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim). See Figure 9.

If not using the remote-sense feature to regulate the output at the point of load, then connect SENSE(+) to Vo(+) and SENSE(-) to Vo(-) at the module.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.



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Figure 9. Effective Circuit Configuration for Single-Module Remote-Sense Operation

#### Feature Descriptions (continued)

## Output Voltage Set-Point Adjustment (Trim)

Output voltage trim allows the user to increase or decrease the output voltage set point of a module. This is accomplished by connecting an external resistor between the TRIM pin and either the SENSE(+) or SENSE(-) pins. The trim resistors should be positioned close to the module.

If not using the trim feature, leave the TRIM pin open.

With an external resistor between the TRIM and SENSE(–) pins (Radj-down), the output voltage set point (Vo, adj) decreases (see Figure 9). The following equation determines the required external-resistor value to obtain a percentage output voltage change of ý%.

Radj-down = 
$$\left(\frac{100}{\Lambda\%} - 2\right)$$
 k $\Omega$ 

The test results for this configuration are displayed in Figure 11. This figure applies to all output voltages.

With an external resistor connected between the TRIM and SENSE(+) pins (Radj-up), the output voltage set point (Vo, adj) increases (see Figure 12).

The following equation determines the required external-resistor value to obtain a percentage output voltage change of ý%.

$$R_{\text{adj-up}} = \left(\frac{V_O(100 + \Delta\%)}{1.225\Delta\%} - \frac{(100 + 2\Delta\%)}{\Delta\%}\right) \text{ k}\Omega$$

The test results for this configuration are displayed in Figure 11.

The voltage between the Vo(+) and Vo(-) terminals must not exceed 1.8 V. This limit includes any increase in voltage due to remote-sense compensation and output voltage set-point adjustment (trim). See Figure 9.

Although the output voltage can be increased by both the remote sense and by the trim, the maximum increase for the output voltage is not the sum of both. The maximum increase is the larger of either the remote sense or the trim. Consult the factory if you need to increase the output voltage more than the above limitation.

The amount of power delivered by the module is defined as the voltage at the output terminals multiplied by the output current. When using remote sense and trim, the output voltage of the module can be increased, which at the same output current would increase the power output of the module. Care should be taken to ensure that the maximum output power of the module remains at or below the maximum rated power.

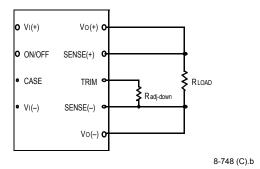


Figure 10. Circuit Configuration to Decrease Output Voltage

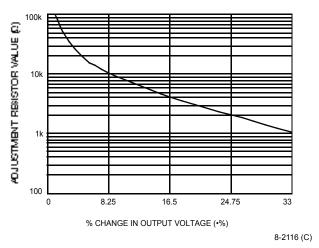


Figure 11. Resistor Selection for Decreased
Output Voltage

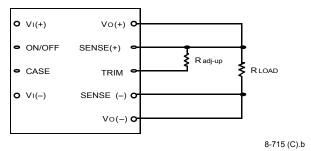


Figure 12. Circuit Configuration to Increase Output Voltage

## Feature Descriptions (continued)

## Output Voltage Set-Point Adjustment (Trim) (continued)

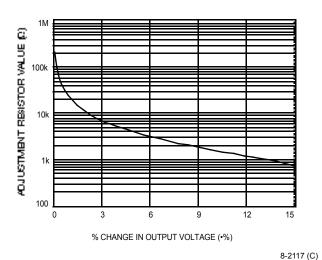


Figure 13. Resistor Selection for Increased Output Voltage

## **Output Overvoltage Protection**

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage control that reduces the risk of output overvoltage.

## **Overtemperature Protection**

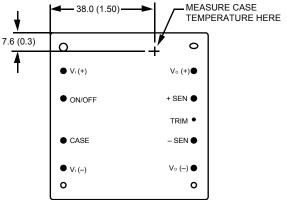
The 30 W and 45 W modules feature an overtemperature protection circuit to safeguard against thermal damage.

The circuit shuts down the module when the maximum case temperature is exceeded. The module restarts automatically after cooling.

#### Thermal Considerations

#### Introduction

The power modules operate in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the unit are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment. Proper cooling can be verified by measuring the case temperature. Peak temperature (Tc) occurs at the position indicated in Figure 14.



8-716 (C).f

Note: Top view, pin locations are for reference only.

Measurements shown in millimeters and (inches).

Figure 14. Case Temperature Measurement Location

The temperature at this location should not exceed 100 °C. The output power of the module should not exceed the rated power for the module as listed in the Ordering Information table.

Although the maximum case temperature of the power modules is 100 °C, you can limit this temperature to a lower value for extremely high reliability.

For additional information on these modules, refer to the *Thermal Management JC-, JFC-, JW-, and JFW-Series 50 W to 150 W Board-Mounted Power Modules* Technical Note (TN97-008EPS).

## Thermal Considerations (continued)

#### **Heat Transfer Without Heat Sinks**

Increasing airflow over the module enhances the heat transfer via convection. Figure 15 shows the maximum power that can be dissipated by the module without exceeding the maximum case temperature versus local ambient temperature (TA) for natural convection through 4 m/s (800 ft./min.).

Note that the natural convection condition was measured at 0.05 m/s to 0.1 m/s (10 ft./min. to 20 ft./min.); however, systems in which these power modules may be used typically generate natural convection airflow rates of 0.3 m/s (60 ft./min.) due to other heat dissipating components in the system. The use of Figure 15 is shown in the following example.

#### Example

What is the minimum airflow necessary for a JW100M operating at V<sub>I</sub> = 54 V, an output current of 20 A, and a maximum ambient temperature of 40 °C?

#### Solution

Given:  $V_1 = 54 \text{ V}$  $I_0 = 20 \text{ A}$ 

T<sub>A</sub> = 40 °C

Determine PD (Use Figure 16.):

Pp = 14.3 W

Determine airflow (v) (Use Figure 15.):

v = 1.5 m/s (300 ft./min.)

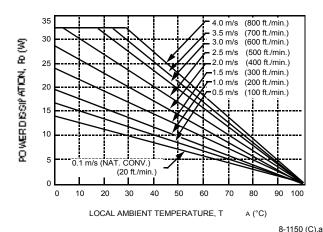


Figure 15. Forced Convection Power Derating with No Heat Sink; Either Orientation

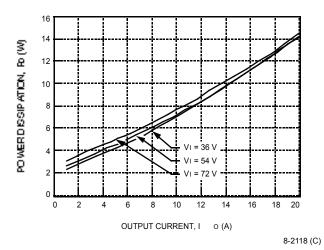


Figure 16. JW100M Power Dissipation vs.
Output Current at 25 °C

## **Heat Transfer with Heat Sinks**

The power modules have through-threaded, M3 x 0.5 mounting holes, which enable heat sinks or cold plates to attach to the module. The mounting torque must not exceed 0.56 N-m (5 in.-lb.). For a screw attachment from the pin side, the recommended hole size on the customer's PWB around the mounting holes is 0.130  $\pm$  0.005 inches. If a larger hole is used, the mounting torque from the pin side must not exceed 0.25 N-m (2.2 in.-lb.).

Thermal derating with heat sinks is expressed by using the overall thermal resistance of the module. Total module thermal resistance ( $\theta$ ca) is defined as the maximum case temperature rise ( $\Delta$ Tc, max) divided by the module power dissipation (Pp):

$$\theta \text{ca} = \left[\frac{\Delta T\text{c,max}}{\text{P}_{D}}\right] = \left[\frac{\left(T\text{c} - T\text{A}\right)}{\text{P}_{D}}\right]$$

The location to measure case temperature (Tc) is shown in Figure 14. Case-to-ambient thermal resistance vs. airflow is shown, for various heat sink configurations and heights, in Figure 17. These curves were obtained by experimental testing of heat sinks, which are offered in the product catalog.

#### Thermal Considerations (continued)

#### Heat Transfer with Heat Sinks (continued)

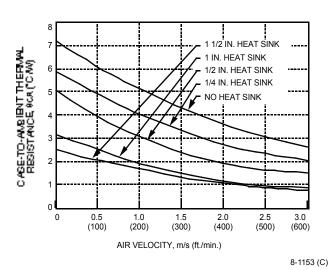


Figure 17. Case-to-Ambient Thermal Resistance Curves; Either Orientation

These measured resistances are from heat transfer from the sides and bottom of the module as well as the top side with the attached heat sink; therefore, the case-to-ambient thermal resistances shown are generally lower than the resistance of the heat sink by itself. The module used to collect the data in Figure 17 had a thermal-conductive dry pad between the case and the heat sink to minimize contact resistance. The use of Figure 17 is shown in the following example.

#### Example

If an 85 °C case temperature is desired, what is the minimum airflow necessary? Assume the JW100M module is operating at  $V_{\rm I}$  = 54 V and an output current of 20 A, maximum ambient air temperature of 40 °C, and the heat sink is 1/2 inch.

#### Solution

Given: VI = 54 V

lo = 20 A

T<sub>A</sub> = 40 °C

Tc = 85 °C

Heat sink = 1/2 inch

Determine PD by using Figure 16:

$$P_D = 14.3 \text{ W}$$

Then solve the following equation:

$$\theta ca = \left\lceil \frac{(Tc - TA)}{PD} \right\rceil$$

$$\theta ca = \left[ \frac{(85-40)}{14.3} \right]$$

$$\theta$$
ca = 3.1 °C/W

Use Figure 17 to determine air velocity for the 1/2 inch heat sink.

The minimum airflow necessary for the JW100M module is 1.0 m/s (200 ft./min.).

#### **Custom Heat Sinks**

A more detailed model can be used to determine the required thermal resistance of a heat sink to provide necessary cooling. The total module resistance can be separated into a resistance from case-to-sink ( $\theta$ cs) and sink-to-ambient ( $\theta$ sa) shown below (Figure 18).

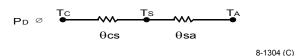


Figure 18. Resistance from Case-to-Sink and Sink-to-Ambient

For a managed interface using thermal grease or foils, a value of  $\theta$ cs = 0.1 °C/W to 0.3 °C/W is typical. The solution for heat sink resistance is:

$$\theta$$
sa =  $\left[\frac{(Tc - TA)}{PD}\right] - \theta cs$ 

This equation assumes that all dissipated power must be shed by the heat sink. Depending on the user-defined application environment, a more accurate model, including heat transfer from the sides and bottom of the module, can be used. This equation provides a conservative estimate for such instances.

Data Sheet April 2008

## **EMC Considerations**

For assistance with designing for EMC compliance, please refer to the FLTR100V10 data sheet (DS99-294EPS).

## **Layout Considerations**

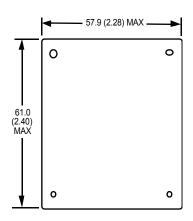
Copper paths must not be routed beneath the power module mounting inserts. For additional layout guidelines, refer to the FLTR100V10 data sheet (DS99-294EPS).

## **Outline Diagram**

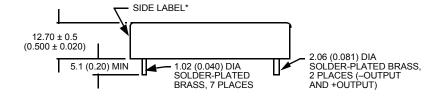
Dimensions are in millimeters and (inches).

Tolerances:  $x.x \text{ mm} \pm 0.5 \text{ mm} (x.xx \text{ in.} \pm 0.02 \text{ in.})$  $x.xx \text{ mm} \pm 0.25 \text{ mm} (x.xxx \text{ in.} \pm 0.010 \text{ in.})$ 

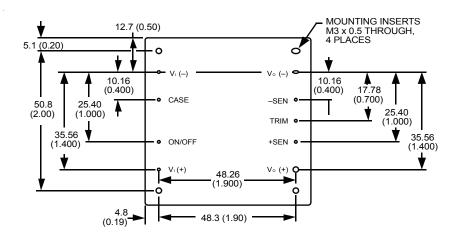
#### **Top View**



#### **Side View**



#### **Bottom View**



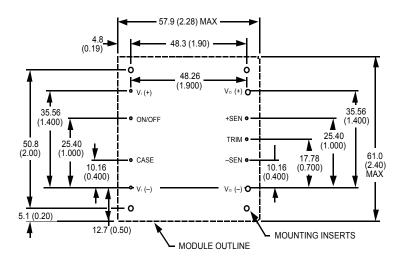
8-1945 (C).a

<sup>\*</sup> Side label includes Lineage name, product designation, safety agency markings, input/output voltage and current ratings, and bar code.

## **Recommended Hole Pattern**

Component-side footprint.

Dimensions are in millimeters and (inches).



8-1945 (C).a

## **Ordering Information**

**Table 4. Device Codes** 

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Comcode
48 V	1.5 V	15 W	Negative	JW050M1	TBD
48 V	1.5 V	22.5 W	Negative	JW075M1	TBD
48 V	1.5 V	30 W	Negative	JW100M1	TBD
48 V	1.5 V	45 W	Negative	JW150M1	TBD
48 V	1.5 V	15 W	Positive	JW050M	TBD
48 V	1.5 V	22.5 W	Positive	JW075M	TBD
48 V	1.5 V	30 W	Positive	JW100M	TBD
48 V	1.5 V	45 W	Positive	JW150M	TBD

## Ordering Information (continued)

**Table 5. Device Accessories** 

Accessory	Comcode
1/4 in. transverse kit (heat sink, thermal pad, and screws)	407243989
1/4 in. longitudinal kit (heat sink, thermal pad, and screws)	407243997
1/2 in. transverse kit (heat sink, thermal pad, and screws)	407244706
1/2 in. longitudinal kit (heat sink, thermal pad, and screws)	407244714
1 in. transverse kit (heat sink, thermal pad, and screws)	407244722
1 in. longitudinal kit (heat sink, thermal pad, and screws)	407244730
1 1/2 in. transverse kit (heat sink, thermal pad, and screws)	407244748
1 1/2 in. longitudinal kit (heat sink, thermal pad, and screws)	407244755

Dimensions are in millimeters and (inches).

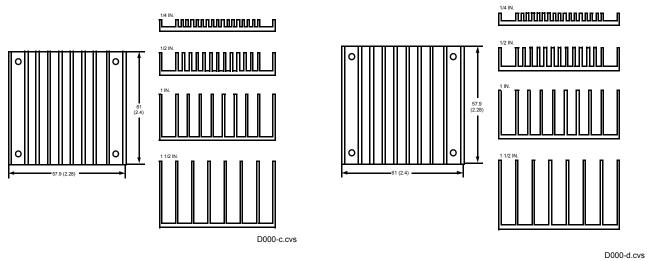


Figure 19. Longitudinal Heat Sink

Figure 20. Transverse Heat Sink



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April 2008 DS98-304EPS