

SEMITOP[®] 2

IGBT Module

SK50GB065

Preliminary Data

Features

- Compact design
- One screw mounting
- Heat transfer and isolation through direct copper bonded aluminium oxide ceramic (DCB)
- N-channel homogeneous silicon structure (NPT-Non-Punch-Through IGBT)
- Low tail current with low
- temperature dependence
- Low treshold voltage

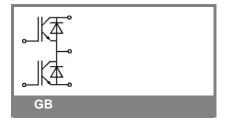
Typical Applications

- Switching (not for linear use)
- Inverter
- Switched mode power supplies

• UPS

Absolute Maximum Ratings T _s = 25 °C, unless otherwise specified					
Symbol	Conditions		Values	Units	
IGBT					
V _{CES}	T _j = 25 °C		600	V	
I _C	T _j = 125 °C	T _s = 25 °C	54	A	
		T _s = 80 °C	40	А	
I _{CRM}	I _{CRM} = 2 x I _{Cnom}		60	А	
V _{GES}			± 20	V	
t _{psc}	$\label{eq:V_CC} \begin{array}{l} V_{CC} \texttt{=} 300 \; V; \; V_{GE} \leq 20 \; V; \\ V_{CES} \texttt{<} 600 \; V \end{array}$	T _j = 125 °C	10	μs	
Inverse D	Diode			•	
I _F	T _j = 150 °C	T _s = 25 °C	64	А	
		T _s = 80 °C	48	А	
I _{FRM}	I _{FRM} = 2 x I _{Fnom}			А	
I _{FSM}	t _p = 10 ms; half sine wave	T _j = 150 °C	200	А	
Module			·		
I _{t(RMS)}				А	
T _{vj}			-40 +150	°C	
T _{stg}			-40 +125	°C	
V _{isol}	AC, 1 min.		2500	V	

Characteristics T _s =			25 °C, unless otherwise specified				
Symbol	Conditions		min.	typ.	max.	Units	
IGBT	_		_			_	
V _{GE(th)}	$V_{GE} = V_{CE}, I_C = 1,4 \text{ mA}$		3	4	5	V	
I _{CES}	V_{GE} = 0 V, V_{CE} = V_{CES}	T _j = 25 °C			0,0044	mA	
I _{GES}	V _{CE} = 0 V, V _{GE} = 20 V	T _j = 25 °C			240	nA	
V _{CE0}		T _j = 25 °C		1,1		V	
		T _j = 125 °C		1,1		V	
r _{CE}	V _{GE} = 15 V	T _i = 25°C		15		mΩ	
		T _j = 125°C		19		mΩ	
V _{CE(sat)}	I _{Cnom} = 60 A, V _{GE} = 15 V	T _j = 25°C _{chiplev.}		2	2,5	V	
		T _j = 125°C _{chiplev.}		2,2		V	
C _{ies}				3,2		nF	
C _{oes}	V_{CE} = 25, V_{GE} = 0 V	f = 1 MHz		0,3		nF	
C _{res}				0,18		nF	
t _{d(on)}				60	80	ns	
t _r	R _{Gon} = 16 Ω	V _{CC} = 300V		30	40	ns	
E _{on}		I _{Cnom} = 40A		1,1	1,4	mJ	
t _{d(off)}	$R_{Goff} = 16 \Omega$	T _i = 125 °C		220	280	ns	
t _f		V _{GE} =±15V		20	26	ns	
E _{off}				0,7	0,9	mJ	
R _{th(j-s)}	per IGBT				0,85	K/W	





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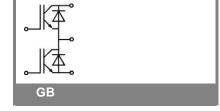
Typical Applications

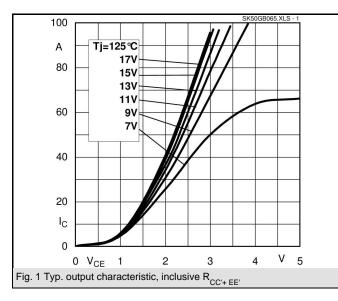
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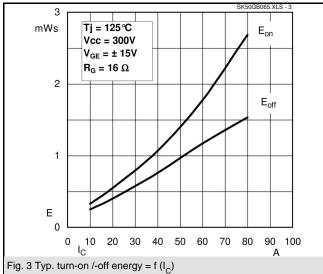
Characteristics									
Symbol	Conditions		min.	typ.	max.	Units			
Inverse Diode									
$V_F = V_{EC}$	I_{Fnom} = 50 A; V_{GE} = 0 V	T _j = 25 °C _{chiplev.}		1,45	1,7	V			
		T _j = 150 °C _{chiplev.}		1,4	1,75	V			
V _{F0}		T _j = 25 °C				V			
		T _j = 125 °C		0,85	0,9	V			
r _F		T _j = 25 °C				mΩ			
		T _j = 125 °C		11	16	mΩ			
I _{RRM}	I _{Fnom} = 50 A	T _i = 125 °C		40		Α			
Q _{rr}	di/dt = -1000 A/µs	,		3,6		μC			
E _{rr}	V _{CC} = 300V			0,55		mJ			
R _{th(j-s)D}	per diode				1,1	K/W			
M _s	to heat sink				2	Nm			
w				19		g			

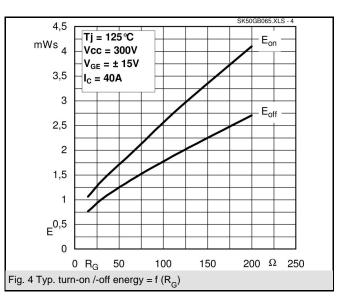
This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

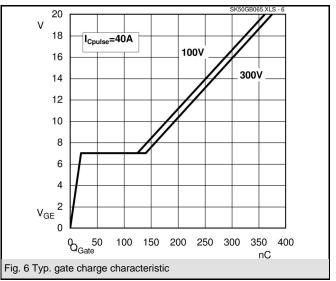
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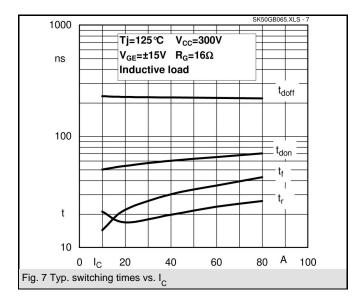


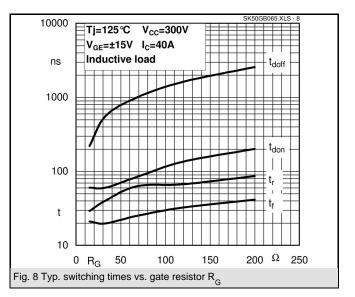


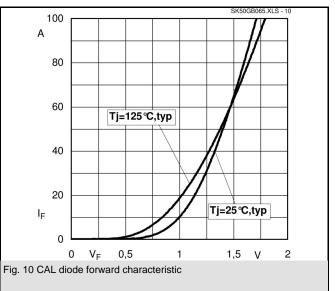
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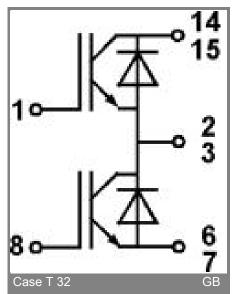




4

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no. E 63 532 UL recognized file •**01**4.5 -678 ()2)66 **\$15** E4E -15.43 ę 3.5 010 MOUNTING HOLE -2.5 28 26.75 24 0 2.5 -**0**12 14 Ŧ φ4'7 θ C 1.25 -5.2 29.75-32.75-36.75-39.25--27.7 10.75 20.25-1225 Case T32 (Suggested hole diameter, in the PCB, for solder pins and plastic mounting pins: 2mm)



5