## Schottky Rectifier, 120 A



| PRODUCT SUMMARY |  |
| :---: | :---: |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | 120 A |
| $\mathrm{~V}_{\mathrm{R}}$ | 15 V |

## FEATURES

- $125{ }^{\circ} \mathrm{C} \mathrm{T}_{\mathrm{J}}$ operation ( $\mathrm{V}_{\mathrm{R}}<5 \mathrm{~V}$ )
- Low forward voltage drop
- High frequency operation


RoHS COMPLIANT

- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free
- Designed and qualified for industrial level


## DESCRIPTION

The 125NQ.. high current Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to $125^{\circ} \mathrm{C}$ junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS

| SYMBOL | CHARACTERISTICS | VALUES | UNITS |
| :--- | :--- | :---: | :---: |
| $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | Rectangular waveform | 120 | A |
| $\mathrm{~V}_{\text {RRM }}$ |  | 15 | V |
| $\mathrm{I}_{\mathrm{FSM}}$ | $\mathrm{t}_{\mathrm{p}}=5 \mu \mathrm{~s}$ sine | 10800 | A |
| $\mathrm{~V}_{\mathrm{F}}$ | $120 \mathrm{Apk}, \mathrm{T}_{J}=125^{\circ} \mathrm{C}$ | 0.37 | V |
| $\mathrm{~T}_{\mathrm{J}}$ | Range | -55 to 125 | ${ }^{\circ} \mathrm{C}$ |


| VOLTAGE RATINGS |  |  |  |
| :--- | :---: | :---: | :---: |
| PARAMETER | SYMBOL | 125NQ015PbF | UNITS |
| Maximum DC reverse voltage | $\mathrm{V}_{\mathrm{R}}$ | 15 | V |
| Maximum working peak reverse voltage | $\mathrm{V}_{\mathrm{RWM}}$ | 25 |  |


| ABSOLUTE MAXIMUM RATINGS |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| Maximum average forward current See fig. 5 | $\mathrm{I}_{\mathrm{F}(\mathrm{AV})}$ | $50 \%$ duty cycle at $\mathrm{T}_{\mathrm{C}}=74{ }^{\circ} \mathrm{C}$, rectangular waveform |  | 120 | A |
| Maximum peak one cycle non-repetitive surge current See fig. 7 | $\mathrm{I}_{\text {FSM }}$ | $5 \mu \mathrm{~s}$ sine or $3 \mu \mathrm{~s}$ rect. pulse 10 ms sine or 6 ms rect. pulse | Following any rated load condition and with rated $\mathrm{V}_{\text {RRM }}$ applied | 10800 1700 |  |
| Non-repetitive avalanche energy | $\mathrm{E}_{\text {AS }}$ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{AS}}=5 \mathrm{~A}, \mathrm{~L}=1 \mathrm{mH}$ |  | 12 | mJ |
| Repetitive avalanche current | $\mathrm{I}_{\text {AR }}$ | Current decaying linearly to zero in $1 \mu \mathrm{~s}$ Frequency limited by $\mathrm{T}_{\mathrm{J}}$ maximum $\mathrm{V}_{\mathrm{A}}=1.5 \times \mathrm{V}_{\mathrm{R}}$ typical |  | 2 | A |

## ELECTRICAL SPECIFICATIONS

| PARAMETER | SYMBOL | TEST CONDITIONS |  | VALUES | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum forward voltage drop per leg See fig. 1 | $\mathrm{V}_{\mathrm{FM}}{ }^{(1)}$ | 120 A | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | 0.43 | V |
|  |  | 240 A |  | 0.58 |  |
|  |  | 120 A | $\mathrm{T}_{\mathrm{J}}=75^{\circ} \mathrm{C}$ | 0.37 |  |
|  |  | 240 A |  | 0.52 |  |
| Maximum reverse leakage current per leg See fig. 2 | $\mathrm{I}_{\mathrm{RM}}{ }^{(1)}$ | $\mathrm{T}_{J}=25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\mathrm{R}}=$ Rated $\mathrm{V}_{\mathrm{R}}$ | 40 | mA |
|  |  | $\mathrm{T}_{\mathrm{J}}=10{ }^{\circ} \mathrm{C}$ |  | 2000 |  |
| Maximum junction capacitance | $\mathrm{C}_{\text {T }}$ | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}_{\mathrm{DC}}$ (test signal range 100 kHz to 1 MHz ) $25^{\circ} \mathrm{C}$ |  | 7700 | pF |
| Typical series inductance | $\mathrm{L}_{\text {s }}$ | From top of terminal hole to mounting plane |  | 7.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated $\mathrm{V}_{\mathrm{R}}$ |  | 10000 | V/ $/$ s |

## Note

(1) Pulse width $<300 \mu$ s, duty cycle $<2 \%$

| PARAMETER | SYMBOL | TEST CONDITIONS | VALUES | UNITS |
| :---: | :---: | :---: | :---: | :---: |
| Maximum junction temperature range | $\mathrm{T}_{J}$ |  | - 55 to 125 | ${ }^{\circ} \mathrm{C}$ |
| Maximum storage temperature range | $\mathrm{T}_{\text {Stg }}$ |  | - 55 to 150 |  |
| Maximum thermal resistance, junction to case | $\mathrm{R}_{\text {thJc }}$ | DC operation See fig. 4 | 0.38 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Typical thermal resistance, case to heatsink | $\mathrm{R}_{\text {thCs }}$ | Mounting surface, smooth and greased | 0.05 |  |
| Approximate weight |  |  | 30 | g |
|  |  |  | 1.06 | oz. |
| Mounting torque minimum |  | Non-lubricated threads | 3 (26.5) | $\begin{gathered} \mathrm{N} \cdot \mathrm{~m} \\ (\mathrm{lbf} \cdot \mathrm{in}) \end{gathered}$ |
| maximum |  |  | 4 (35.4) |  |
| Terminal torque minimum <br>  maximum <br> Case style  |  |  | 3.4 (30) |  |
|  |  |  | 5 (44.2) |  |
|  |  |  | HALF-PAK module |  |



Fig. 1 - Maximum Forward Voltage Drop Characteristics


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance $Z_{\text {thJc }}$ Characteristics


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current


Fig. 6 - Forward Power Loss Characteristics


Fig. 7 - Maximum Non-Repetitive Surge Current


Fig. 8 - Unclamped Inductive Test Circuit

## Note

(1) Formula used: $T_{C}=T_{J}-\left(P d+P d_{R E V}\right) \times R_{t h J C}$;
$\mathrm{Pd}=$ Forward power loss $=\mathrm{I}_{\mathrm{F}(\mathrm{AV})} \times \mathrm{V}_{\mathrm{FM}}$ at $\left(\mathrm{I}_{\mathrm{F}(\mathrm{AV})} / \mathrm{D}\right)$ (see fig. 6);
$P d_{\text {REV }}=$ Inverse power loss $=V_{R 1} \times I_{R}(1-D) ; I_{R}$ at $V_{R 1}=$ Rated $V_{R}$

## ORDERING INFORMATION TABLE



| 1 | - | Average current rating $(\times 10)$ |
| :--- | :--- | :--- |
| 2 | - | Product silicon identification |
| 3 | $-\quad N=$ Not isolated |  |
| 4 | $-\quad Q=$ Schottky rectifier diode |  |
| 5 | $-\quad$ Voltage rating $(015=15 \mathrm{~V})$ |  |
| 6 | $-\quad$ Lead $(\mathrm{Pb})$-free |  |


| LINKS TO RELATED DOCUMENTS |  |
| :--- | :---: |
| Dimensions | http://www.vishay.com/doc?95020 |

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