

DATA SHEET

SA5752

Audio processor – companding, VOX and amplifier section

Product Specification
Replaces data of December 6, 1993
IC17 Data Handbook

1997 Nov 07

Philips Semiconductors



PHILIPS

Audio processor – companding, VOX and amplifier section

SA5752

DESCRIPTION

The SA5752 is a high performance low power audio signal processing system especially designed to meet the requirements for small size and low voltage operation of hand-held equipment. The SA5752 subsystem includes a low noise microphone preamplifier with adjustable gain, a noise cancellation switching amplifier with adjustable threshold, a voice operated transmitter (VOX) switch, VOX control, an audio compressor with buffered input, audio expander, and an internal bandgap voltage regulator with power down capability. When used with Philips Semiconductors' SA5753, the complete audio processing function of an AMPS or TACS cellular telephone is easily implemented. The system also meets the requirements of the proposed NAMPS or NTACS specifications. The SA5752 can also be used without the SA5753 in a wide variety of radio communications applications.

FEATURES

- Operating voltage range: 2V to 5.5V
- Miniature SSOP
- High performance
- Adjustable VOX and noise cancellation threshold
- Adjustable gain preamplifier
- Audio companding
- ESD protected
- Open collector VOX output
- Logic inputs CMOS compatible
- Power down mode
- Few external components
- Meets AMPS/TACS/NAMPS/NTACS requirements

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
20-Pin Plastic Shrink Small Outline Package (SSOP)	-40 to +85°C	SA5752DK	SOT266-1

PIN CONFIGURATION

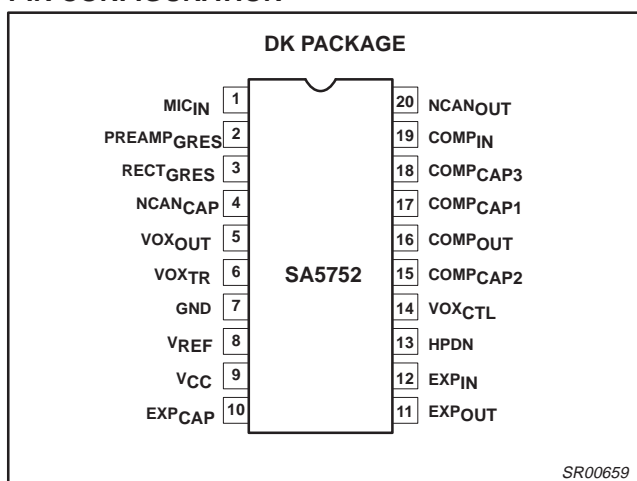


Figure 1. Pin Configuration

BENEFITS

- Very compact applications
- Long battery life in portable equipment
- Complete cellular audio function with the SA5753

APPLICATIONS

- Cellular radio
- Mobile communications
- High performance cordless telephones
- 2-way radio

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PIN DESCRIPTIONS

PIN NO.	SYMBOL	DESCRIPTION
1	MIC _{IN}	Microphone input
2	PREAMP _{GRES}	Preamplifier gain resistor
3	RECT _{GRES}	Rectifier gain resistor
4	NCAN _{CAP}	Noise cancellation timing capacitor
5	VOX _{OUT}	Voice operated transmission output
6	VOX _{TR}	Voice operated transmission threshold resistor
7	GND	Ground
8	V _{REF}	Reference voltage
9	V _{CC}	Positive supply
10	EXP _{CAP}	Expander timing capacitor
11	EXP _{OUT}	Expander output
12	EXP _{IN}	Expander input
13	HPDN	Hardware power-down
14	VOX _{CTL}	Voice operated transmission control
15	COMP _{CAP2}	Compressor capacitor 2 DC block
16	COMP _{OUT}	Compressor output
17	COMP _{CAP1}	Compressor timing capacitor 1
18	COMP _{CAP3}	Compressor capacitor 3 DC block
19	COMP _{IN}	Compressor input
20	NCAN _{OUT}	Noise cancellation output

BLOCK DIAGRAM

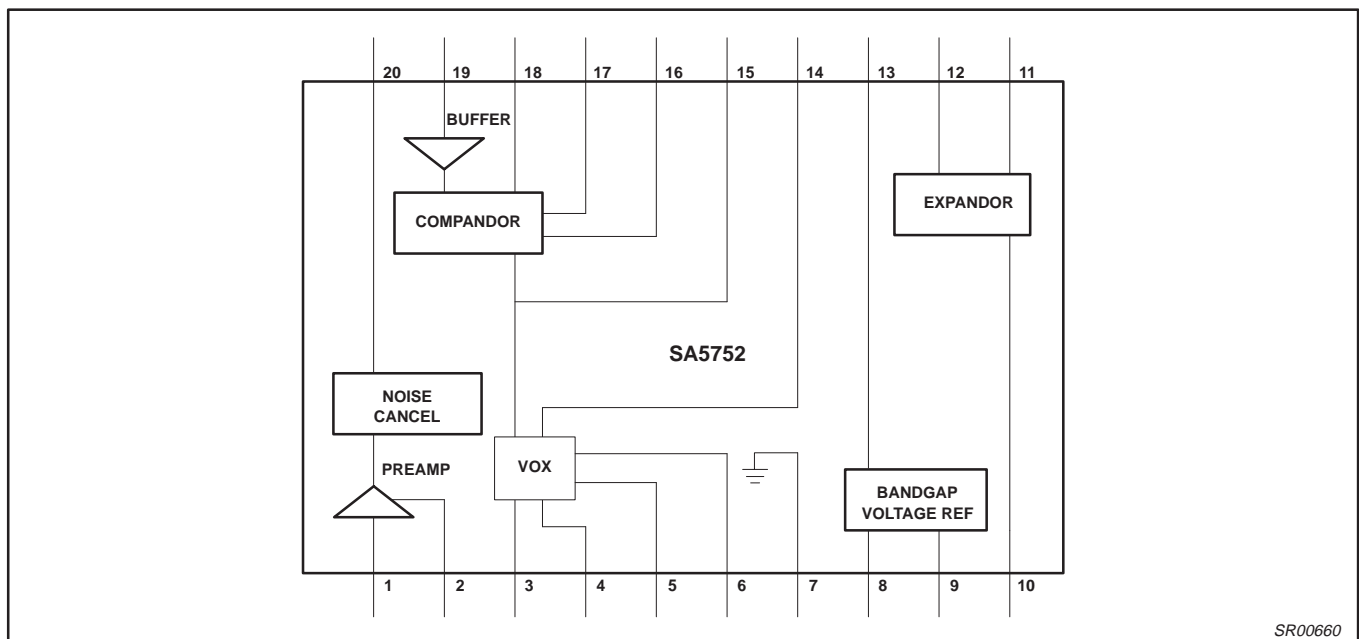


Figure 2. Block Diagram

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ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNIT
V_{CC}	Power supply voltage range	-0.3 to 6	V
V_{IN}	Voltage applied to any other pin	-0.3 to ($V_{CC}+0.3$)	V
T_{STG}	Storage temperature	-65 to +150	°C
T_A	Ambient operating temperature	-40 to +85	°C

DC ELECTRICAL CHARACTERISTICS

 $T_A = 25^\circ\text{C}$, $V_{CC} = +3.0\text{V}$, $0\text{dB} = 77.5\text{mV}_{\text{RMS}}$. See test circuit, Figure 6.

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
V_{CC}	Supply voltage		2.7 ⁴	3.0	5.5	V
I_{CC}	Supply current	No signal Power down mode		3.1 125	4.0	mA μA
Z_L	Load impedance pins NCAN_{OUT} , EXP_{OUT}		50			$\text{k}\Omega$
	$\text{COMP}_{\text{OUT}}^1$		10			$\text{k}\Omega$
Z_{IN}	Input impedance COMP_{IN} , MIC_{IN}		40	50	60	$\text{k}\Omega$
	EXP_{IN}^2		2.0			$\text{k}\Omega$
	Noise cancellation current	Pin 6		25		μA
V_{OS}	DC offset $\text{NCAN}_{\text{OUT}}^3$		-50	-3.0	50	mV

NOTES:

- Compressor is tested in production with $50\text{k}\Omega$ load.
- Not tested in production.
- Offset values are identical for both gain states of noise reduction circuit.
- Operational down to $V_{CC} = 2\text{V}$.

AC ELECTRICAL CHARACTERISTICS

 $T_A = 25^\circ\text{C}$, $V_{CC} = +3.0\text{V}$, $0\text{dB level} = 77.5\text{mV}_{\text{RMS}}$. See test circuit, Figure 6.

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
	Preamplicator gain range Preamplicator voltage gain 0dB Preamplicator voltage gain 40dB	Pin 2 open Pin 2 AC ground	0 -1.0 39.0	0 40	40 1.0 41.0	dB dB dB
	Preamplicator noise density	Pin 2 AC grounded RS = $50\text{k}\Omega$ unweighted 20Hz-20kHz		7		$\text{nV}/\sqrt{\text{Hz}}$
		weighted CCIR DIN45405 20-20kHz		8		$\text{nV}/\sqrt{\text{Hz}}$
	Switch amplifier gain		9	10	11	dB
Compondor 1kHz, all tests¹						
COMP_{OUT}	Compressor error at -21dB output level	Input level = -42dB	-1.0	-0.16	1.0	dB
COMP_{OUT}	Compressor error at -10dB output level	Input level = -20dB	-1.0	-0.11	1.0	dB
COMP_{OUT}	Compressor error at 0dB output level	Input level = 0dB	-1.5	+0.1	1.5	dB
COMP_{OUT}	Compressor error at +5dB output level	Input level = +10dB	-1.0	+0.04	1.0	dB
COMP_{OUT}	Compressor error at +10dB output level	Input level = +20dB	-1.0	+0.02	1.0	dB
EXP_{OUT}	Expander error at -42dB output level	Input level = -21dB	-1.0	-0.12	1.0	dB
EXP_{OUT}	Expander error at -21dB output level	Input level = -10.5dB	-1.0	+0.1	1.0	dB
EXP_{OUT}	Expander error at -10dB output level	Input level = -5dB	-1.0	+0.03	1.0	dB

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AC ELECTRICAL CHARACTERISTICS (Continueud)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
EXP _{OUT}	Expander error at 0dB output level	Input level = 0dB	-1.5	-0.2	1.5	dB
EXP _{OUT}	Expander error at +10dB output level	Input level = +5dB	-1.0	+0.03	1.0	dB
EXP _{OUT}	Expander error at +20dB output level ²	Input level = +10dB	-1.0	-0.1	1.0	dB
EXP _{OUT}	Expander V _{OS}	No signal	-50.0	+3.0	50.0	mV
EXP _{OUT}	Expander output DC shift	No signal to 0dB	-100	+2.0	100	mV
	Timing capacitors compandor			2200		nF
THD	Total harmonic distortion					
	Compressor	1kHz, 0dB BW=300-3kHz		0.2	1	%
	Expander	1kHz, 0dB BW=300-3kHz		0.1	1	%
	NCAN _{OUT}	1kHz, Pin 2 open output level = 0dB			0.02	1
1kHz, Pin 2 open output level = +20dB				0.06	1	%
VOX _{OUT}	Sink current				0.5	mA
	Low level High level	Open collector I _L = 0.5mA		V _{CC}	0.4	V V
VOX _{CTL}	Input current	Low	-50	-6.6	0	μA
		High	-10	-0.02	+10	μA
	Input level	Low High	0 0.7V _{CC}		0.3V _{CC} V _{CC}	V V
H _{PDN}	Input current	Low	-10	-4.1	+10	μA
		High	-10	-0.2	+10	μA
	Input level	Low High	0 0.7V _{CC}		0.3V _{CC} V _{CC}	V V
	Reference filter capacitor			10		μF

NOTE:

1. Measurements are relative to 0dB output.
2. Measurement is indicative of the output dynamic range capability.

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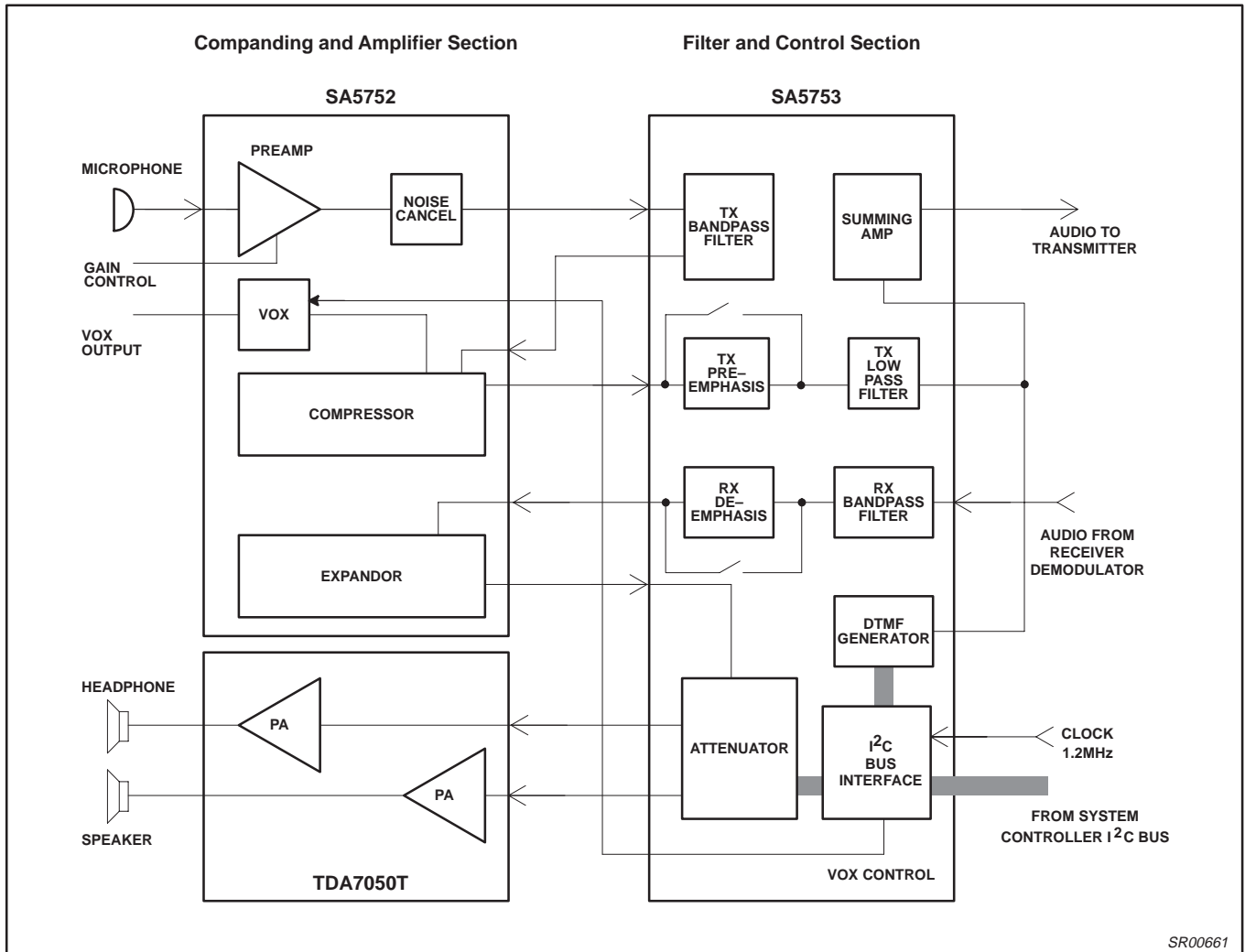


Figure 3. Typical Configuration of Audio Processor (APROC) System Chip Set

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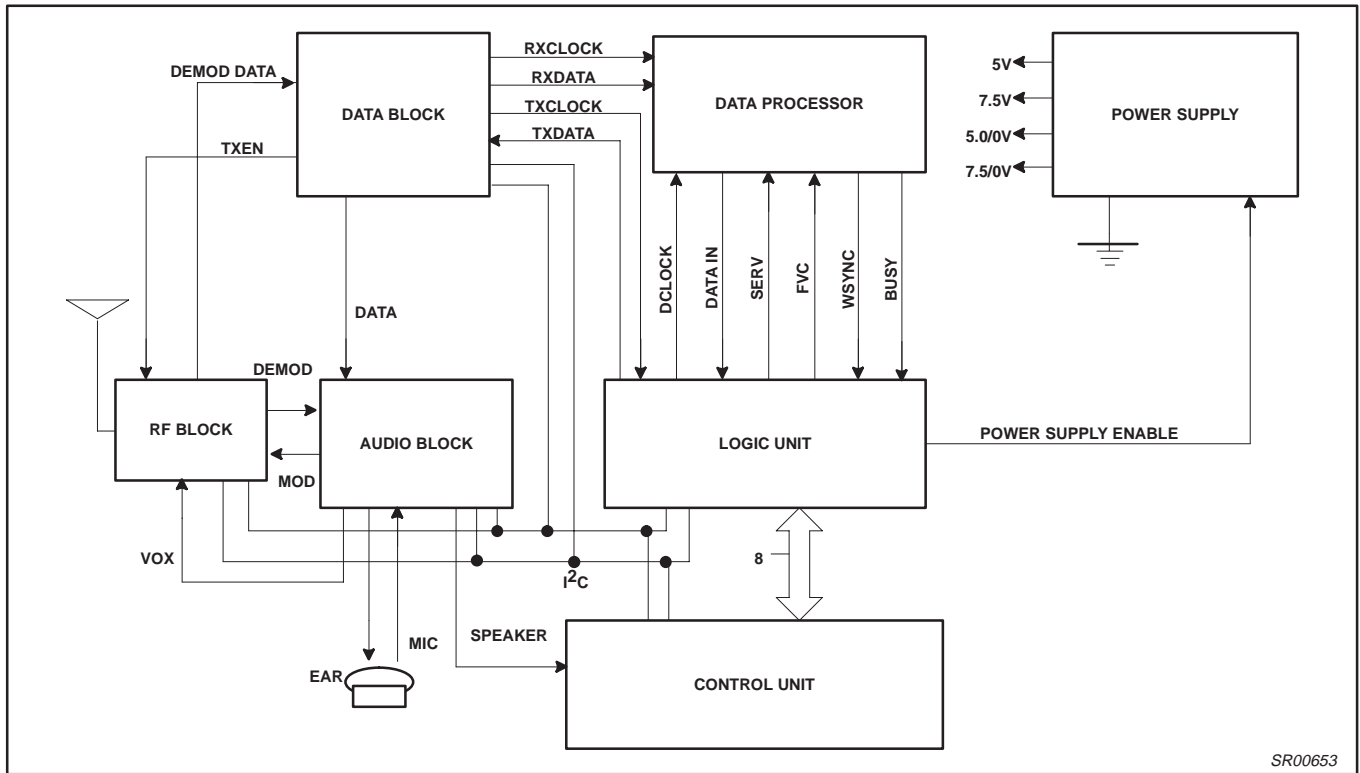


Figure 4. Cellular Radio System

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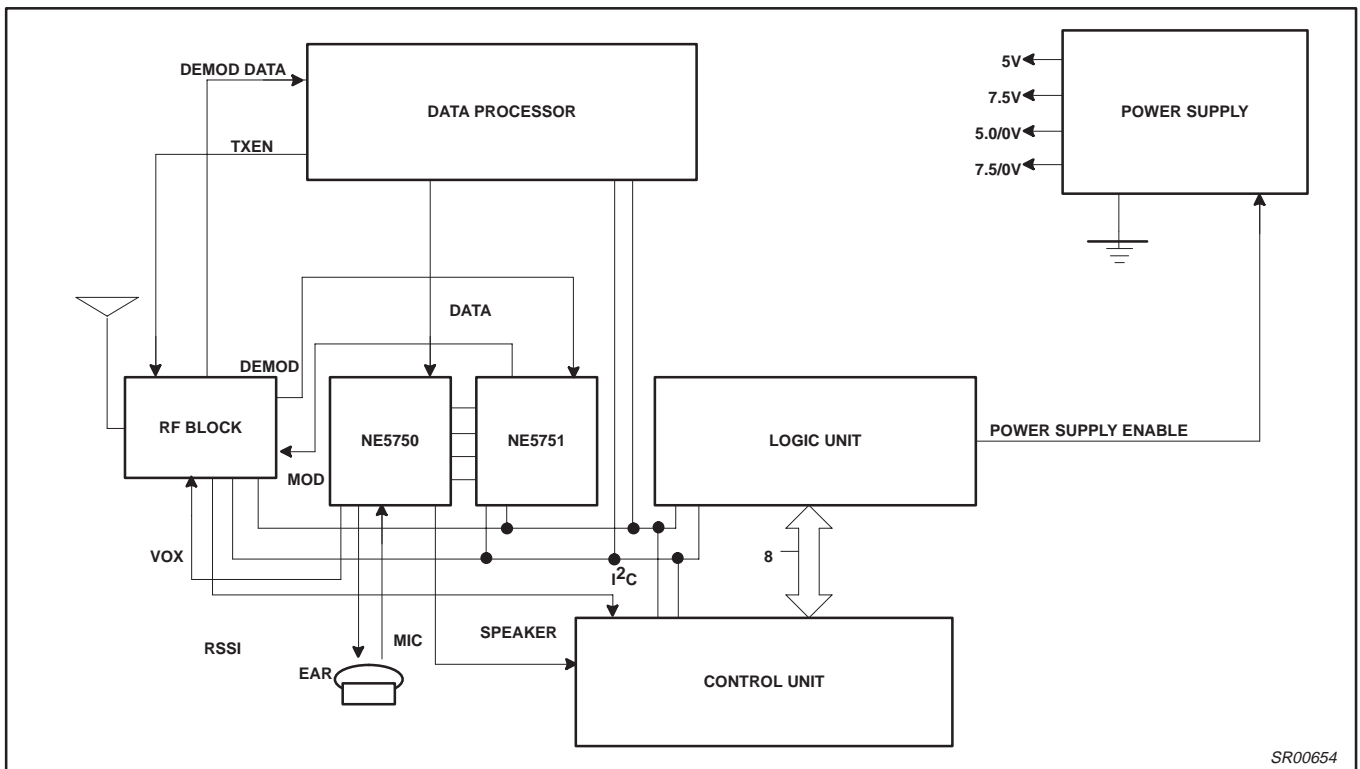


Figure 5. APROC Application Diagram

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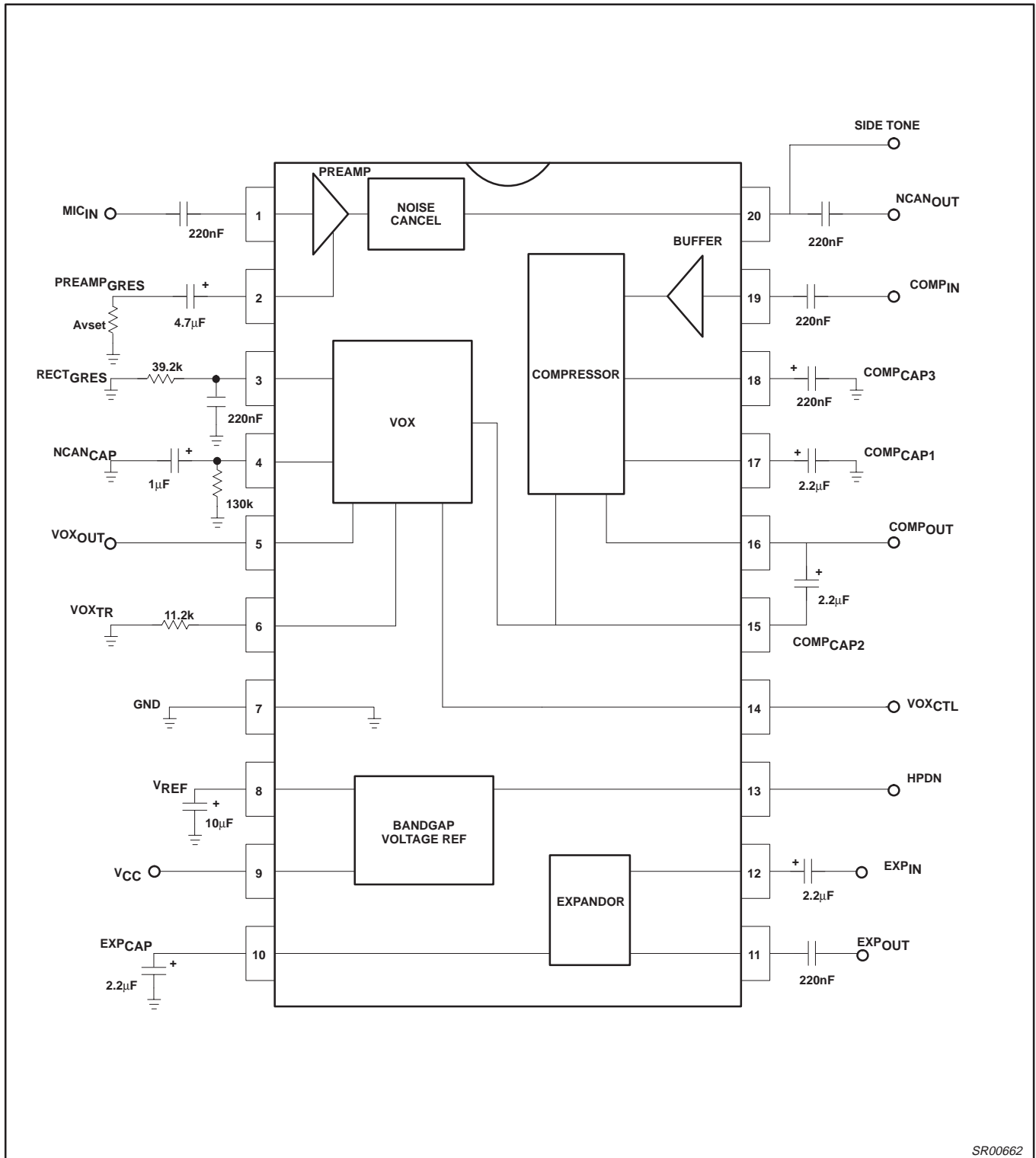


Figure 6. SA5752 Test and Application Circuit

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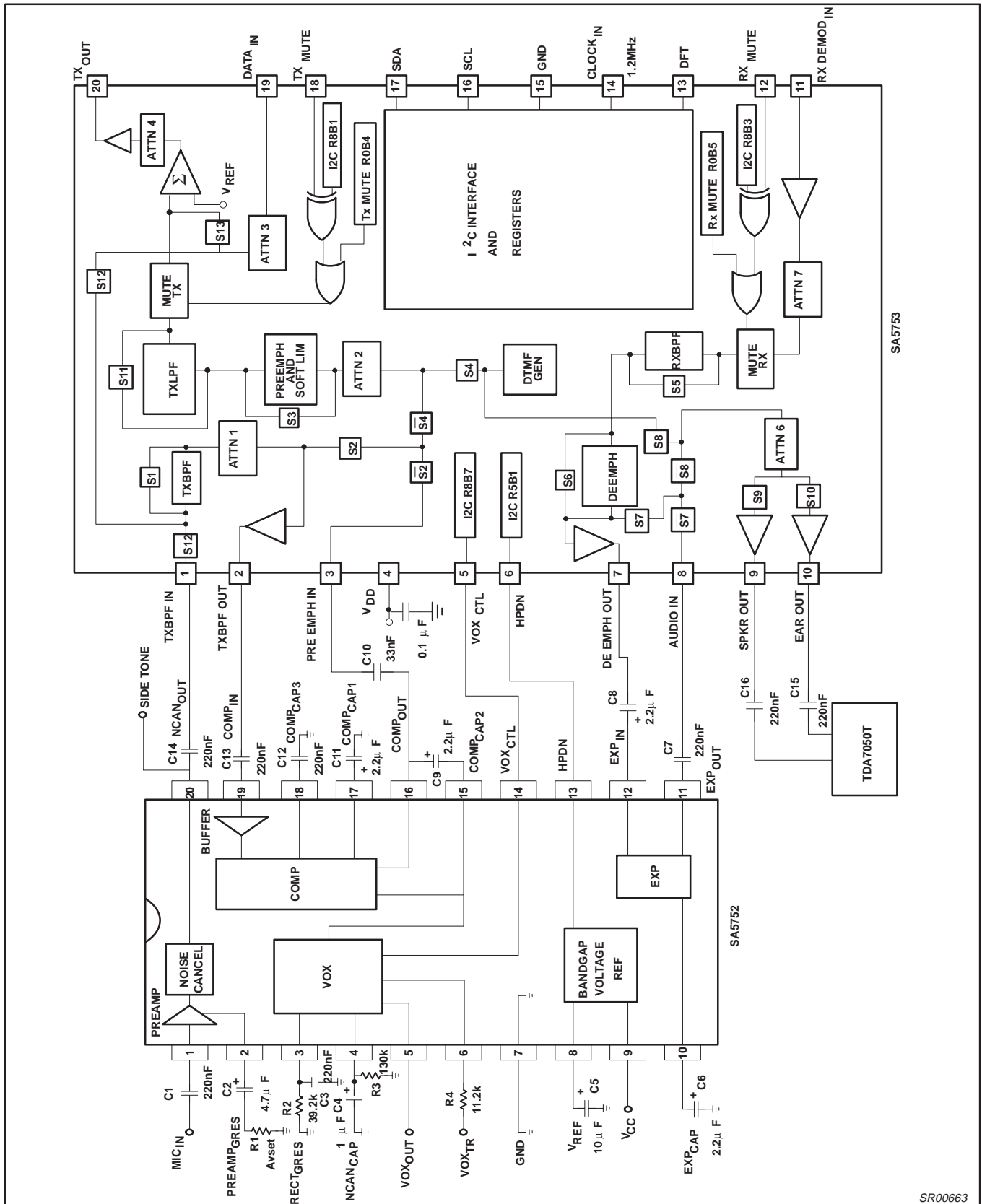


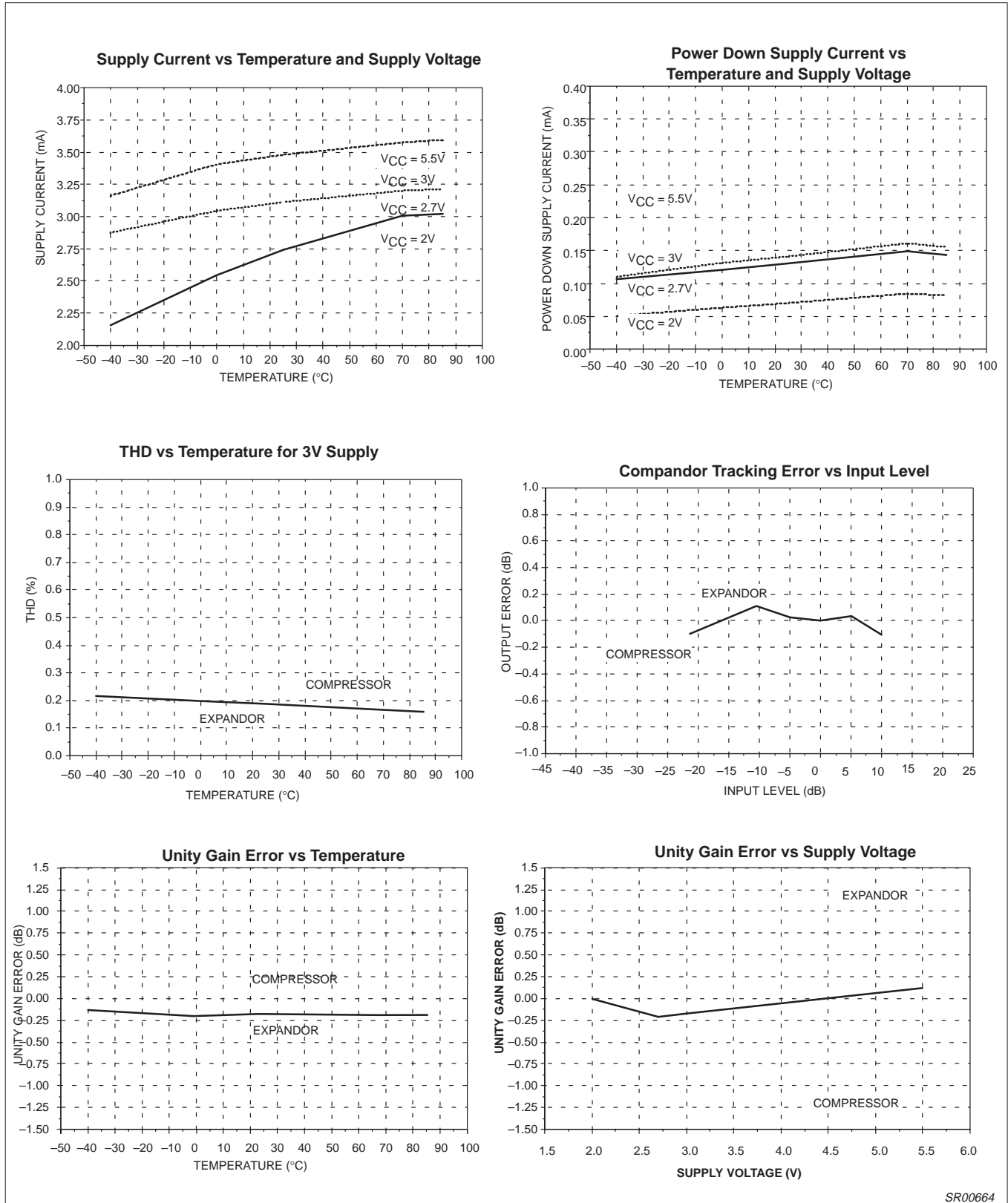
Figure 7. Application Diagram for the Audio Processor

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TYPICAL PERFORMANCE CHARACTERISTICS



SR00664

Figure 8. Typical Performance Characteristics

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TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

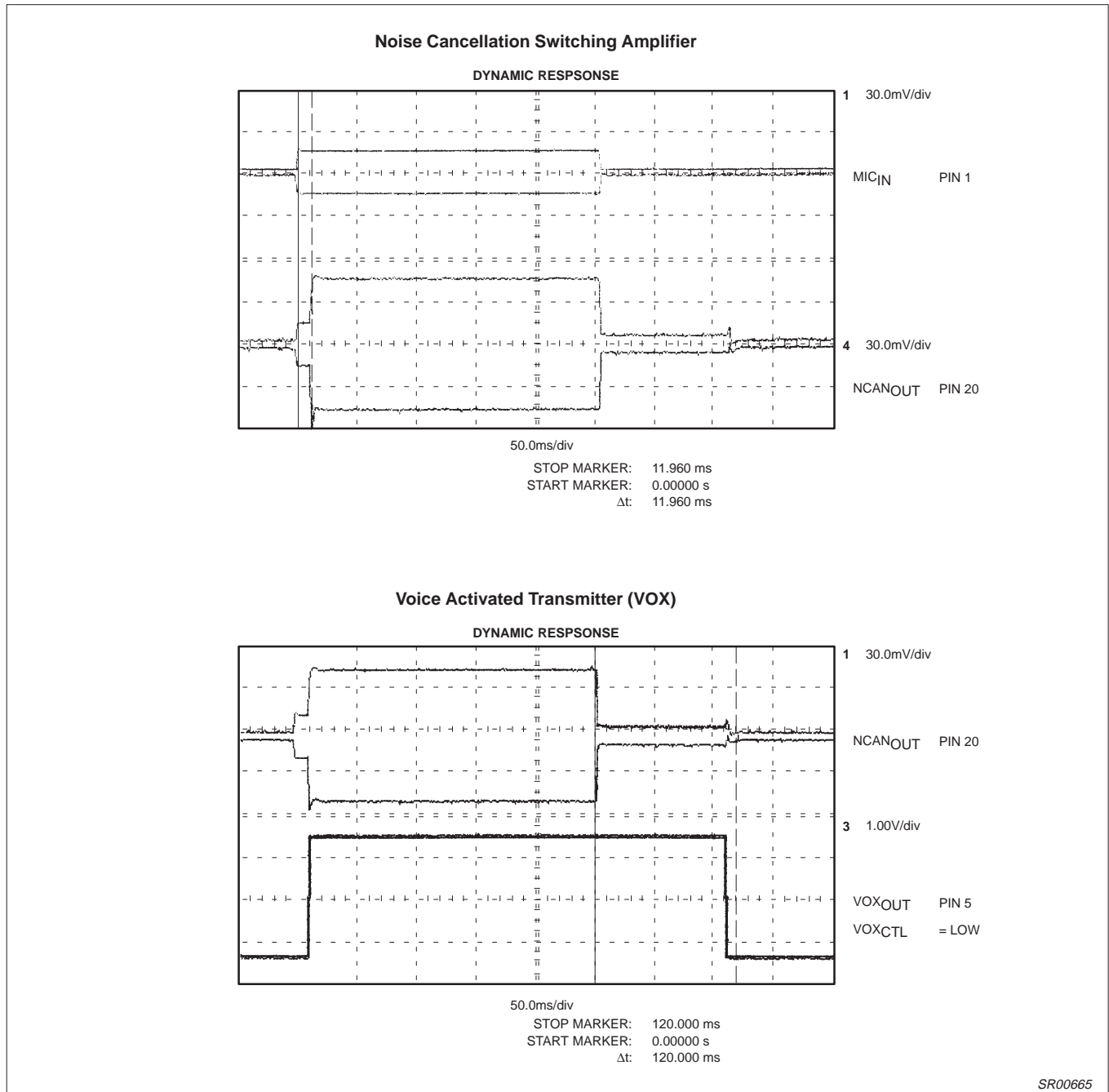


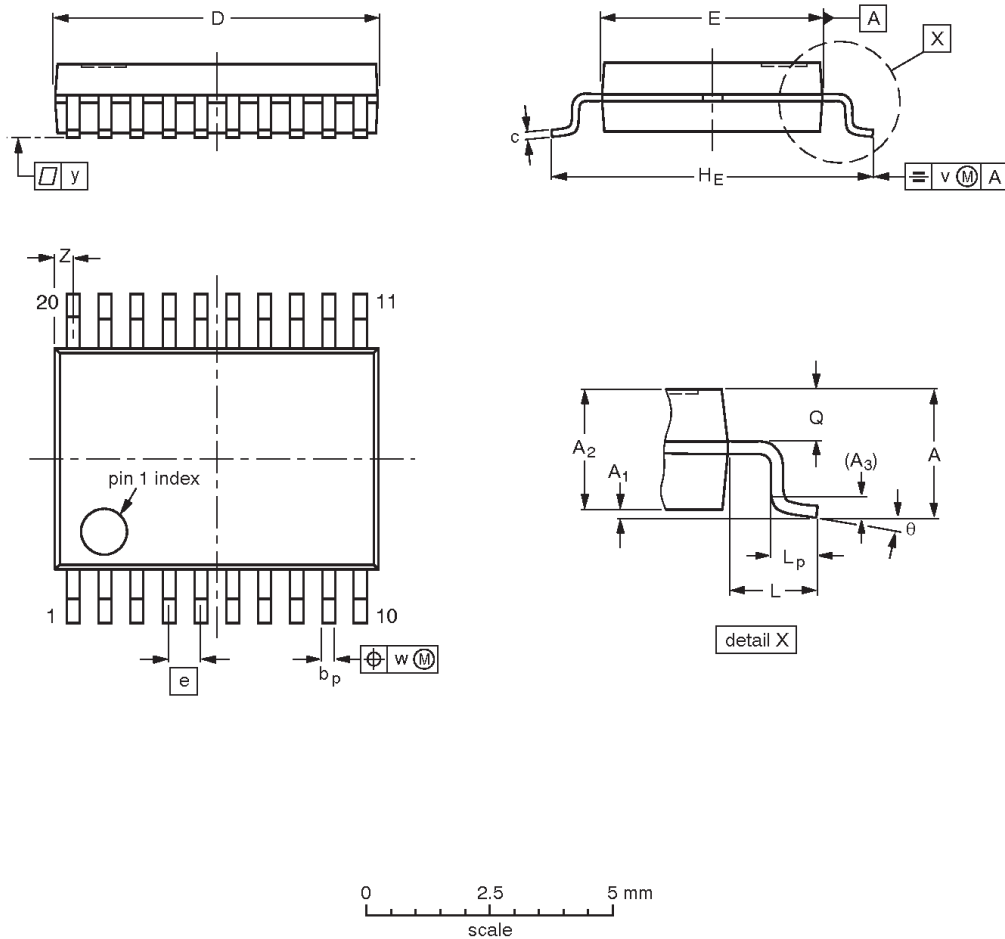
Figure 9. Typical Performance Characteristics (cont.)

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SSOP20: plastic shrink small outline package; 20 leads; body width 4.4 mm

SOT266-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽¹⁾	e	H _E	L	L _p	Q	v	w	y	Z ⁽¹⁾	θ
mm	1.5	0.15 0	1.4 1.2	0.25	0.32 0.20	0.20 0.13	6.6 6.4	4.5 4.3	0.65	6.6 6.2	1.0	0.75 0.45	0.65 0.45	0.2	0.13	0.1	0.48 0.18	10° 0°

Note

1. Plastic or metal protrusions of 0.20 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT266-1						90-04-05 95-02-25

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DEFINITIONS

Data Sheet Identification	Product Status	Definition
<i>Objective Specification</i>	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.
<i>Preliminary Specification</i>	Preproduction Product	This data sheet contains preliminary data, and supplementary data will be published at a later date. Philips Semiconductors reserves the right to make changes at any time without notice in order to improve design and supply the best possible product.
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