

LM4050

Precision Micropower Shunt Voltage Reference

General Description

Ideal for space critical applications, the LM4050 precision voltage reference is available in the sub-miniature (3 mm x 1.3 mm) SOT-23 surface-mount package. The LM4050's design eliminates the need for an external stabilizing capacitor while ensuring stability with any capacitive load, thus making the LM4050 easy to use. Further reducing design effort is the availability of several fixed reverse breakdown voltages: 2.048V, 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V. The minimum operating current increases from 60 μA for the LM4050-2.0 to 100 μA for the LM4050-10.0. All versions have a maximum operating current of 15 mA.

The LM4050 utilizes fuse and zener-zap reverse breakdown voltage trim during wafer sort to ensure that the prime parts have an accuracy of better than $\pm 0.1\%$ (A grade) at $25\,^{\circ}\text{C}.$ Bandgap reference temperature drift curvature correction and low dynamic impedance ensure stable reverse breakdown voltage accuracy over a wide range of operating temperatures and currents.

All grades and voltage options of the LM4050 are available in both an industrial temperature range (-40°C and +85°C) and an extended temperature range (-40°C and +125°C).

Features

Small packages: SOT-23No output capacitor required

- Tolerates capacitive loads
- Fixed reverse breakdown voltages of 2.048V, 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V

Key Specifications (LM4050-2.5)

Output voltage tolerance

(A grade, 25°C) ±0.1% (max)

■ Low output noise (10 Hz to 10 kHz)

41 $\mu V_{rms}(typ)$

■ Wide operating current range

60 μA to 15 mA -40°C to +85°C

Industrial temperature rangeExtended temperature range

-40°C to +125°C

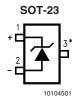
■ Low temperature coefficient

50 ppm/°C (max)

Applications

- Portable, Battery-Powered Equipment
- Data Acquisition Systems
- Instrumentation
- Process Control
- Energy Management
- Product Testing
- Automotive
- Precision Audio Components

Connection Diagram



Top View
See NS Package Number MF03A

^{*}This pin must be left floating or connected to pin 2.

Ordering Information

Industrial Temperature Range (-40°C to +85°C)

Reverse Breakdown	LM4050 Ourselled as 4000 Halfa	LM4050 Occupitad as 0000 Huita
Voltage Tolerance at 25°C and Average	LM4050 Supplied as 1000 Units,	LM4050 Supplied as 3000 Units
Reverse Breakdown	Tape and Reel	Tape and Reel
Voltage Temperature Coefficient		
	LM4050AIM3-2.0	LM4050AIM3X-2.0
	LM4050AIM3-2.5	LM4050AIM3X-2.5
	LM4050AIM3-4.1	LM4050AIM3X-4.1
±0.1%, 50 ppm/°C max (A grade)	LM4050AIM3-5.0	LM4050AIM3X-5.0
	LM4050AIM3-8.2	LM4050AIM3X-8.2
	LM4050AIM3-10	LM4050AIM3X-10
	LM4050BIM3-2.0	LM4050BIM3X-2.0
	LM4050BIM3-2.5	LM4050BIM3X-2.5
	LM4050BIM3-4.1	LM4050BIM3X-4.1
±0.2%, 50 ppm/°C max (B grade)	LM4050BIM3-5.0	LM4050BIM3X-5.0
	LM4050BIM3-8.2	LM4050BIM3X-8.2
	LM4050BIM3-10	LM4050BIM3X-10
	LM4050CIM3-2.0	LM4050CIM3X-2.0
	LM4050CIM3-2.5	LM4050CIM3X-2.5
	LM4050CIM3-4.1	LM4050CIM3X-4.1
±0.5%, 50 ppm/°C max (C grade)	LM4050CIM3-5.0	LM4050CIM3X-5.0
	LM4050CIM3-8.2	LM4050CIM3X-8.2
	LM4050CIM3-10	LM4050CIM3X-10

Extended Temperature Range (-40°C to +125°C)

Reverse Breakdown	LAMAGE O LA PLANTA A COOL HAIR	LM4050 O I' I 0000 II. I'.
Voltage Tolerance at 25°C and Average	LM4050 Supplied as 1000 Units,	LM4050 Supplied as 3000 Units
Reverse Breakdown	Tape and Reel	Tape and Reel
Voltage Temperature Coefficient		
	LM4050AEM3-2.0	LM4050AEM3X-2.0
	LM4050AEM3-2.5	LM4050AEM3X-2.5
	LM4050AEM3-4.1	LM4050AEM3X-4.1
±0.1%, 50 ppm/°C max (A grade)	LM4050AEM3-5.0	LM4050AEM3X-5.0
	LM4050AEM3-8.2	LM4050AEM3X-8.2
	LM4050AEM3-10	LM4050AEM3X-10
	LM4050BEM3-2.0	LM4050BEM3X-2.0
	LM4050BEM3-2.5	LM4050BEM3X-2.5
	LM4050BEM3-4.1	LM4050BEM3X-4.1
±0.2%, 50 ppm/°C max (B grade)	LM4050BEM3-5.0	LM4050BEM3X-5.0
	LM4050BEM3-8.2	LM4050BEM3X-8.2
	LM4050BEM3-10	LM4050BEM3X-10
	LM4050CEM3-2.0	LM4050CEM3X-2.0
	LM4050CEM3-2.5	LM4050CEM3X-2.5
	LM4050CEM3-4.1	LM4050CEM3X-4.1
±0.5%, 50 ppm/°C max (C grade)	LM4050CEM3-5.0	LM4050CEM3X-5.0
	LM4050CEM3-8.2	LM4050CEM3X-8.2
	LM4050CEM3-10	LM4050CEM3X-10

SOT-23 Package Marking Information

Only three fields of marking are possible on the SOT-23's small surface. This table gives the meaning of the three fields.

Part Marking	Field Definition
RCA	First Field:
RDA	R = Reference
REA	Second Field:
RFA	N = 2.048V Voltage Option
RGA	C = 2.500V Voltage Option
RNA	D = 4.096V Voltage Option
RCB	E = 5.000V Voltage Option
RDB	F = 8.192V Voltage Option
REB	G = 10.000V Voltage Option
RFB	
RGB	Third Field:
RNB	
RCC	A-C = Initial Reverse Breakdown Voltage or Reference Voltage Tolerance
RDC	$A = \pm 0.1\%$, $B = \pm 0.2\%$, $C = +0.5\%$,
REC	
RFC	
RGC	
RNC	

Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Reverse Current 20 mA Forward Current 10 mA

Power Dissipation ($T_A = 25^{\circ}C$) (Note 2)

M3 Package 280 mW Storage Temperature -65°C to +150°C

Lead Temperature

M3 Package

Vapor phase (60 seconds) +215°C Infrared (15 seconds) +220°C

ESD Susceptibility

Human Body Model (Note 3) 2 kV Machine Model (Note 3) 200V

See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for other methods of soldering surface mount devices.

Operating Ratings (Note 2)

LM4050-4.1

LM4050-5.0

Temperature Range $(T_{min} \le T_A \le T_{max})$ Industrial Temperature Range $-40^{\circ}\text{C} \le T_A \le +85^{\circ}\text{C}$ Extended temperature Range $-40^{\circ}\text{C} \le T_A \le +125^{\circ}\text{C}$ Reverse Current LM4050-2.0 60 μA to 15 mA LM4050-2.5 60 μA to 15 mA

LM4050-8.2 91 μA to 15 mA LM4050-10.0 100 μA to 15 mA

 $68 \mu A$ to 15 mA

 $74 \mu A$ to 15 mA

LM4050-2.0 Electrical Characteristics

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$, $\pm 0.2\%$, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 100 μA	2.048				V
	Reverse Breakdown Voltage	I _R = 100 μA		±2.048	±4.096	±10.24	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±9.0112	±11.4688	±14.7456	mV (max)
		Extended Temp. Range		±12.288	±14.7456	±17.2032	mV (max)
I _{RMIN}	Minimum Operating Current		41				μΑ
				60	60	60	μA (max)
				65	65	65	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±20				ppm/°C
	Voltage Temperature Coefficient (Note 6)	I _R = 1 mA	±15				ppm/°C
	Coomercial (riete e)	I _R = 100 μA	±15	±50	±50	±50	ppm/°C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 7)	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.3				mV
				0.8	0.8	0.8	mV (max)
				1.2	1.2	1.2	mV (max)
		1 mA ≤ I _R ≤ 15 mA	2.3				mV
				6.0	6.0	6.0	mV (max)
				8.0	8.0	8.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz, I _{AC} = 0.1 I _R	0.3				Ω
e _N	Wideband Noise	I _R = 100 μA	34				μV _{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C I _R = 100 μA	120				ppm
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ}C$ to 125°C	0.7				mV

LM4050-2.5 Electrical Characteristics

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$, $\pm 0.2\%$, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 100 μA	2.500				V
	Reverse Breakdown Voltage	I _R = 100 μA		±2.5	±5.0	±13	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±11	±14	±21	mV (max)
		Extended Temp. Range		±15	±18	±25	mV (max)
I _{RMIN}	Minimum Operating Current		41				μΑ
				60	60	60	μA (max)
				65	65	65	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±20				ppm/°C
	Voltage Temperature Coefficient (Note 6)	I _R = 1 mA	±15				ppm/°C
		I _R = 100 μA	±15	±50	±50	±50	ppm/°C (max)
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 7)	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.3				mV
				0.8	0.8	0.8	mV (max)
				1.2	1.2	1.2	mV (max)
		1 mA ≤ I _R ≤ 15 mA	2.3				mV
				6.0	6.0	6.0	mV (max)
				8.0	8.0	8.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz, I _{AC} = 0.1 I _R	0.3				Ω
e _N	Wideband Noise	I _R = 100 μA	41				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_{R}	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C I _R = 100 μA	120				ppm
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ}C$ to 125°C	0.7				mV

LM4050-4.1 Electrical Characteristics

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$, $\pm 0.2\%$, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 100 μA	4.096				V
	Reverse Breakdown Voltage	I _R = 100 μA		±4.1	±8.2	±21	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±18	±22	±34	mV (max)
		Extended Temp. Range		±25	±29	±41	mV (max)
I _{RMIN}	Minimum Operating Current		52				μA
				68	68	68	μA (max)
		Industrial Temp. Range		73	73	73	μA (max)
		Extended Temp. Range		78	78	78	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±30				ppm/°C
	Voltage Temperature Coefficient (Note 6)	I _R = 1 mA	±20				ppm/°C
		I _R = 100 μA	±20	±50	±50	±50	ppm/°C (max)
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.2				mV
	Change with Operating Current Change (Note 7)			0.9	0.9	0.9	mV (max)
				1.2	1.2	1.2	mV (max)
		1 mA ≤ I _R ≤ 15 mA	2.0				mV
				7.0	7.0	7.0	mV (max)
				10.0	10.0	10.0	mV (max)

LM4050-4.1 Electrical Characteristics (Continued)

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$, $\pm 0.2\%$, and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz,	0.5				Ω
		$I_{AC} = 0.1 I_{R}$					
e _N	Wideband Noise	I _R = 100 μA	93				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV _R	Reverse Breakdown Voltage Long Term Stability	t = 1000 hrs T = 25°C ±0.1°C I _R = 100 μA	120				ppm
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ} C$ to 125°C	1.148				mV

LM4050-5.0 Electrical Characteristics

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$, $\pm 0.2\%$ and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 100 μA	5.000				V
	Reverse Breakdown Voltage	I _R = 100 μA		±5.0	±10	±25	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±22	±27	±42	mV (max)
		Extended Temp. Range		±30	±35	±50	mV (max)
I _{RMIN}	Minimum Operating Current		56				μA
				74	74	74	μA (max)
		Industrial Temp. Range		80	80	80	μA (max)
		Extended Temp. Range		90	90	90	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±30				ppm/°C
	Voltage Temperature Coefficient (Note 6)	I _R = 1 mA	±20				ppm/°C
		I _R = 100 μA	±20	±50	±50	±50	ppm/°C (max)
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 7)	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.2				mV
				1.0	1.0	1.0	mV (max)
				1.4	1.4	1.4	mV (max)
		$1 \text{ mA} \leq I_{R} \leq 15 \text{ mA}$	2.0				mV
				8.0	8.0	8.0	mV (max)
				12.0	12.0	12.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz,	0.5				Ω
		$I_{AC} = 0.1 I_{R}$					Ω (max)
e _N	Wideband Noise	I _R = 100 μA	93				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_R	Reverse Breakdown Voltage	t = 1000 hrs					
	Long Term Stability	$T = 25^{\circ}C \pm 0.1^{\circ}C$	120				ppm
		I _R = 100 μA					
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ}C$ to 125°C	1.4				mV

LM4050-8.2 Electrical Characteristics

Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}C$. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$ and $\pm 0.2\%$ and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 150 μA	8.192				V
	Reverse Breakdown Voltage	I _R = 150 μA		±8.2	±16	±41	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±35	±43	±68	mV (max)
		Extended Temp. Range		±49	±57	±82	mV (max)
I _{RMIN}	Minimum Operating Current		74				μΑ
				91	91	91	μA (max)
		Industrial Temp. Range		95	95	95	μA (max)
		Extended Temp. Range		100	100	100	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown	I _R = 10 mA	±40				ppm/°C
	Voltage Temperature Coefficient (Note 6)	I _R = 1 mA	±20				ppm/°C
		I _R = 150 μA	±20	±50	±50	±50	ppm/°C (max)
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 7)	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.6				mV
				1.3	1.3	1.3	mV (max)
				2.5	2.5	2.5	mV (max)
		1 mA ≤ I _R ≤ 15 mA	7.0				mV
				10.0	10.0	10.0	mV (max)
				18.0	18.0	18.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz,	0.6				Ω
		$I_{AC} = 0.1 I_{R}$					
e _N	Wideband Noise	I _R = 150 μA	150				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_R	Reverse Breakdown Voltage	t = 1000 hrs					
	Long Term Stability	$T = 25^{\circ}C \pm 0.1^{\circ}C$	120				ppm
		I _R = 150 μA					
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ} C \text{ to } 125^{\circ} C$	2.3				mV

LM4050-10.0 Electrical Characteristics

Boldface limits apply for T_A = T_J = T_{MIN} to T_{MAX}; all other limits $T_A = T_J = 25$ °C. The grades A, B and C designate initial Reverse Breakdown Voltage tolerances of $\pm 0.1\%$ and $\pm 0.2\%$ and 0.5% respectively.

Symbol	Parameter	Conditions	Typical (Note 4)	LM4050AIM3 LM4050AEM3 Limits (Note 5)	LM4050BIM3 LM4050BEM3 Limits (Note 5)	LM4050CIM3 LM4050CEM3 Limits (Note 5)	Units (Limit)
V _R	Reverse Breakdown Voltage	I _R = 150 μA	10.00				V
	Reverse Breakdown Voltage	I _R = 150 μA		±10	±20	±50	mV (max)
	Tolerance (Note 6)	Industrial Temp. Range		±43	±53	±83	mV (max)
		Extended Temp. Range		±60	±70	±100	mV (max)
I _{RMIN}	Minimum Operating Current		80				μA
				100	100	100	μA (max)
		Industrial Temp. Range		103	103	103	μA (max)
		Extended Temp. Range		110	110	110	μA (max)
$\Delta V_R/\Delta T$	Average Reverse Breakdown Voltage Temperature Coefficient (Note 6)	I _R = 10 mA	±40				ppm/°C
		I _R = 1 mA	±20				ppm/°C
		I _R = 150 μA	±20	±50	±50	±50	ppm/°C (max)
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage Change with Operating Current Change (Note 7)	$I_{RMIN} \le I_R \le 1 \text{ mA}$	0.8				mV
				1.5	1.5	1.5	mV (max)
				3.5	3.5	3.5	mV (max)
		1 mA ≤ I _R ≤ 15 mA	8.0				mV
				12.0	12.0	12.0	mV (max)
				23.0	23.0	23.0	mV (max)
Z _R	Reverse Dynamic Impedance	I _R = 1 mA, f = 120 Hz,	0.7				Ω
		$I_{AC} = 0.1 I_{R}$					
e _N	Wideband Noise	I _R = 150 μA	150				μV_{rms}
		10 Hz ≤ f ≤ 10 kHz					
ΔV_R	Reverse Breakdown Voltage	t = 1000 hrs					
	Long Term Stability	$T = 25^{\circ}C \pm 0.1^{\circ}C$	120				ppm
		I _R = 150 μA					
V _{HYST}	Thermal Hysteresis (Note 8)	$\Delta T = -40^{\circ} C$ to $125^{\circ} C$	2.8				mV

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 2: The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{Jmax} (maximum junction temperature), θ_{JA} (junction to ambient thermal resistance), and T_A (ambient temperature). The maximum allowable power dissipation at any temperature is $PD_{max} = (T_{Jmax} - T_A)/\theta_{JA}$ or the number given in the Absolute Maximum Ratings, whichever is lower. For the LM4050, $T_{Jmax} = 125^{\circ}C$, and the typical thermal resistance (θ_{JA}), when board mounted, is 326°C/W for the SOT-23 package.

Note 3: The human body model is a 100 pF capacitor discharged through a 1.5 k Ω resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

Note 4: Typicals are at $T_J = 25^{\circ}C$ and represent most likely parametric norm.

Note 5: Limits are 100% production tested at 25°C. Limits over temperature are guaranteed through correlation using Statistical Quality Control (SQC) methods. The limits are used to calculate National's AOQL.

Note 6: The boldface (over-temperature) limit for Reverse Breakdown Voltage Tolerance is defined as the room temperature Reverse Breakdown Voltage Tolerance $\pm[(\Delta V_R/\Delta T)(max\Delta T)(V_R)]$. Where, $\Delta V_R/\Delta T$ is the V_R temperature coefficient, $max\Delta T$ is the maximum difference in temperature from the reference point of 25°C to T_{MIN} or T_{MAX} , and V_R is the reverse breakdown voltage. The total over-temperature tolerance for the different grades in the industrial temperature range where $max\Delta T = 65$ °C is shown below:

A-grade: ±0.425% = ±0.1% ±50 ppm/°C x 65°C B-grade: ±0.525% = ±0.2% ±50 ppm/°C x 65°C C-grade: ±0.825% = ±0.5% ±50 ppm/°C x 65°C

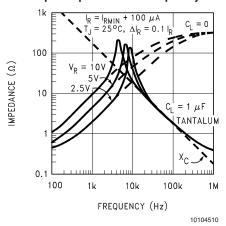
Therefore, as an example, the A-grade LM4050-2.5 has an over-temperature Reverse Breakdown Voltage tolerance of ±2.5V x 0.425% = ±11 mV.

Note 7: Load regulation is measured on pulse basis from no load to the specified load current. Output changes due to die temperature change must be taken into account separately.

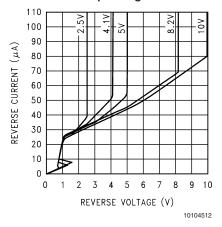
Note 8: Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

Typical Performance Characteristics

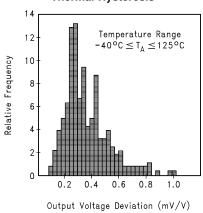
Output Impedance vs Frequency



Reverse Characteristics and Minimum Operating Current

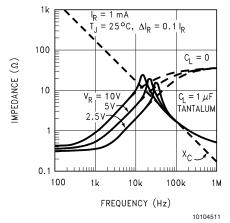


Thermal Hysteresis

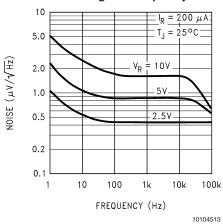


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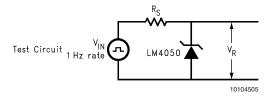
Output Impedance vs Frequency

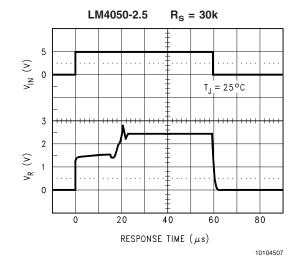


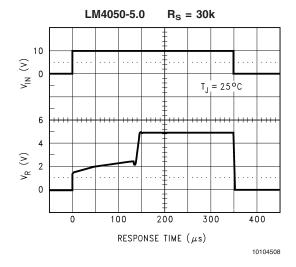
Noise Voltage vs Frequency

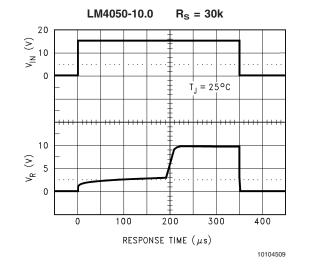


Start-Up Characteristics

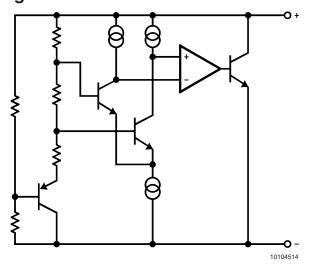








Functional Block Diagram



Applications Information

The LM4050 is a precision micro-power curvature-corrected bandgap shunt voltage reference. For space critical applications, the LM4050 is available in the sub-miniature SOT-23 surface-mount package. The LM4050 has been designed for stable operation without the need of an external capacitor connected between the "+" pin and the "–" pin. If, however, a bypass capacitor is used, the LM4050 remains stable. Reducing design effort is the availability of several fixed reverse breakdown voltages: 2.048V, 2.500V, 4.096V, 5.000V, 8.192V, and 10.000V. The minimum operating current increases from 60 μA for the LM4050-2.0 to 100 μA for the LM4050-10.0. All versions have a maximum operating current of 15 mA.

LM4050s in the SOT-23 packages have a parasitic Schottky diode between pin 2 (–) and pin 3 (Die attach interface contact). Therefore, pin 3 of the SOT-23 package must be left floating or connected to pin 2.

The 4.096V version allows single +5V 12-bit ADCs or DACs to operate with an LSB equal to 1 mV. For 12-bit ADCs or DACs that operate on supplies of 10V or greater, the 8.192V version gives 2 mV per LSB.

The typical thermal hysteresis specification is defined as the change in +25°C voltage measured after thermal cycling. The device is thermal cycled to temperature -40°C and then measured at 25°C. Next the device is thermal cycled to

temperature +125°C and again measured at 25°C. The resulting $V_{\rm OUT}$ delta shift between the 25°C measurements is thermal hysteresis. Thermal hysteresis is common in precision references and is induced by thermal-mechanical package stress. Changes in environmental storage temperature, operating temperature and board mounting temperature are all factors that can contribute to thermal hysteresis.

In a conventional shunt regulator application (*Figure 1*) , an external series resistor ($R_{\rm S}$) is connected between the supply voltage and the LM4050. $R_{\rm S}$ determines the current that flows through the load ($I_{\rm L}$) and the LM4050 ($I_{\rm Q}$). Since load current and supply voltage may vary, $R_{\rm S}$ should be small enough to supply at least the maximum guaranteed $I_{\rm RMIN}$ (spec. table) to the LM4050 even when the supply voltage is at its minimum and the load current is at its maximum value. When the supply voltage is at its maximum and $I_{\rm L}$ is at its minimum, $R_{\rm S}$ should be large enough so that the current flowing through the LM4050 is less than 15 mA.

 $\rm R_S$ is determined by the supply voltage, $\rm (V_S),$ the load and operating current, ($\rm I_L$ and $\rm I_Q),$ and the LM4050's reverse breakdown voltage, $\rm V_R.$

$$R_S = \frac{V_S - V_R}{I_L + I_Q}$$

Typical Applications

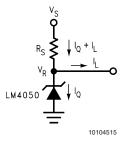
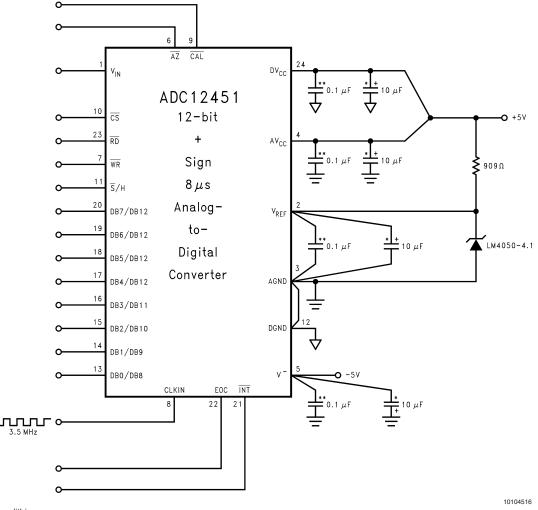


FIGURE 1. Shunt Regulator

Typical Applications (Continued)



^{**}Ceramic monolithic

FIGURE 2. LM4050-4.1's Nominal 4.096 breakdown voltage gives ADC12451 1 mV/LSB

^{*}Tantalum

Typical Applications (Continued)

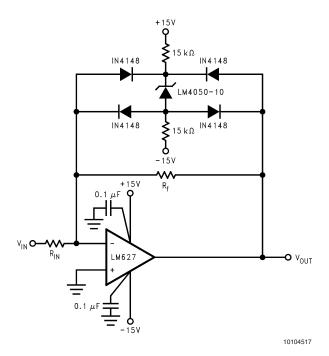


FIGURE 3. Bounded amplifier reduces saturation-induced delays and can prevent succeeding stage damage. Nominal clamping voltage is $\pm 11.5V$ (LM4050's reverse breakdown voltage +2 diode V_F).

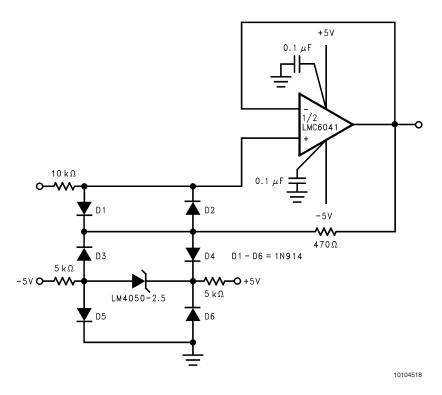


FIGURE 4. Protecting Op Amp input. The bounding voltage is $\pm 4V$ with the LM4050-2.5 (LM4050's reverse breakdown voltage + 3 diode V_F).

Typical Applications (Continued)

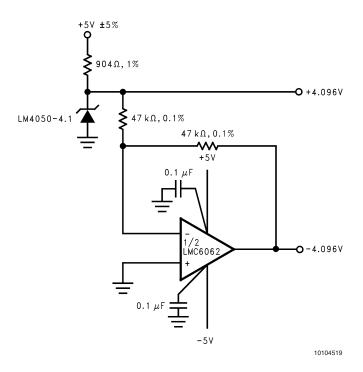


FIGURE 5. Precision ±4.096V Reference

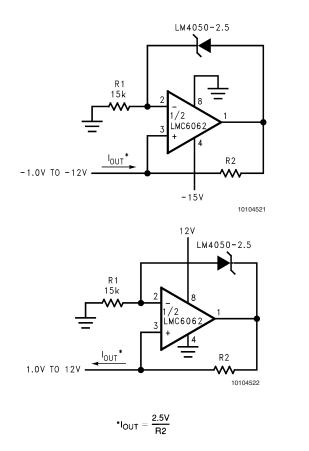
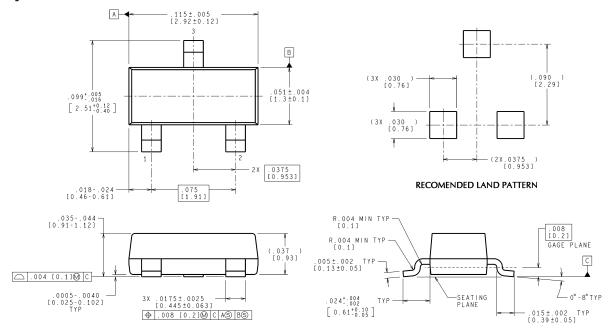


FIGURE 6. Precision 1 μA to 1 mA Current Sources

Physical Dimensions inches (millimeters) unless otherwise noted



CONTROLLING DIMENSION IS INCH VALUES IN [] ARE MILLIMETERS

MF03A (Rev B)

Plastic Surface Mount Package (M3) NS Package Number MF03A (JEDEC Registration TO-236AB)

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