05081

PROJEK DEVICES

PP0640SA thru PP3500SC THYRISTOR SURGE SUPPRESSOR

APPLICATIONS

✔ T1/E1 Trunk & Line Card

- ✓ SLIC Line Card
- ✓ DBX Branch Exchange Switches
- ✔ FCC Part 68 Customer Premise Equipment
- ✓ Line Interface Modem
- ✓ xDSL Architecture Interface
- ✓ ISDN Architecture Interface

IEC COMPATIBILITY (EN61000-4)

- ✔ 61000-4-2 (ESD): Air 15kV, Contact 8kV
- ✓ 61000-4-4 (EFT): 40A 5/50ns
- ✓ 61000-4-5 (Surge): 8/20µs 95A, L4(Line-Gnd), 48A, L4(Line-Line) & 83A, L2(Power)

FEATURES

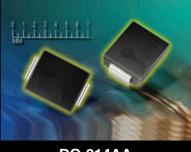
- ✔ Complies with: FCC Part 68, UL 1459, Bellcore 1089, ITU-K.20 & K.21
- ✓ UL File Recognition # E208219
- ✓ Peak Off-State Voltage from 25 to 300 Volts
- ✓ Surge Current Capability (See Table 1)
- ✓ ESD Protection > 40 kilovolts
- ✓ Low Capacitance for T1/E1 Trunk & Line Card Applications
- ✔ Bidirectional Configurations
- ✔ RoHS Compliant

MECHANICAL CHARACTERISTICS

- ✔ Molded Plastic DO-214AA Package
- ✔ Weight 2.5 grams (Approximate)
- ✔ Available in Lead-Free Pure-Tin Plating(Annealed)
- ✓ Solder Reflow Temperature:
 - Pure-Tin Sn, 100: 260-270°C
- Leaded Device Availability
- ✔ Flammability Rating UL 94V-0
- ✓ 12mm Tape and Reel Per EIA Standard 481
- ✓ Marking: Logo & Marking Code

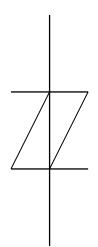
TABLE 1 - SURGE RATINGS									
SERIES							di/dt	dv/dt	
	2 X 10µs AMPS	8 X 20µs AMPS	10 X 160µs AMPS	AMPS	10 X 1000µs AMPS		AMPS/µs (See Note 1)	V/µs (See Note 1)	
							(,	(
SA	150	150	100	50	50	20	500	2000	
SB	300	300	150	100	80	32	500	2000	
SC	500	400	200	200	100	60	500	2000	

Note 1: Critital Rate of Rise for On-State Current (di/dt) and Off-State Voltage (dv/dt).



DO-214AA





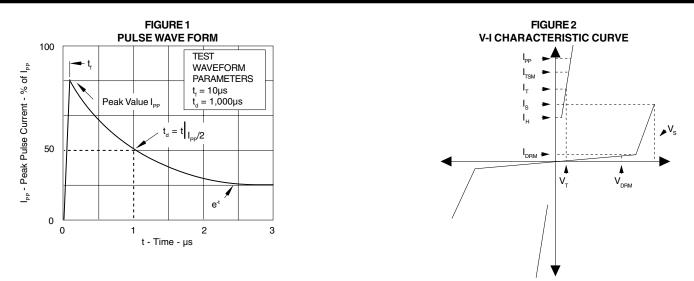
DEVICE CHARACTERISTICS

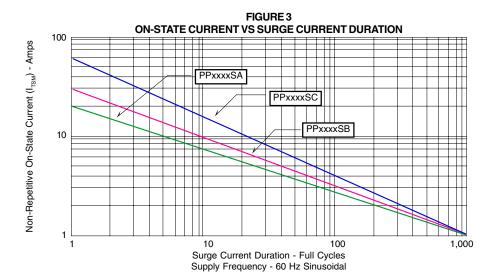
MAXIMUM RATINGS @ 25°C Unless Otherwise Specified							
PARAMETER	SYMBOL	VALUE	UNITS				
Surge Current - 50/60 Hz	I _{TSM}	60	Watts				
Junction Temperature	T _A	-40 to 150	C				
Storage Temperature	T _{stg}	-55 to 150	C				
Thermal Resistance (Junction) - SA & SB Series	Rauc	28	°C/Watt				
Thermal Resistance (Junction) - SC Series	R _{ajc}	26	°C/Watt				
Thermal Resistance (Ambient) - SA & SB Series	R _{QJA}	90	°C/Watt				
Thermal Resistance (Ambient) - SC Series	R _{QJA}	85	°C/Watt				

	ELEC	TRICAL CH	HARACTEF	RISTICS PI	ER LINE @	25°C Unless	Otherwise Sp	ecified	
PART NUMBER	DEVICE MARKING CODE	REPETITIVE PEAK OFF-STATE VOLTAGE	SWITCHING VOLTAGE	MINIMUM HOLDING CURRENT (See Fig. 7)	SWITCHING CURRENT	MAXIMUM OFF-STATE CURRENT (See Fig. 4)	MAXIMUM ON-STATE VOLTAGE (See Fig. 5)	ON-STATE CURRENT	TYPICAL CAPACITANCE (See Note 1)
		V _{DRM} VOLTS	@100V/µs V _s VOLTS	di/dt = 1A/ms I _H mA	I _s mA	@V _{DRM} Ι _{DRM} μΑ	©I _⊤ V _⊤ VOLTS	I _T AMPS	@2V, 1 MHz C pF
PP0640SA PP0720SA PP0800SA PP1100SA PP1300SA PP1500SA PP1800SA PP2300SA PP2600SA PP2600SA PP3100SA	GC D E F G H G G K G	58 65 75 90 120 140 160 190 220 275	77 88 98 130 160 180 220 260 300 350	150 150 150 150 150 150 150 150 150 150	800 800 800 800 800 800 800 800 800 800	5 5 5 5 5 5 5 5 5	4 4 4 4 4 4 4 4 4	2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2	60 60 60 40 40 40 30 30 30 30
PP3500SA PP0640SB PP0720SB PP0800SB PP1100SB PP1300SB PP1500SB PP1800SB PP2300SB PP2600SB PP3100SB PP3500SB	GM GP GQR GGR GGT GV GV GV GZ	300 25 58 65 75 90 120 140 160 190 220 275 300	400 40 77 88 98 130 160 180 220 260 300 350 400	50 150 150 150 150 150 150 150 150 150 1	800 800 800 800 800 800 800 800 800 800	5 5 5 5 5 5 5 5 5 5 5 5 5	4 4 4 4 4 4 4 4 4 4 4 4 4	2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2	30 110 60 60 60 40 40 40 40 30 30 30 30 30 30
PP3500SB PP0640SC PP0720SC PP1100SC PP1300SC PP1500SC PP1800SC PP2300SC PP2600SC PP3100SC PP3500SC	G CDHFGHFGHFJKLM	300 58 65 75 90 120 140 160 190 220 275 300	400 77 88 98 130 160 180 220 260 300 350 400	150 150 150 150 150 150 150 150 150 150	800 800 800 800 800 800 800 800 800 800	5 5 5 5 5 5 5 5 5 5	4 4 4 4 4 4 4 4 4 4 4 4 4	2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2 2.2	30 120 120 120 80 80 80 60 60 60 60 60

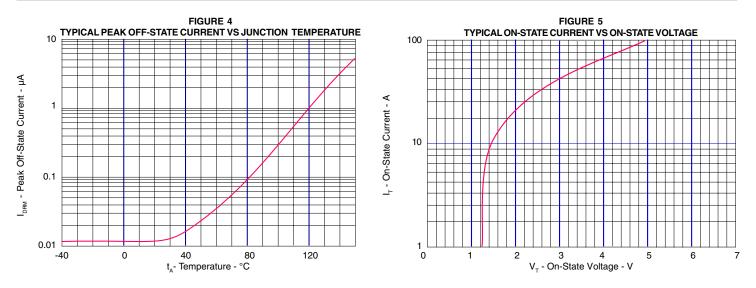
Note 1: Capacitance imbalance between positive and negative polarities is typically < 15 pF.

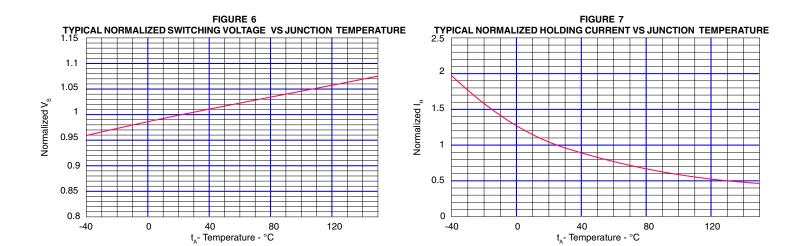
GRAPHS





GRAPHS





APPLICATION NOTE

FIGURE 1: UL 1459 & FCC Part 68 - Metallic Protection

The TSS (Thyristor Surge Suppressor) device is located across the tip-to-ring after a limiting resistor and fuse combination. R_{TIP} and R_{RING} resistors are optional depending upon the TSS device selection. Without the resistors, the PP3100SB/SC is recommended. However, with a resistance value of 7.5 Ohms for tip and ring, the PP3100SA is recommended. Digital signals may use a lower TSS device depending upon the total tip to ring voltage range. Selection of the TSS device, either PPxxxxSA or SB/SC is based upon the value of the tip and ring resistors. For the National Electric Code (NEC) article 800, it is recommended that at least one fuse be used in the tip or ring line for metallic surges. Fuses may be replaced with a suitable Positive Temperature Coefficient (PTC) automatic resettable current limiting device.

FIGURE 2 - UL 1459 & FCC Part 68 - Longitudinal Protection

There are two TSS devices, one located from tip-to-ground and one ring-to-ground. For standard analog signals, the PP3100SA is recommended with a typical resistor value for tip and ring of 15 Ohms. The PP3100SB/SC is recommended for resistor values of 7.5 Ohms each. The National Electric Code (NEC) article 800 requires two fuse elements when connecting to ground. Fuses or a suitable Positive Temperature Coefficient (PTC) automatic resettable current limiting device may be used. The purpose of this circuit is to limit AC power current from getting on the ground line causing any safety hazard.

FIGURE 3 - UL 1459 & FCC Part 68 - Metallic & Longitudinal Protection

Three equal TSS devices are used in this application for metallic (tipto-ring) and longitudinal (tip-to-ground and ring-to-ground) protection. For analog signals, the PP3100SB/SC is recommended. With a resistance value of 15 Ohms for the tip and ring resistors, the PP3100SA may be used. The National Electric Code (NEC) article 800 requires two fuse elements when connecting to ground. Fuses or a suitable Positive Temperature Coefficient (PTC) automatic resettable current limiting device may be used. This circuit is recommended for protection against the Bellcore requirement: First Level Lightning Surge Tests (Telecommunications Port), document # GR-1089-CORE.

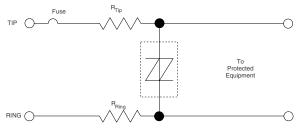
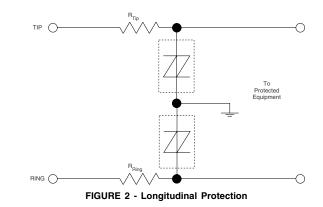


FIGURE 1 - Metallic Protection



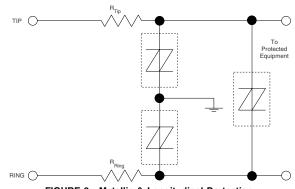
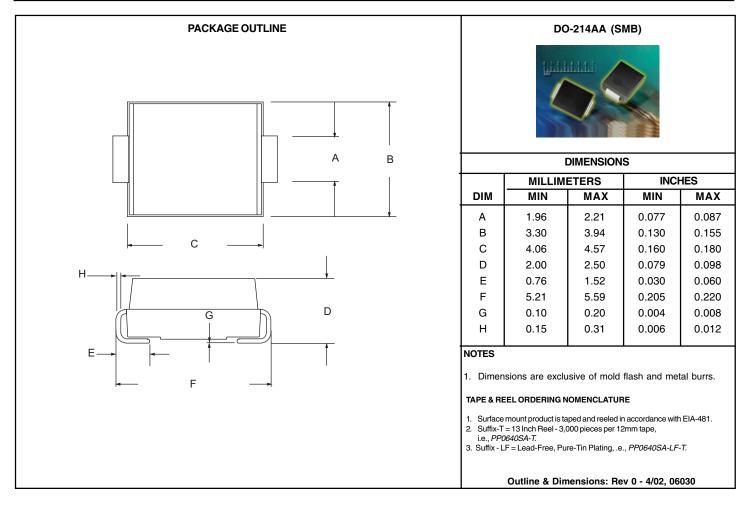


FIGURE 3 - Metallic & Longitudinal Protection

DO-214AA PACKAGE OUTLINE & DIMENSIONS



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