

MOS FIELD EFFECT TRANSISTOR μ PA1900

N-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μ PA1900 is a switching device which can be driven directly by a 2.5 V power source.

The μ PA1900 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 2.5 V power source
- Low on-state resistance

 $\begin{array}{l} {\sf R}_{\sf DS(on)1} = 35 \mbox{ m}\Omega \mbox{ MAX.} ({\sf V}_{\sf GS} = 4.5 \mbox{ V}, \mbox{ Id} = 3.0 \mbox{ A}) \\ {\sf R}_{\sf DS(on)2} = 38 \mbox{ m}\Omega \mbox{ MAX.} ({\sf V}_{\sf GS} = 4.0 \mbox{ V}, \mbox{ Id} = 3.0 \mbox{ A}) \\ {\sf R}_{\sf DS(on)3} = 45 \mbox{ m}\Omega \mbox{ MAX.} ({\sf V}_{\sf GS} = 2.5 \mbox{ V}, \mbox{ Id} = 3.0 \mbox{ A}) \\ \end{array}$

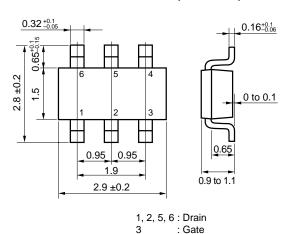
ORDERING INFORMATION

PART NUMBER	PACKAGE		
μΡΑ1900ΤΕ	6-pin Mini Mold (Thin Type)		

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage	VDSS	20	V
Gate to Source Voltage	Vgss	±12	V
Drain Current (DC)	D(DC)	±5.5	А
Drain Current (pulse) Note1	D(pulse)	±22	А
Total Power Dissipation	P T1	0.2	W
Total Power Dissipation Note2	P T2	2	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C

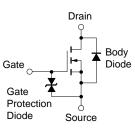
PACKAGE DRAWING (Unit : mm)



4

EQUIVALENT CIRCUIT

: Source



Marking: TG

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1 %

- **2.** Mounted on FR-4 Board, $t \le 5$ sec.
- **Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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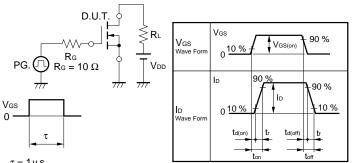
The mark **★** shows major revised points.

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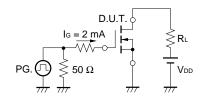
★ ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 20 V, V_{GS} = 0 V$			10	μA
Gate Leakage Current	lgss	$V_{GS} = \pm 12 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Gate Cut-off Voltage	VGS(off)	Vds = 10 V, Id = 1 mA	0.5	0.93	1.5	V
Forward Transfer Admittance	yfs	Vds = 10 V, Id = 3.0 A	3	9.2		S
Drain to Source On-state Resistance	RDS(on)1	$V_{GS} = 4.5 V, I_D = 3.0 A$		28	35	mΩ
	RDS(on)2	$V_{GS} = 4.0 \text{ V}, \text{ Id} = 3.0 \text{ A}$		29	38	mΩ
	RDS(on)3	$V_{GS} = 2.5 V, I_D = 3.0 A$		37	45	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		595		pF
Output Capacitance	Coss	Vgs = 0 V		222		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		133		pF
Turn-on Delay Time	td(on)	Vdd = 10 V		61		ns
Rise Time	tr	ID = 3.0 A		172		ns
Turn-off Delay Time	td(off)	$V_{GS(on)} = 4.0 V$		220		ns
Fall Time	tr	R _G = 10 Ω		293		ns
Total Gate Charge	QG	V _{DS} = 16 V		6.7		nC
Gate to Source Charge	QGS	ID = 5.5 A		1.2		nC
Gate to Drain Charge	Qgd	Vgs = 4.0 V		3.1		nC
Diode Forward Voltage	VF(S-D)	IF = 5.5 A, VGs = 0 V		0.87		V

TEST CIRCUIT 1 SWITCHING TIME



TEST CIRCUIT 2 GATE CHARGE



 $\tau = 1 \mu s$ Duty Cycle $\leq 1 \%$

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2

20

< 16 ∢

12

8

0

1.5

1.0

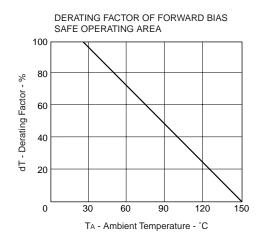
0.5 L -50

0

Ib - Drain Current -

Vestorn) - Gate to Source Cut-off Voltage - V

★ TYPICAL CHARACTERISTICS (T_A = 25 °C)



DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

4.0 V

2.5 V

0.6

VDS - Drain to Source Voltage - V

GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE

50

Tch - Channel Temperature - °C

100

150

0.8

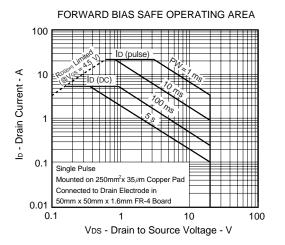
Vps = 10 V lp = 1 mA

1.0

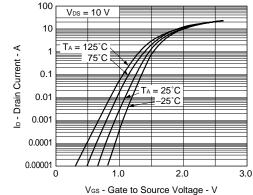
 $V_{GS} = 4.5 V$

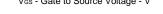
0.4

0.2

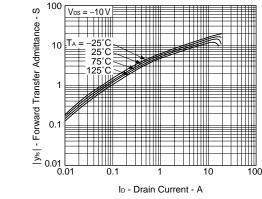






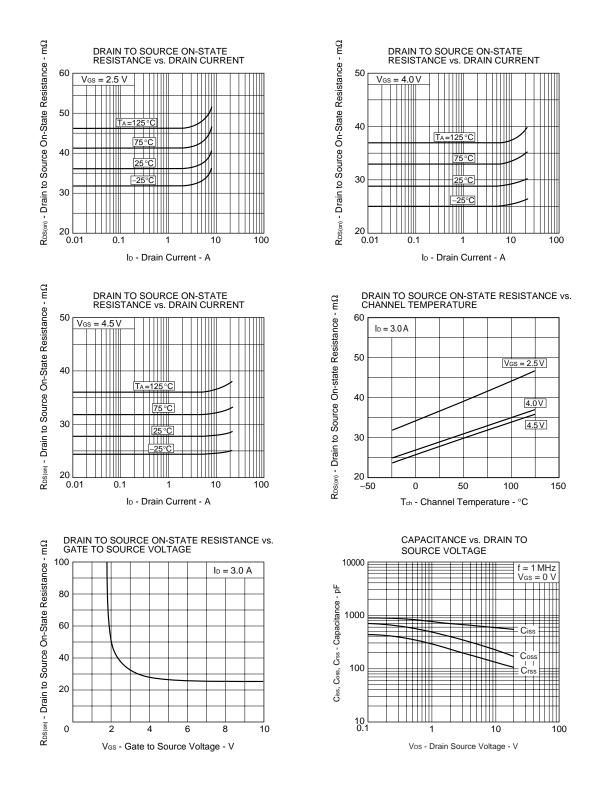






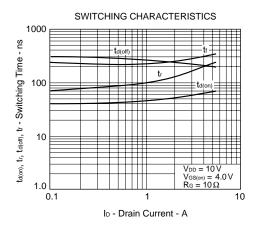
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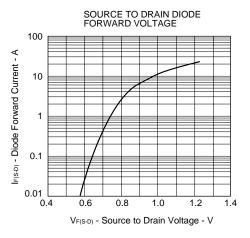
Downloaded from Elcodis.com electronic components distributor

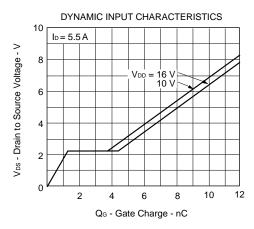


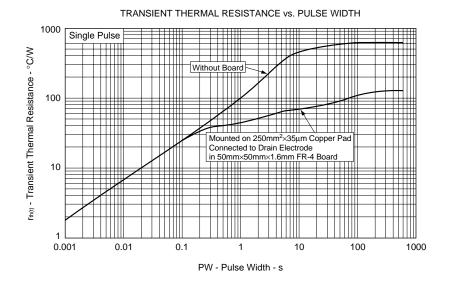
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