## MC12080

### 1.1 GHz Prescaler

## Description

The MC12080 is a single modulus divide by $10,20,40,80$ prescaler for low power frequency division of a 1.1 GHz high frequency input signal. Divide ratio control inputs SW1, SW2 and SW3 select the required divide ratio of $\div 10, \div 20, \div 40$, or $\div 80$.

An external load resistor is required to terminate the output. An $820 \Omega$ resistor is recommended to achieve a $1.2 \mathrm{~V}_{\mathrm{pp}}$ output swing, when dividing a 1.1 GHz input signal by the minimum divide by ratio of 10 , assuming a 8.0 pF load. Output current can be minimized dependent on conditions such as output frequency, capacitive load being driven, and output voltage swing required. Typical values for load resistors are included in the $\mathrm{V}_{\text {out }}$ specification for various divide ratios at 1.1 GHz input frequency.

## Features

- 1.1 GHz Toggle Frequency
- Supply Voltage 4.5 to 5.5 V
- Low Power 3.7 mA Typical at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$
- Operating Temperature Range of -40 to $85^{\circ} \mathrm{C}$
- Pb-Free Packages are Available

Table 1. FUNCTIONAL TABLE

| SW1 | SW2 | SW3 | Divide Ratio |
| :---: | :---: | :---: | :---: |
| L | L | L | 80 |
| L | L | H | 40 |
| L | H | L | 40 |
| L | H | H | 20 |
| H | L | L | 40 |
| H | L | H | 20 |
| H | H | L | 20 |
| H | H | H | 10 |

NOTE: SW1, SW2 and SW3: $\mathrm{H}=\mathrm{V}_{\mathrm{CC}}, \mathrm{L}=$ Open.
Table 2. MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Power Supply Voltage, Pin 2 | $\mathrm{V}_{\mathrm{CC}}$ | -0.5 to 7.0 | Vdc |
| Operating Temperature Range | $\mathrm{T}_{\mathrm{A}}$ | -40 to 85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\text {stg }}$ | -65 to 150 | ${ }^{\circ} \mathrm{C}$ |
| Maximum Output Current, Pin 4 | $\mathrm{I}_{\mathrm{O}}$ | 10 | mA |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.
NOTE: ESD data available upon request.

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PIN CONNECTIONS


## ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

## MC12080

Table 3. ELECTRICAL CHARACTERISTICS ( $\mathrm{V}_{\mathrm{CC}}=4.5$ to $5.5 \mathrm{~V} ; \mathrm{T}_{\mathrm{A}}=-40$ to $85^{\circ} \mathrm{C}$, unless otherwise noted.)

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Toggle Frequency (Sine Wave) | ft | 0.1 | 1.4 | 1.1 | GHz |
| Supply Current Output (Pin 2) | $\mathrm{I}_{\mathrm{CC}}$ | - | 3.7 | 5.0 | mA |
| Input Voltage Sensitivity 100 to 250 MHz 250 to 1100 MHz | $\mathrm{V}_{\text {in }}$ | $\begin{aligned} & 400 \\ & 100 \end{aligned}$ | - | $\begin{aligned} & 1000 \\ & 1000 \end{aligned}$ | mVpp |
| Divide Ratio Control Input High (SW1, SW2, SW3) | $\mathrm{V}_{\mathrm{IH}}$ | $\mathrm{V}_{\mathrm{CC}}-0.5 \mathrm{~V}$ | $\mathrm{V}_{\mathrm{CC}}$ | $\mathrm{V}_{\mathrm{CC}}+0.5 \mathrm{~V}$ | V |
| Divide Ratio Control Input Low (SW1, SW2, SW3) | $\mathrm{V}_{\text {IL }}$ | Open | Open | Open | - |
| $\begin{aligned} & \text { Output Voltage Swing (Note 1) } \\ & \qquad \begin{aligned} & R_{\mathrm{L}}=820 \Omega, \mathrm{I}_{\mathrm{O}}=4.0 \mathrm{~mA} \text { for } \div 10 \\ & \mathrm{R}_{\mathrm{L}}=1.6 \mathrm{k} \Omega, \mathrm{I}_{\mathrm{O}}=2.1 \mathrm{~mA} \text { for } \div 20 \\ & \mathrm{R}_{\mathrm{L}}=3.3 \mathrm{k} \Omega, \mathrm{I}_{\mathrm{O}}=1.1 \mathrm{~mA} \text { for } \div 40 \\ & \mathrm{R}_{\mathrm{L}}=6.2 \mathrm{k} \Omega, \mathrm{I}_{\mathrm{O}}=0.57 \mathrm{~mA} \text { for } \div 80 \end{aligned} \end{aligned}$ | $\mathrm{V}_{\text {out }}$ | 0.8 | 1.2 | - | $\mathrm{V}_{\mathrm{pp}}$ |

1. Assumes 8.0 pF load and 1.1 GHz input frequency (typical), $\mathrm{I}_{\mathrm{O}}$ at $\mathrm{V}_{\mathrm{CC}}=5.0 \mathrm{~V}$ and $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.


Figure 1. Logic Diagram


Figure 2. AC Test Circuit


Figure 3. Input Signal Amplitude versus Input Frequency


Figure 4. Output Amplitude versus Input Frequency
ORDERING INFORMATION

| Device | Package | Shipping $^{\dagger}$ |
| :--- | :---: | :---: |
| MC12080D | SOIC-8 | 98 Units / Rail |
| MC12080DG | SOIC-8 <br> (Pb-Free) | 98 Units / Rail |
| MC12080DR2 | SOIC-8 | 2500 / Tape \& Reel |
| MC12080DR2G | SOIC-8 <br> (Pb-Free) | $2500 /$ Tape \& Reel |

$\dagger$ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## PACKAGE DIMENSIONS

SOIC-8 NB
CASE 751-07
ISSUE AG
NOTES

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982
2. CONTROLLING DIMENSION: MILLIMETER
3. DIMENSION A AND B DO NOT INCLUDE
MOLD PROTRUSION.
4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT
MAXIMUM MATERIAL CONDITION.
6. 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

|  | MILLIMETERS |  | INCHES |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| DIM | MIN | MAX | MIN | MAX |  |
| A | 4.80 | 5.00 | 0.189 | 0.197 |  |
| B | 3.80 | 4.00 | 0.150 | 0.157 |  |
| C | 1.35 | 1.75 | 0.053 | 0.069 |  |
| D | 0.33 | 0.51 | 0.013 | 0.020 |  |
| G | 1.27 |  | BSC | 0.050 BSC |  |
| H | 0.10 | 0.25 | 0.004 | 0.010 |  |
| J | 0.19 | 0.25 | 0.007 | 0.010 |  |
| K | 0.40 | 1.27 | 0.016 | 0.050 |  |
| M | 0 | $\circ$ | $8 \circ$ | 0 |  |
| $\circ$ | 8 | 8 |  |  |  |
| N | 0.25 | 0.50 | 0.010 | 0.020 |  |
| S | 5.80 | 6.20 | 0.228 | 0.244 |  |

SOLDERING FOOTPRINT*

 details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.


#### Abstract

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