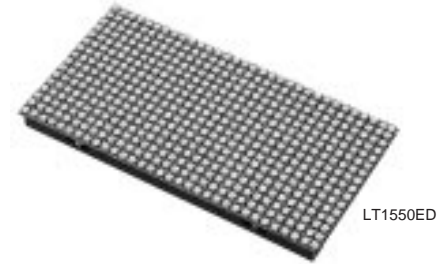


Dot Matrix LED Unit for Indoor Use LT1550ED(Chip Type)

■ Features

- No. of dots : 16X32dots
- Outline dimensions : 64.0X128.0mm
- Dot size : 3.0X3.0mm
- Dot pitch : 4.0mm
- Radiation color : Yellow-green+Red(dichromatic type)
- Driving method : 1/16 duty dynamic drive



■ Absolute Maximum Ratings

(Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply voltage for IC	V _{CC}	-0.3 to +5.5	V
Supply voltage for LED	V _{LED}	-0.3 to +4.5	V
Input voltage	V _I	-0.3 to V _{CC} +0.3	V
Turn-on time	t _{ON}	1	ms
Operating temperature	T _{opr}	-10 to +60*1	°C
Storage temperature	T _{stg}	-20 to +70	°C
Power dissipation	P	20	W

*1 25% or less of lighting

■ Optical Characteristics

(V_{CC}=5V, V_{LED}=4V, Ta=25°C)

Parameter	Symbol	TYP.	Unit
Luminance	Red	120	cd/m ²
	Yellow-green	180	
Viewing angle	2θ ^{1/2}	120	°
Peak emission wavelength	Red	635	nm
	Yellow-green	565	

■ Terminal Functions

Connector	Symbol	Function
Power supply (CN1)	V _{LED}	Supply voltage for LED (+4V)
	V _{CC}	Supply voltage for IC (+5V)
	GND	Ground
Input signal (CN2)	A0 to A3	Address specification signal for row driver
	RDATA	Serial data input for red (H=ON, L=OFF)
	GDATA	Serial data input for yellow-green (H=ON, L=OFF)
	LATCH	Latch signal of display data. H: Serial data is converted to parallel data. L: Contents are latched.
	ENABLE	Controls ON/OFF of LED (H: LED OFF)
	CLOCK	Clock signal for data transmission in the shift-register. (L→H: serial data is shifted.)
	GND	Ground for signal
Output signal (CN3)	A0 to A3	Buffered input signal
	RDATA	Input signal generated through 32-bit shift register or buffer
	GDATA	Input signal generated through 32-bit shift register or buffer
	LATCH	Buffered input signal
	ENABLE	Buffered input signal
	CLOCK	Buffered input signal
	GND	Ground for signal

Each signal is used as input signal for next unit.

* As for the terminal number, refer to the outline dimensions.

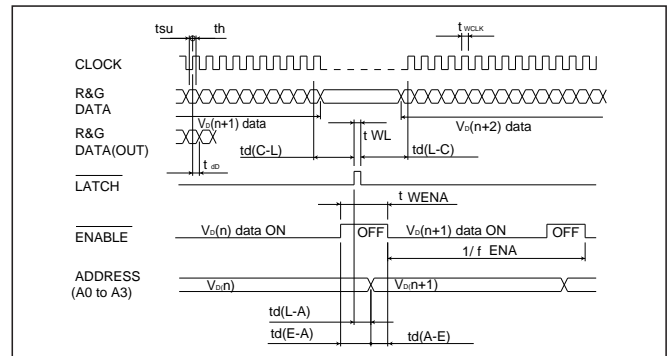
■ Electrical Characteristics

(V_{CC}=5V, V_{LED}=4V, Ta=25°C)

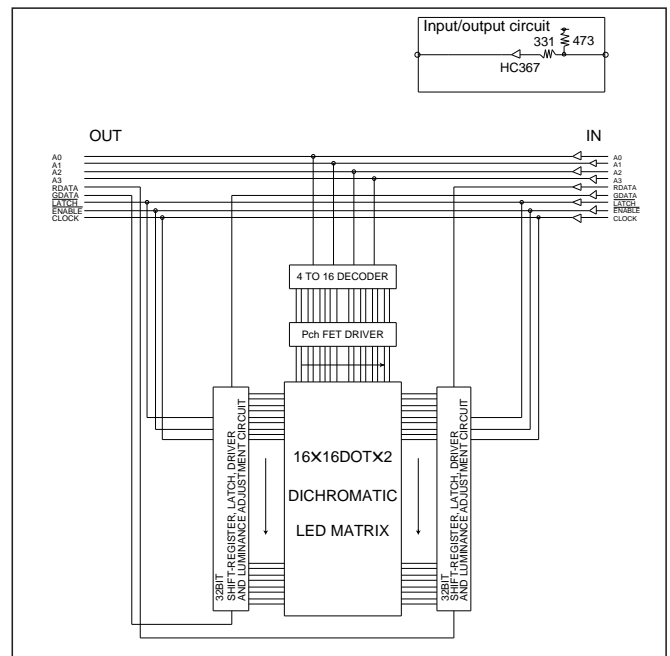
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply voltage for IC	V _{CC}	4.75	5.0	5.25	V
Supply voltage for LED	V _{LED}	3.75	4.0	4.25	V
IC current dissipation*1	I _{CC}	—	140	200	mA
LED current dissipation*1	I _{LED}	—	3.8	4.2	A
Input voltage	V _{IH}	3.5	—	—	V
	V _{IL}	—	—	1.5	V
Input current	I _{IH}	—	—	0.1	μA
	I _{IL}	—	—	0.12	mA
Clock frequency	f _{CLK}	—	—	10	MHz
Frame frequency	f _{FR}	70	250	1 000	Hz

*1 Under the condition that dichromatic all dots are lit.

■ Timing Chart

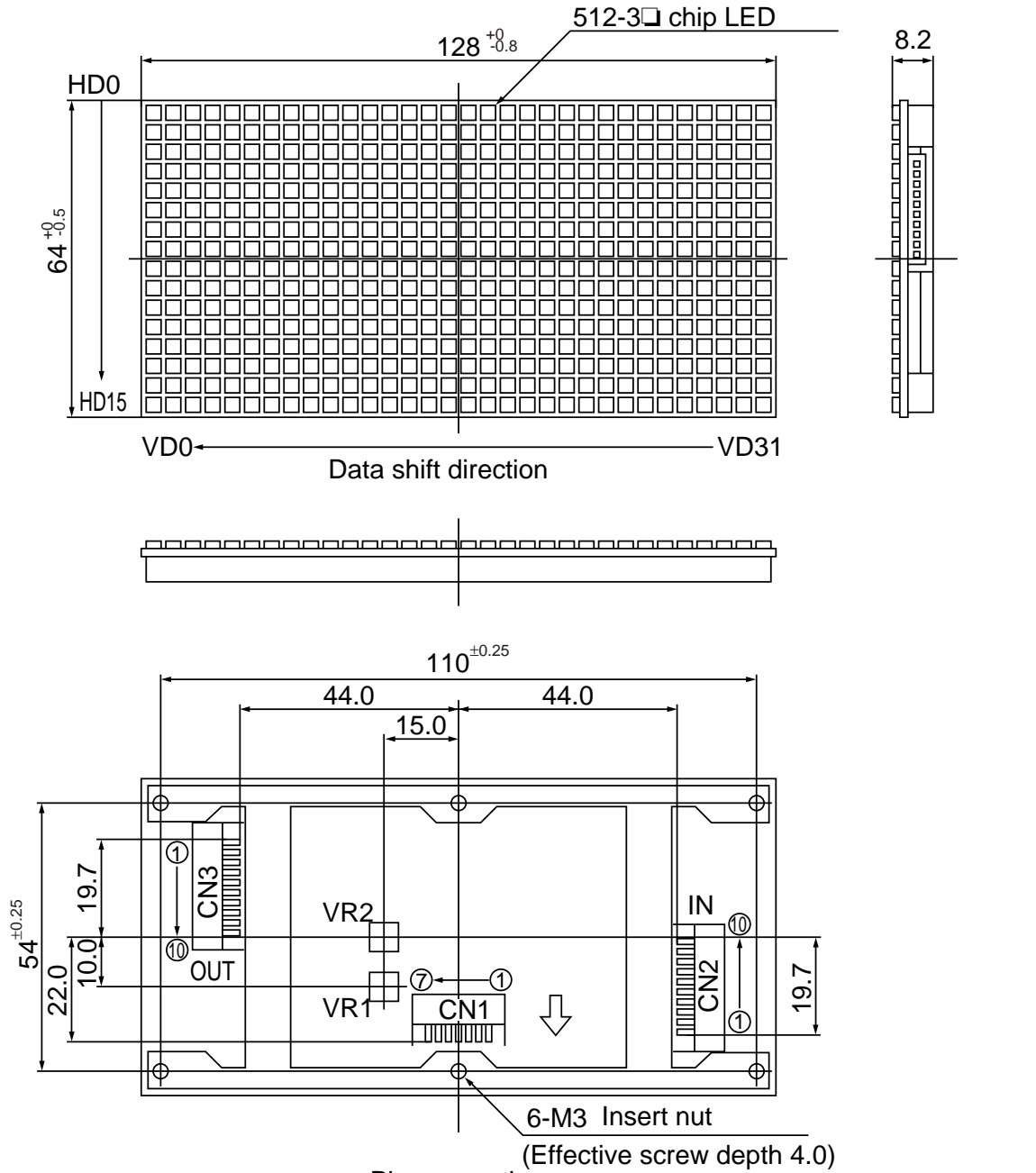


■ Block Diagram



(Notice) ● In the absence of confirmation by device specification sheets, SHARP takes no responsibility for any defects that may occur in equipment using any SHARP devices shown in catalogs, data books, etc. Contact SHARP in order to obtain the latest device specification sheets before using any SHARP device.
 (Internet) ● Data for sharp's optoelectronic/power device is provided for internet.(Address <http://www.sharp.co.jp/ecg/>)

4



Pin connection

CN1(Power supply)	
Pin No.	Name
1	VLED
2	VLED
3	VLED
4	VCC
5	GND
6	GND
7	GND

CN2(Input signal)	
Pin No.	Name
1	A0
2	A1
3	A2
4	A3
5	RDATA
6	GDATA
7	LATCH
8	ENABLE
9	CLOCK
10	GND

CN3(Output signal)	
Pin No.	Name
1	A0
2	A1
3	A2
4	A3
5	RDATA
6	GDATA
7	LATCH
8	ENABLE
9	CLOCK
10	GND