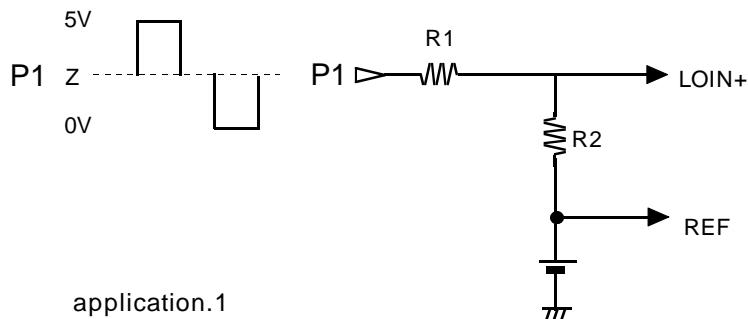


application.1 : One port H/Z/L control)

Logic control P1	Situation of loading channel	Output Voltage swing
5V	Forward rotation	$V_o = \frac{8(5[V] - REF[V])}{XR2/(R1+R2)}$
Z (Hi impedance)	Short brake --> Stop	$V_o = 0 [V]$
0	Reverse rotation	$V_o = \frac{-8(5[V] - REF[V])}{XR2/(R1+R2)}$



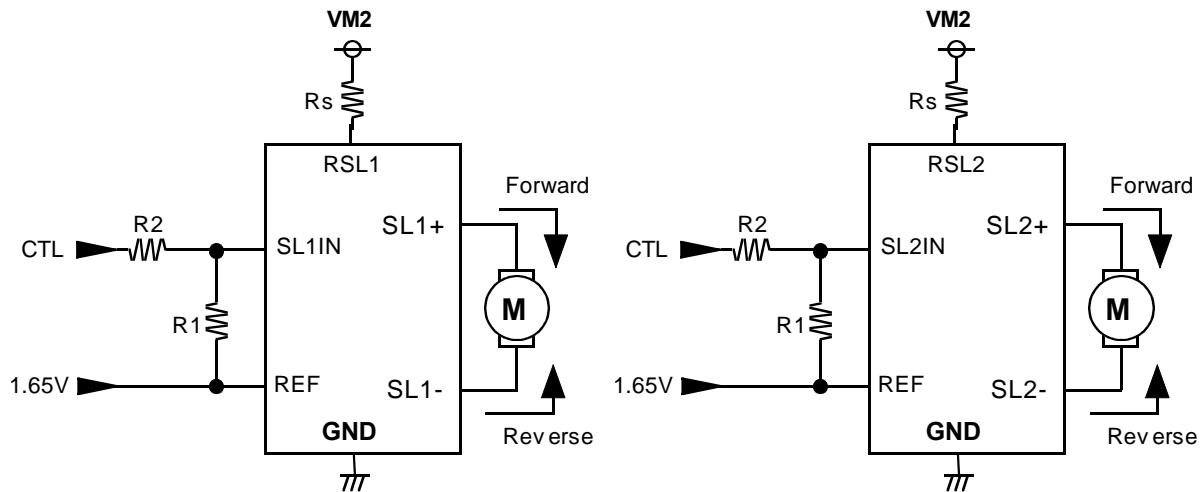
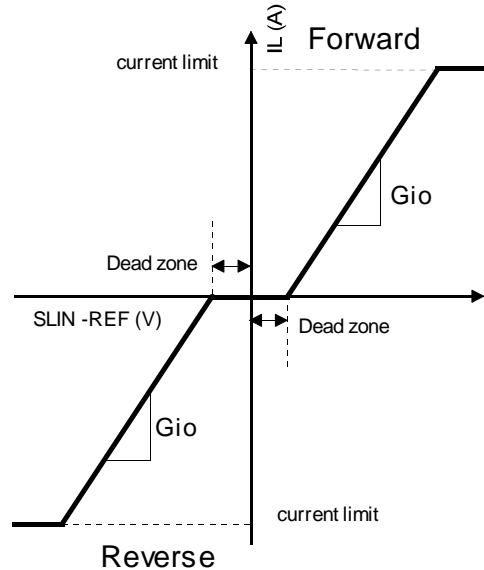
application.1
(One port H/Z/L control)

OSLIDE channel

The relationship between the differential Voltage between SLIN and REF and the torque is shown in right Figure. The Voltage gain[GVo] is 1.0 [V/V]. The current gain is 2.0[A/V] (at sensing resistor : 0.5 ohm and R1=∞,R2=0ohm) in forward torque directions, and the dead zone is from 0mV to 80mV (at R1=∞,R2=0ohm)).

The coil current gain under the reverse torque is the same with in forward torque directions. And the limitation function gets on when the differential Voltage of VM2(12V) ~ RSL is 0.5V.

Therefore current-gain-control and current-limit of this IC is determined with sensing resistor value.



The example of current-gain and current-limit of SLIDE.

Rs [ohm]	Ilim [A]	Gio* [A/V]		
		R1=∞ R2=0 ohm	R1=R2	R2=2XR1
0.50	1.00	2.00	1.00	0.66
0.75	0.66	1.33	0.66	0.44
1.00	0.50	1.00	0.50	0.33

$$Gio^* = R1 / [(R1+R2) \times Rs] \quad [A/V]$$

OFOCUS / TRACKING channel

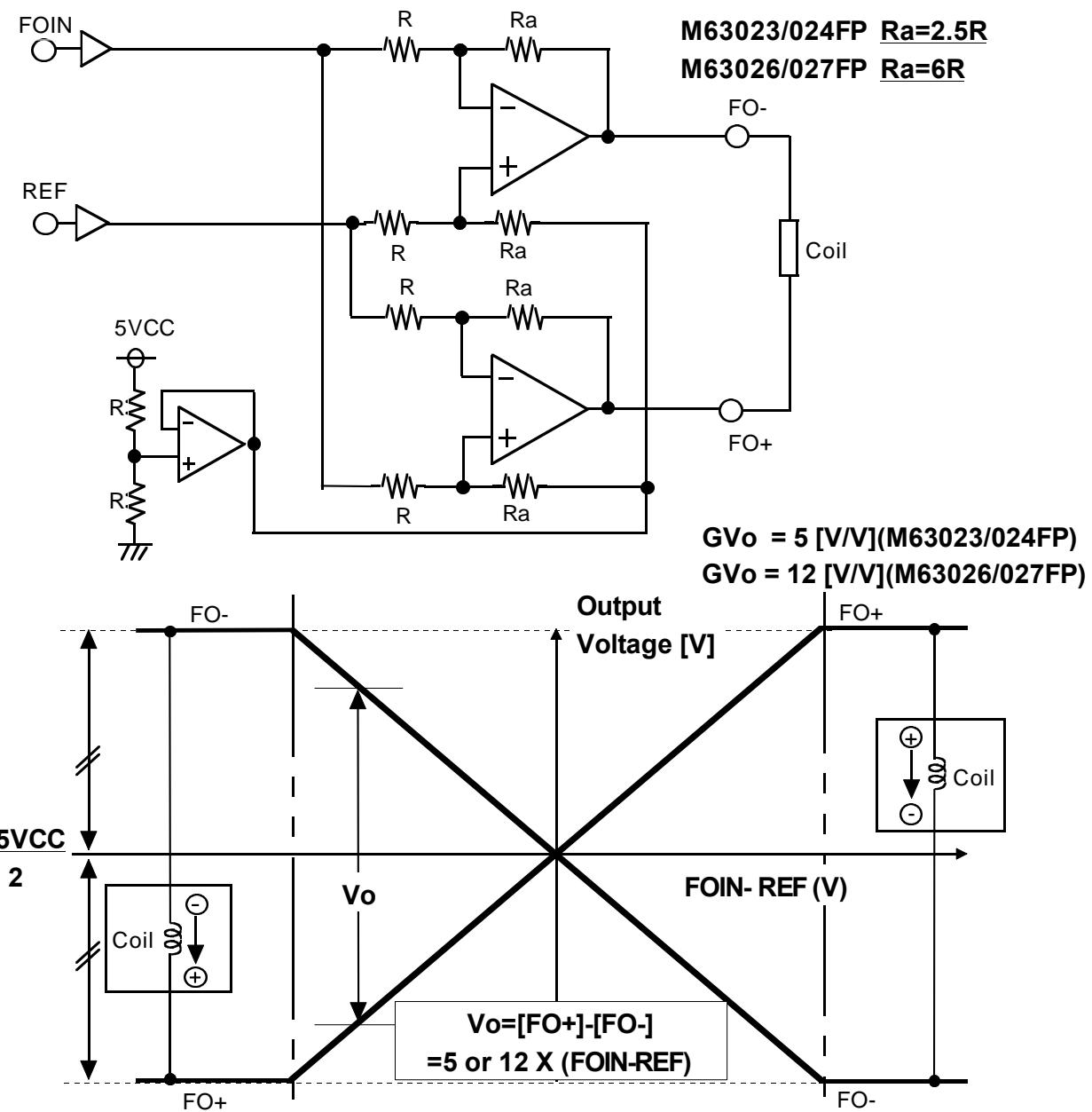
The focus and tracking channel is the Voltage control drive using BTL .

The focus and tracking is the same composition.

The relationship between the differential Voltage between FOIN and REF and the output Voltage is shown in below Figure.

The Voltage gain [GVo] is 5.0 [V/V].(M63023/024FP)

The Voltage gain [GVo] is 12.0 [V/V].(M63026/027FP)



ODirect PWM operation

The spindle and the slide channel is controlled by the direct PWM control.
Analog input voltage control the driving current which is in proportion to input voltage.

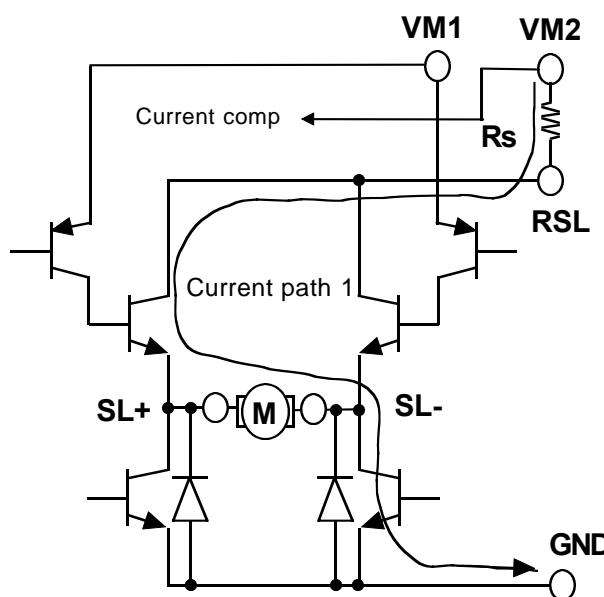
This control is direct PWM control type of motor current chopper.

Also,built-in the current limit circuit. This IC controls the motor current directly.
direct PWM operation as follows;

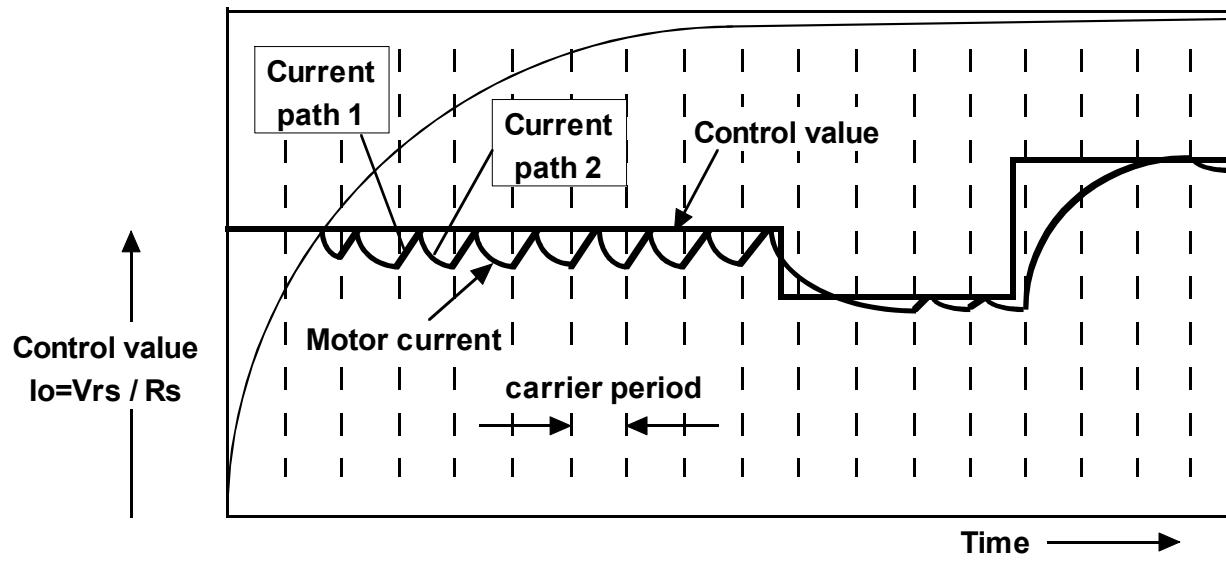
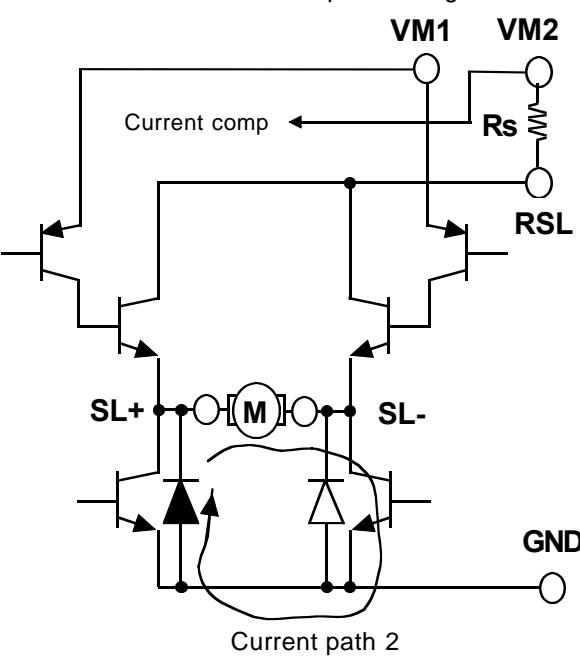
- 1) The current which flows on a motor is detected, and current is supplied from a power supply until it reaches the predetermined instruction value to which the current is proportional to input voltage.
- 2) When current reaches an instruction value, an output transistor is changed and the period coil inertia energy to a career cycle is made to regeneration using an internal path.
- 3) After repeat 1) and 2).

Therefore, the IC constantly surveillance and control the current value itself use sensor resistor.
Moreover, these ICs built-in current limit circuit so that protect to large current.
Thus, if input excessive control voltage, the current don't flow that settle limit current.

FORWARD Current path timing 1.



FORWARD Current path timing 2.



○ PWM carrier frequency setting

PWM carrier frequency is decided by charging and discharging the capacitor that is connected to OSC terminal outer IC. Examination of the relationship the capacitor connected to OSC terminal and PWM carrier frequency is given in following table.

Capacitor [pF]	820	750	330	220	180	130	110
Carrier Frequency [KHz]	28	30	65	90	110	140	160

*note) This PWM carrier frequency is TYP value.

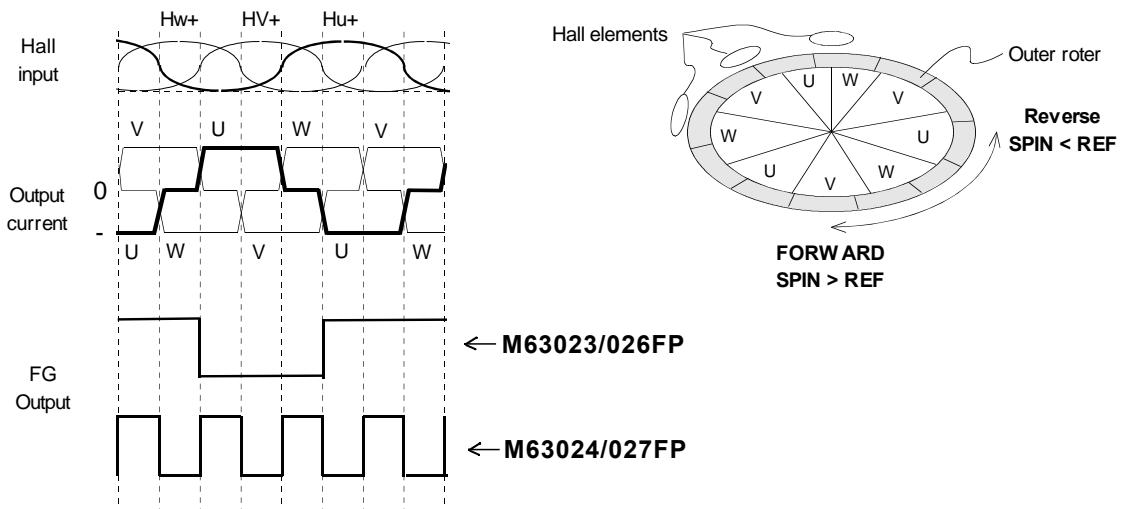
○ Recommendation of SHORT BRAKE MODE at SPINDLE drive

This IC has two brake mode, PWM-BRAKE-MODE and SHORT-BRAKE-MODE. In this IC recommendation, SHORT-BRAKE-MODE is superior to PWM-BRAKE-MODE to reducing the power dissipation and to avoid braking down of this IC.

(By excessive reverse torque current in braking a motor with PWM-BRAKE from high-speed-rotation with being excessive Back-EMF, this IC could be broken.)

○ The relationship between hall-amplifier-input and output-current-commutation/FG output at SPINDLE drive

The relationship between the hall elements and the motor output current/FG output is shown in bellow Figure.



○FG function at SPINDLE drive

The FG terminal outputs the square pulse signal synchronizing with the Hall inputs timing.
And, the FG terminal is open-collector output.

○Phase delay circuit at SLIDE

Phase delay circuit is built in the IC to detect an output spike current, when the motor current direction is switching.

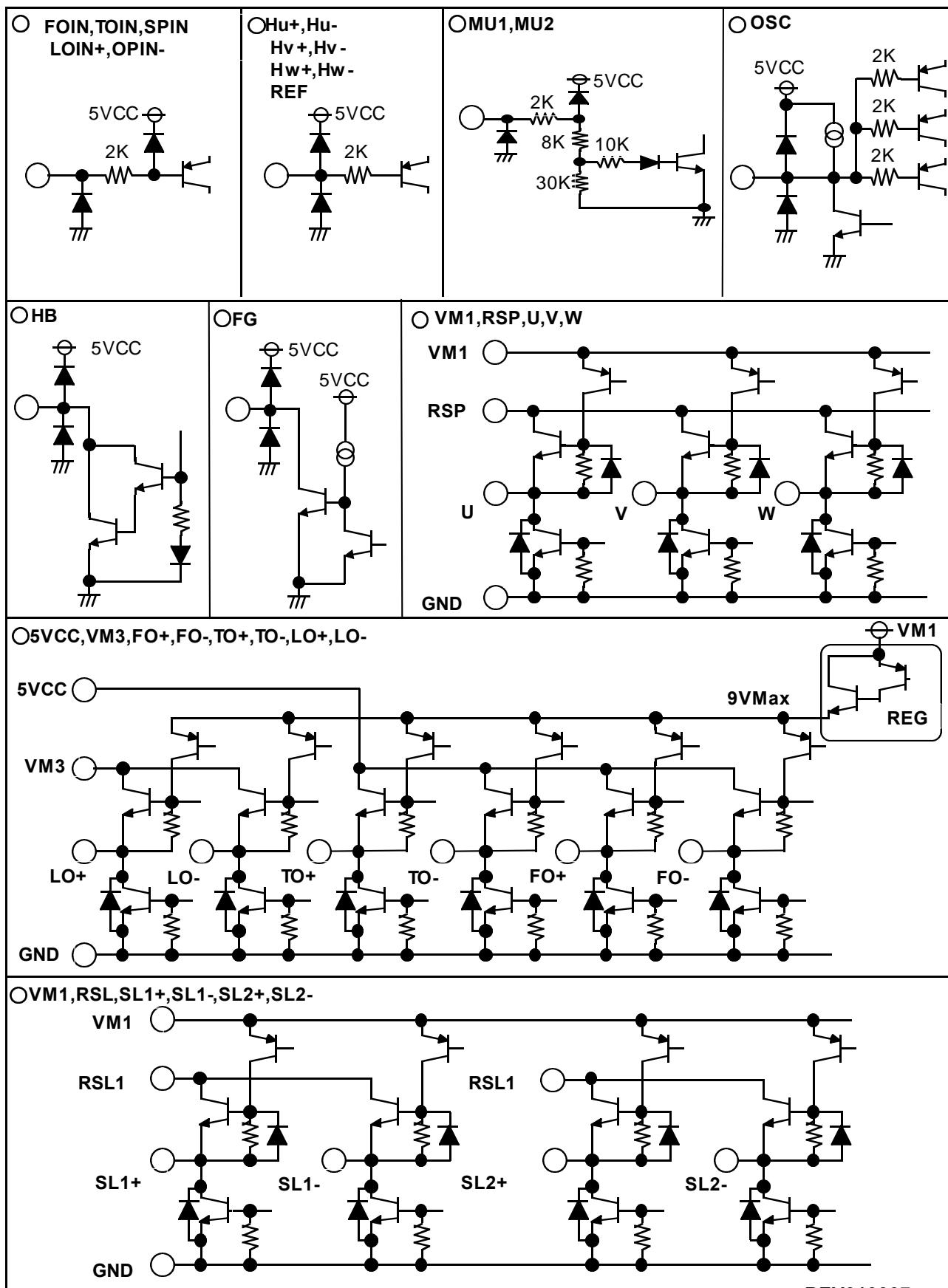
In switching the motor current direction, Phase delay circuit switch-off all output transistor of H-bridge for 3usec.

○Output current setting at SLIDE

In this IC, since output transistor is NPN-type transistor, motor coil current (I_o) is larger than sensing resistance current about 10mA (TYP.) according to base current of output transistor.
Therefore please design output current with consisting these base current.

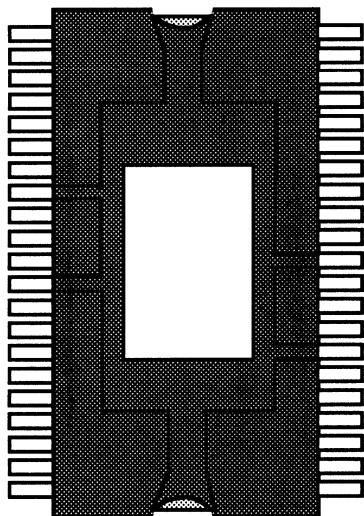
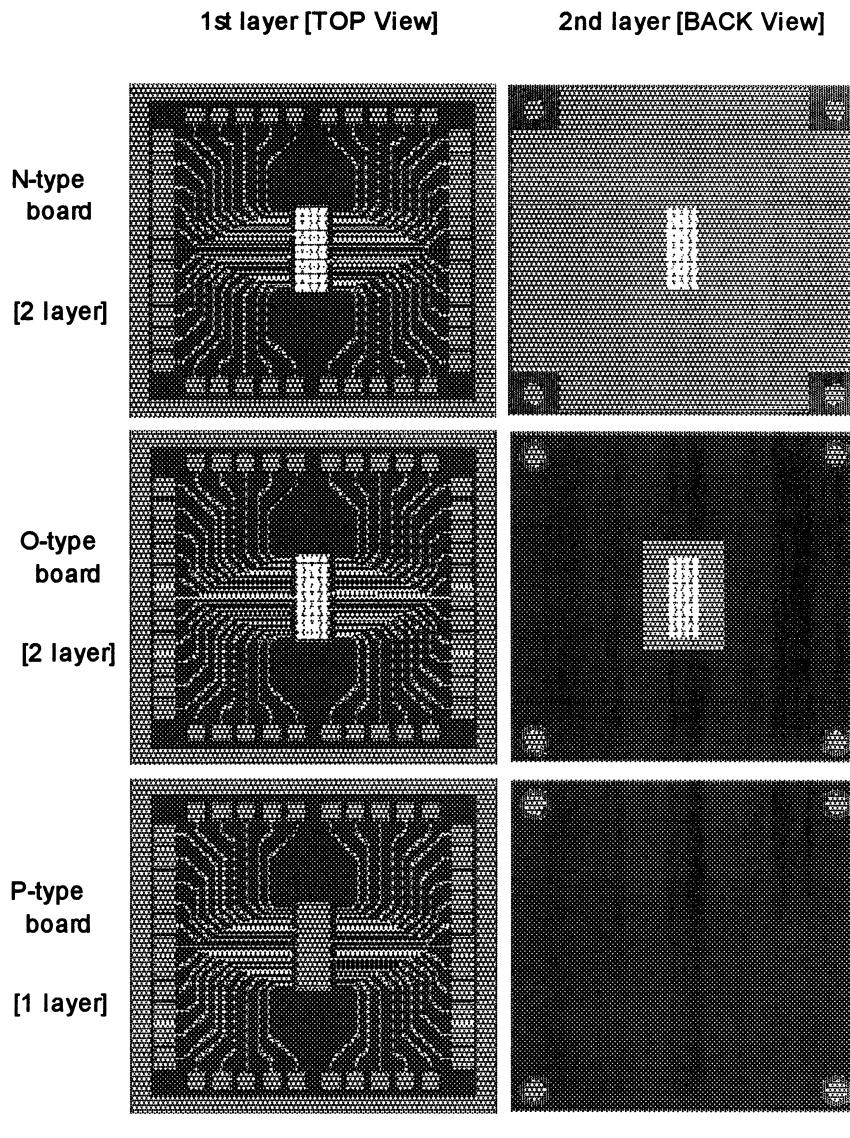
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SPINDLE MOTOR AND 5CH ACTUATOR driveR

<I/O circuit>

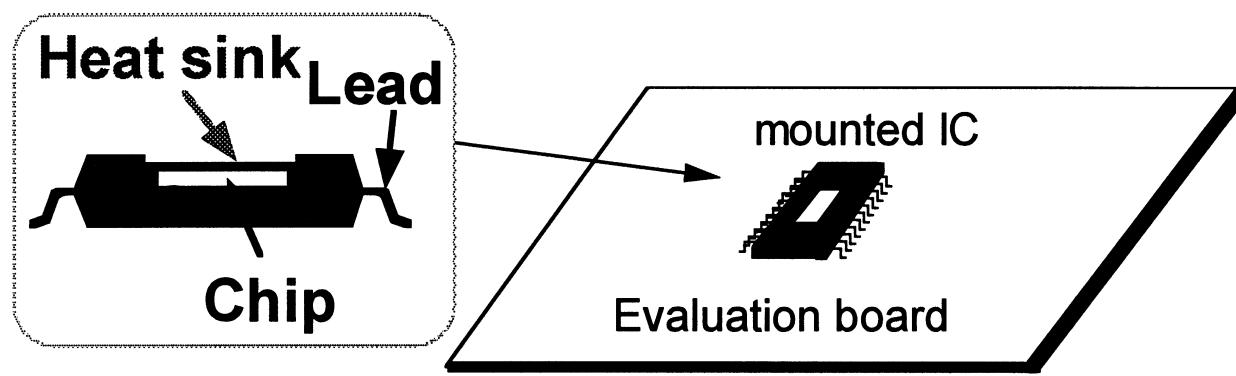


[The boards for thermal derating evaluation]

Board material
Glass - epoxy FR4
Board Size
70 X 70mm
Board thickness :T=1.6mm
1 and 2 layers material : copper thickness :T=18um

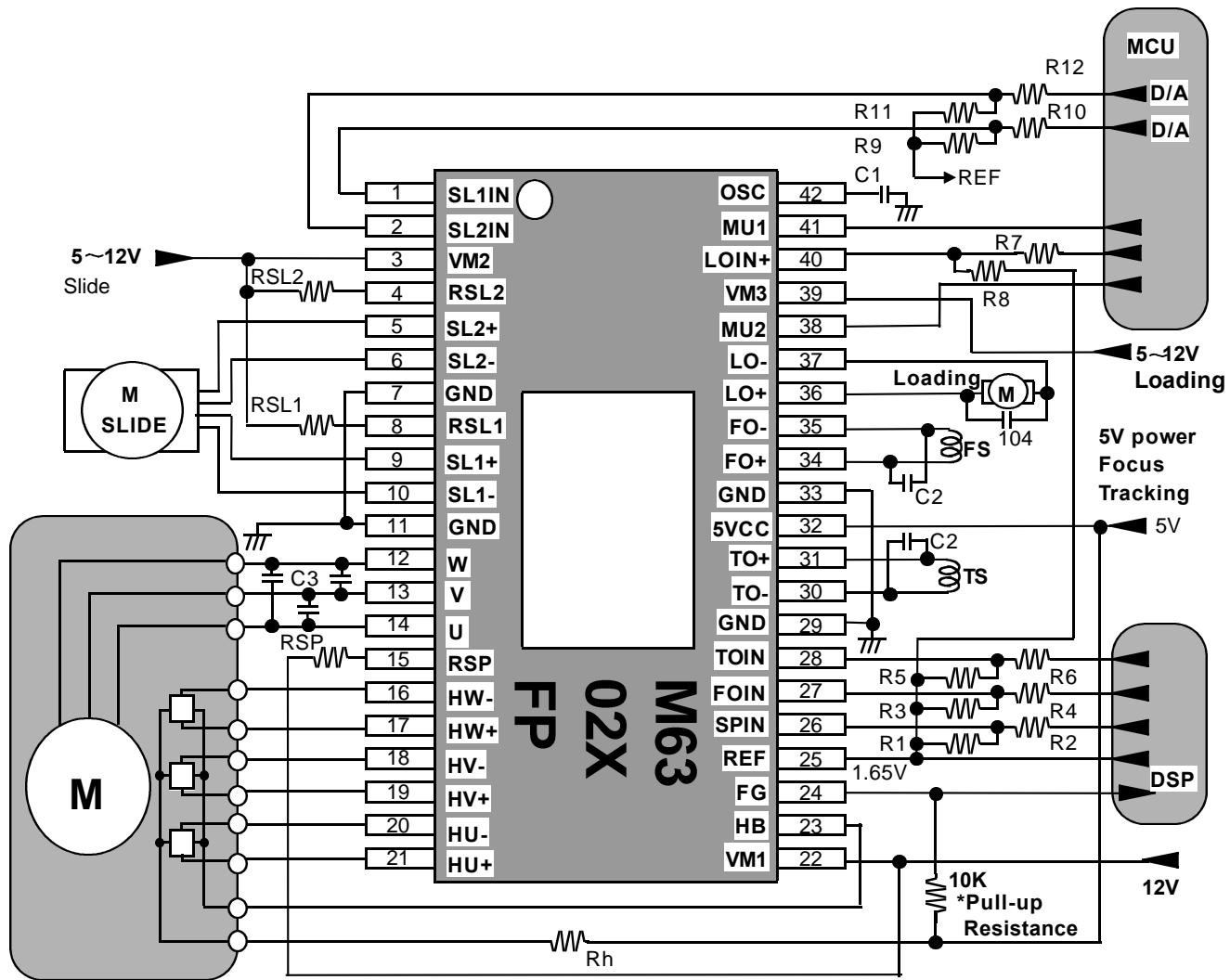


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[APPLICATION CIRCUIT no.1]



[An example of the values of the external parts]

*These values are only example, not the guaranteed values. And the values differ in each application.

External Parts Name	Typ.value	UNIT	note
RSP	0.33	ohm	Ilim1F=1.5A, Ilim1R=1.0A, Gain=3.0A/V
RSL1,RSL2	2	ohm	Ilim=0.25A, Gain=0.5A/V
Rh	200	ohm	
R1, R2, R3, R4, R5, R6	10K	ohm	The Resistor are not necessary in some application.
R7, R8	10K	ohm	The Resistor are not necessary in some application.
C1	330p	F	Fosc=65KHz
R9, R10, R11, R12	10K	ohm	The Resistor are not necessary in some application.
C2	10n	F	
C3	6.8n	F	

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www.datasheetcatalog.com

Datasheets for electronics components.