## DATA SHEET

## SAA1064

4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

Product specification
File under Integrated Circuits, IC01

## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

## GENERAL DESCRIPTION

The LED-driver is a bipolar integrated circuit made in an $I^{2} \mathrm{~L}$ compatible 18 volts process. The circuit is especially designed to drive four 7 -segment LED displays with decimal point by means of multiplexing between two pairs
 of digits. It features an $\mathrm{I}^{2} \mathrm{C}$-Bus slave transceiver interface with the possibility to program four different SLAVE ADDRESSES, a POWER RESET flag, 16 current sink OUTPUTS, controllable by software up to 21 mA , two multiplex drive outputs for common anode segments, an on-chip multiplex oscillator, control bits to select static, dynamic and blank mode, and one bit for segment test.

## QUICK REFERENCE DATA

| PARAMETER | CONDITIONS | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage | $\mathrm{V}_{\mathrm{EE}}=0 \mathrm{~V}$ | $\mathrm{V}_{\mathrm{CC}}$ | 4.5 | 5 | 15 | V |
| Supply current all outputs OFF | $\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}$ | $\mathrm{ICC}^{(1)}$ | 7 | 9.5 | 14 | mA |
| Total power dissipation |  |  |  |  |  |  |
| 24-lead DIL (SOT101B) |  | $P_{\text {tot }}$ | - | - | 1000 | mW |
| 24-lead DIL SO (SOT137A) |  | $P_{\text {tot }}$ | - | - | 500 | mW |
| Operating ambient temperature range |  | $\mathrm{T}_{\text {amb }}$ | -40 | - | +85 | ${ }^{\circ} \mathrm{C}$ |

## Note

1. The positive current is defined as the conventional current flow into a device (sink current).

## PACKAGE OUTLINE

SAA1064: 24-lead DIL; plastic with internal heat spreader (SOT101B); SOT101-1; 1996 August 30.
SAA1064T: 24-lead mini-pack; plastic (SO-24; SOT137A); SOT137-1; 1996 August 30.


## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

PINNING

| SYMBOL | PIN | DESCRIPTION |
| :--- | :--- | :--- |
| ADR | 1 | I $^{2}$ C-Bus slave address input |
| $C_{\text {EXT }}$ | 2 | external control |
| P8 to P1 | $3-10$ | segment output |
| MX1 | 11 | multiplex output |
| $\mathrm{V}_{\text {EE }}$ | 12 | ground |
| $\mathrm{V}_{\mathrm{CC}}$ | 13 | positive supply |
| MX2 | 14 | multiplex output |
| P9 to P16 | $15-22$ | segment output |
| SDA | 23 | I $^{2} \mathrm{C}-$ Bus serial data line |
| SCL | 24 | I $^{2} \mathrm{C}-$-Bus serial clock line |



Fig. 2 Pinning diagram.

## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

## FUNCTIONAL DESCRIPTION

| S | 0 | 1 | 1 | 1 | 0 |  | A1 | AO | 1 |  | A | PR | 0 | 0 | 0 | 0 | 0 | 0 |  | 0 | 0 | 1 | P |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  |  | ve | add | dre |  |  |  |  |  |  |  |  | tus |  |  |  |  |  |  |  |

a. READ mode.

b. WRITE mode.

| $\mathrm{S}=$ start condition | A1, A0 | $=$ programmable address bits |
| :--- | :--- | :--- |
| $\mathrm{P}=$ stop condition | $\mathrm{SC} \mathrm{SB} \mathrm{SA}=$ subaddress bits |  |
| $\mathrm{A}=$ acknowledge | C 6 to CO | $=$ control bits |
| $\mathrm{X}=$ don't care | PR | $=$ POWER RESET flag |

Fig. $3 \mathrm{I}^{2} \mathrm{C}$-Bus format.

## Address pin ADR

Four different slave addresses can be chosen by connecting ADR either to $\mathrm{V}_{\mathrm{EE}}, 3 / 8 \mathrm{~V}_{\mathrm{CC}}, 5 / 8 \mathrm{~V}_{\mathrm{CC}}$ or $\mathrm{V}_{\mathrm{CC}}$. This results in the corresponding valid addresses HEX 70, 72, 74 and 76 for writing and $71,73,75$ and 77 for reading. All other addresses cannot be acknowledged by the circuit.

## Status byte

Only one bit is present in the status byte, the POWER RESET flag. A logic 1 indicates the occurence of a power failure since the last time it was read out. After completion of the READ action this flag will be set to logic 0 .

## Subaddressing

The bits SC, SB and SA form a pointer and determine to which register the data byte following the instruction byte will be written. All other bytes will then be stored in the registers with consecutive subaddresses. This feature is called Auto-Increment ( Al ) of the subaddress and enables a quick initialization by the master.
The subaddress pointer will wrap around from 7 to 0 .
The subaddresses are given as follows:

| SC | SB | SA | SUB-ADDRESS | FUNCTION |
| :---: | :---: | :---: | :---: | :--- |
| 0 | 0 | 0 | 00 | control register |
| 0 | 0 | 1 | 01 | digit 1 |
| 0 | 1 | 0 | 02 | digit 2 |
| 0 | 1 | 1 | 03 | digit 3 |
| 1 | 0 | 0 | 04 | digit 4 |
| 1 | 0 | 1 | 05 | reserved, not used |
| 1 | 1 | 0 | 06 | reserved, not used |
| 1 | 1 | 1 | 07 | reserved, not used |

## Control bits (see Fig.4)

The control bits C 0 to C 6 have the following meaning:

| $C 0=0$ | static mode, i.e. continuous display of digits 1 and 2 |
| :--- | :--- |
| $C 0=1$ | dynamic mode, i.e. alternating display of digit $1+3$ and $2+4$ |
| $C 1=0 / 1$ | digits $1+3$ are blanked/not blanked |
| $C 2=0 / 1$ | digits $2+4$ are blanked/not blanked |
| $C 3=1$ | all segment outputs are switched-on for segment test ${ }^{(1)}$ |
| $C 4=1$ | adds 3 mA to segment output current |
| $C 5=1$ | adds 6 mA to segment output current |
| $C 6=1$ | adds 12 mA to segment output current |

## Note

1. At a current determined by $\mathrm{C} 4, \mathrm{C} 5$ and C 6 .

## Data

A segment is switched ON if the corresponding data bit is logic 1. Data bits D17 to D10 correspond with digit 1, D27 to D20 with digit 2, D37 to D30 with digit 3 and D47 to D40 with digit 4.

The MSBs correspond with the outputs P8 and P16, the LSBs with P1 and P9. Digit numbers 1 to 4 are equal to their subaddresses (hex) 1 to 4.

## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

## SDA, SCL

The SDA and SCL I/O meet the $I^{2} \mathrm{C}$-Bus specification. For protection against positive voltage pulses on these inputs voltage regulator diodes are connected to $\mathrm{V}_{\mathrm{EE}}$. This means that normal line voltage should not exceed 5,5 volt. Data will be latched on the positive-going edge of the acknowledge related clock pulse.

## Power-on reset

The power-on reset signal is generated internally and sets all bits to zero, resulting in a completely blanked display. Only the POWER RESET flag is set.

## External Control ( $\mathrm{C}_{\mathrm{EXT}}$ )

With a capacitor connected to pin 2 the multiplex frequency can be set (see Fig.5). When static this pin can be connected to $\mathrm{V}_{\mathrm{EE}}$ or $\mathrm{V}_{\mathrm{CC}}$ or left floating since the oscillator will be switched off.

## Segment outputs

The segment outputs P1 to P16 are controllable current-sink sources. They are switched on by the corresponding data bits and their current is adjusted by control bits C4, C5 and C6.

## Multiplex outputs

The multiplex outputs MX1 and MX2 are switched alternately in dynamic mode with a frequency derived from the clock-oscillator. In static mode MX1 is switched on. The outputs consist of an emitter-follower, which can be used to drive the common anodes of two displays directly provided that the total power dissipation of the circuit is not exceeded. If this occurs external transistors should be connected to pins 11 and 14 as shown in Fig.5.

## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

SAA1064

## RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

| PARAMETER | CONDITIONS | SYMBOL | MIN. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply voltage (pin 13) | $\mathrm{V}_{\mathrm{EE}}=0 \mathrm{~V}$ | $\mathrm{V}_{\mathrm{CC}}$ | -0.5 | 18 | V |
| Supply current (pin 13) |  | $\mathrm{I}_{\mathrm{CC}}$ | -50 | 200 | mA |
| Total power dissipation |  |  |  |  |  |
| 24-lead DIL (SOT101B) |  | $\mathrm{P}_{\text {tot }}$ |  | 1000 | mW |
| 24-lead SO (SO137A) |  | $P_{\text {tot }}$ |  | 500 | mW |
| SDA, SCL voltages |  | $\mathrm{V}_{\mathrm{EE}}=0 \mathrm{~V}$ | $\mathrm{V}_{23,24}$ | -0.5 | 5.9 | V |
| Voltages ADR-MX1 and MX2-P16 | $\mathrm{V}_{\mathrm{EE}}=0 \mathrm{~V}$ | $\mathrm{V}_{1-11}, \mathrm{~V}_{14-22}$ | -0.5 | $\mathrm{V}_{\mathrm{CC}}+0.5$ | V |
| Input/output current all pins | outputs OFF | $\pm \mathrm{I}_{1 / \mathrm{O}}$ | - | 10 | mA |
| Operating ambient |  |  |  |  |  |
| temperature range |  | $\mathrm{T}_{\text {amb }}$ | -40 | +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature range |  | $\mathrm{T}_{\text {stg }}$ | -55 | +150 | ${ }^{\circ} \mathrm{C}$ |

## THERMAL RESISTANCE

| From crystal to ambient |  |  |
| :--- | :--- | :--- |
| 24-lead DIL $R_{\text {th } j-a}$ $35 \mathrm{~K} / \mathrm{W}$ <br> 24-lead SO (on ceramic substrate) $\mathrm{R}_{\text {th j-a }}$ $75 \mathrm{~K} / \mathrm{W}$ <br> 24-lead SO (on printed circuit board) $\mathrm{R}_{\text {th } j-a}$ $105 \mathrm{~K} / \mathrm{W}$ |  |  |

## CHARACTERISTICS

$\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V} ; \mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$; voltages are referenced to ground ( $\mathrm{V}_{\mathrm{EE}}=0 \mathrm{~V}$ ); unless otherwise specified


## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

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| PARAMETER | CONDITIONS | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Segment outputs |  |  |  |  |  |  |
| (P8 to P1; pins 3 to 10) |  |  |  |  |  |  |
| P9 to P16; pins 15 to 22) |  |  |  |  |  |  |
| Output voltages | $\mathrm{l}_{0}=15 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{O}}$ | - | - | 0.5 | V |
| Output leakage current HIGH | $\mathrm{V}_{\mathrm{O}}=\mathrm{V}_{\mathrm{CC}}=15 \mathrm{~V}$ | lo | - | - | $\pm 10$ | $\mu \mathrm{A}$ |
| Output current LOW |  |  |  |  |  |  |
| All control bits (C4, C5 |  |  |  |  |  |  |
| and C6) are HIGH | V OL $=5 \mathrm{~V}$ | IoL | 17.85 | 21 | 25.2 | mA |
| Contribution of: |  |  |  |  |  |  |
| control bit C4 |  | 10 | 2.55 | 3.0 | 3.6 | mA |
| control bit C5 |  | $\mathrm{I}_{0}$ | 5.1 | 6.0 | 7.2 | mA |
| control bit C6 |  | l | 10.2 | 12.0 | 14.4 | mA |
| Relative segment output current accuracy |  |  |  |  |  |  |
| with respect to highest value |  | $\Delta \mathrm{l}_{0}$ | - | - | 7.5 | \% |
| Multiplex 1 and 2 (pins 11 and 14) |  |  |  |  |  |  |
| Maximum output voltage (when ON) | $-\mathrm{I}_{\mathrm{MPX}}=50 \mathrm{~mA}$ | $\mathrm{V}_{\text {MPX }}$ | $\mathrm{V}_{\mathrm{CC}}-1.5$ | - | - | V |
| Maximum output current HIGH (when ON) | $\mathrm{V}_{\mathrm{MPX}}=2 \mathrm{~V}$ | $-I_{\text {MPX }}$ | 50 | - | 110 | mA |
| Maximum output current LOW (when OFF) | $\mathrm{V}_{\mathrm{O}}=2 \mathrm{~V}$ | $+_{\text {MPX }}$ | 50 | 70 | 110 | $\mu \mathrm{A}$ |
| Multiplex output period | $\mathrm{C}_{\text {EXT }}=2.7 \mathrm{nF}$ | $\mathrm{T}_{\text {MPX }}$ | 5 | - | 10 | ms |
| Multiplexed duty factor |  |  | - | 48.4 | - | \% |

* Value to be fixed.

4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface


## APPLICATION INFORMATION



Fig. 5 Dynamic mode application diagram.


Fig. 6 Static mode application diagram.

## 4-digit LED-driver with $\mathrm{I}^{2} \mathrm{C}$-Bus interface

## POWER DISSIPATION

The total maximum power dissipation of the SAA1064 is made up by the following parts:

1. Maximum dissipation when none of the outputs are programmed (continuous line in Fig.7).
2. Maximum dissipation of each programmed output. The dashed line in Fig. 7 visualises the dissipation when all the segments are programmed (max. 16 in the static, and max. 32 in the dynamic mode). When less segments are programmed one should take a proportional part of the maximum value.
3. Maximum dissipation of the programmed segment drivers which can be expressed as:
$\mathrm{P}_{\text {add }}=\mathrm{V}_{\mathrm{O}} \times \mathrm{I}_{\mathrm{O}} \times \mathrm{N}$.

Where: $\quad P_{\text {add }}=$ The additional power dissipation of the segment drivers
$\mathrm{V}_{\mathrm{O}}=$ The low state segment driver output voltage
$\mathrm{I}_{\mathrm{O}}=$ The programmed segment output current
$\mathrm{N}=$ The number of programmed segments in the static mode, or half the number of programmed segment drivers in the dynamic mode.

Under no conditions the total maximum dissipation ( 500 mW for the SO and 1000 mW for the DIL package) should be exceeded.

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Example: \(\mathrm{V}_{\mathrm{CC}}=5 \mathrm{~V}\)
    \(\mathrm{V}_{\mathrm{O}}=0.25 \mathrm{~V}\)
    \(\mathrm{I}_{\mathrm{O}}=12 \mathrm{~mA}\)
    24 programmed segments in dynamic mode
    \(P_{\text {tot }}=P_{1}+P_{2}+P_{3}\)
        \(=75 \mathrm{~mW}+(50\) * \(24 / 32) \mathrm{mW}+\left(0.25\right.\) * \(12.10^{-3}\) * 12) mW
        \(=148.5 \mathrm{~mW}\)
```


(1) All outputs programmed (no segment current sink).
(2) Outputs not programmed.

Fig. 7 SAA1064 power dissipation as a function of supply voltage.

## PACKAGE OUTLINES



| UNIT | A max. | $\mathbf{A}_{1}$ $\mathbf{m i n}$ | $\mathrm{A}_{2}$ max. | b | $\mathrm{b}_{1}$ | c | $\mathrm{D}^{(1)}$ | $E^{(1)}$ | e | $\mathrm{e}_{1}$ | L | $\mathrm{M}_{\mathrm{E}}$ | $M_{H}$ | w | $\mathrm{Z}^{(1)}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| mm | 5.1 | 0.51 | 4.0 | $\begin{aligned} & 1.7 \\ & 1.3 \end{aligned}$ | $\begin{aligned} & 0.53 \\ & 0.38 \end{aligned}$ | $\begin{aligned} & 0.32 \\ & 0.23 \end{aligned}$ | $\begin{aligned} & 32.0 \\ & 31.4 \end{aligned}$ | $\begin{aligned} & 14.1 \\ & 13.7 \end{aligned}$ | 2.54 | 15.24 | $\begin{aligned} & 3.9 \\ & 3.4 \end{aligned}$ | $\begin{aligned} & 15.80 \\ & 15.24 \end{aligned}$ | $\begin{aligned} & 17.15 \\ & 15.90 \end{aligned}$ | 0.25 | 2.2 |
| inches | 0.20 | 0.020 | 0.16 | $\begin{aligned} & 0.066 \\ & 0.051 \end{aligned}$ | $\begin{aligned} & 0.021 \\ & 0.015 \end{aligned}$ | $\begin{aligned} & 0.013 \\ & 0.009 \end{aligned}$ | $\begin{aligned} & 1.26 \\ & 1.24 \end{aligned}$ | $\begin{aligned} & 0.56 \\ & 0.54 \end{aligned}$ | 0.10 | 0.60 | $\begin{aligned} & 0.15 \\ & 0.13 \end{aligned}$ | $\begin{aligned} & 0.62 \\ & 0.60 \end{aligned}$ | $\begin{aligned} & 0.68 \\ & 0.63 \end{aligned}$ | 0.01 | 0.087 |

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

| OUTLINE VERSION | REFERENCES |  |  | EUROPEAN PROJECTION | ISSUE DATE |
| :---: | :---: | :---: | :---: | :---: | :---: |
|  | IEC | JEDEC | EIAJ |  |  |
| SOT101-1 | 051G02 | MO-015AD |  | $\square \oplus$ | $\begin{aligned} & 92-11-17 \\ & 95-01-23 \end{aligned}$ |



| UNIT | A max. | $\mathrm{A}_{1}$ | $\mathrm{A}_{2}$ | $\mathrm{A}_{3}$ | $\mathrm{b}_{\mathrm{p}}$ | c | $D^{(1)}$ | $E^{(1)}$ | e | $\mathrm{H}_{\mathrm{E}}$ | L | $\mathrm{L}_{\mathrm{p}}$ | Q | v | w | y | $\mathrm{Z}^{(1)}$ | $\theta$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| mm | 2.65 | $\begin{aligned} & 0.30 \\ & 0.10 \end{aligned}$ | $\begin{aligned} & 2.45 \\ & 2.25 \end{aligned}$ | 0.25 | $\begin{aligned} & 0.49 \\ & 0.36 \end{aligned}$ | $\begin{aligned} & 0.32 \\ & 0.23 \end{aligned}$ | $\begin{aligned} & 15.6 \\ & 15.2 \end{aligned}$ | $\begin{aligned} & 7.6 \\ & 7.4 \end{aligned}$ | 1.27 | $\begin{aligned} & 10.65 \\ & 10.00 \end{aligned}$ | 1.4 | $\begin{aligned} & 1.1 \\ & 0.4 \end{aligned}$ | $\begin{aligned} & 1.1 \\ & 1.0 \end{aligned}$ | 0.25 | 0.25 | 0.1 | $\begin{aligned} & 0.9 \\ & 0.4 \end{aligned}$ | $\begin{aligned} & 8^{\circ} \\ & 0^{\circ} \end{aligned}$ |
| inches | 0.10 | $\begin{aligned} & 0.012 \\ & 0.004 \end{aligned}$ | $\begin{aligned} & 0.096 \\ & 0.089 \end{aligned}$ | 0.01 | $\begin{aligned} & 0.019 \\ & 0.014 \end{aligned}$ | $\begin{aligned} & 0.013 \\ & 0.009 \end{aligned}$ | $\begin{aligned} & 0.61 \\ & 0.60 \end{aligned}$ | $\begin{aligned} & 0.30 \\ & 0.29 \end{aligned}$ | 0.050 | $\begin{aligned} & 0.419 \\ & 0.394 \end{aligned}$ | 0.055 | $\begin{aligned} & 0.043 \\ & 0.016 \end{aligned}$ | $\begin{aligned} & 0.043 \\ & 0.039 \end{aligned}$ | 0.01 | 0.01 | 0.004 | $\begin{aligned} & 0.035 \\ & 0.016 \end{aligned}$ |  |

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.


## SOLDERING

## Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398652 90011).

## DIP

## Soldering by dipping or by wave

The maximum permissible temperature of the solder is $260^{\circ} \mathrm{C}$; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature ( $\mathrm{T}_{\text {stg max }}$ ). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

## Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V ) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than $300^{\circ} \mathrm{C}$ it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and $400^{\circ} \mathrm{C}$, contact may be up to 5 seconds.

## SO

## Reflow soldering

Reflow soldering techniques are suitable for all SO packages.
Reflow soldering requires solder paste (a suspension of fine solder particles, flux and binding agent) to be applied to the printed-circuit board by screen printing, stencilling or pressure-syringe dispensing before package placement.

Several techniques exist for reflowing; for example, thermal conduction by heated belt. Dwell times vary between 50 and 300 seconds depending on heating method. Typical reflow temperatures range from 215 to $250^{\circ} \mathrm{C}$.

Preheating is necessary to dry the paste and evaporate the binding agent. Preheating duration: 45 minutes at $45^{\circ} \mathrm{C}$.

## Wave soldering

Wave soldering techniques can be used for all SO packages if the following conditions are observed:

- A double-wave (a turbulent wave with high upward pressure followed by a smooth laminar wave) soldering technique should be used.
- The longitudinal axis of the package footprint must be parallel to the solder flow.
- The package footprint must incorporate solder thieves at the downstream end.

During placement and before soldering, the package must be fixed with a droplet of adhesive. The adhesive can be applied by screen printing, pin transfer or syringe dispensing. The package can be soldered after the adhesive is cured.

Maximum permissible solder temperature is $260^{\circ} \mathrm{C}$, and maximum duration of package immersion in solder is 10 seconds, if cooled to less than $150^{\circ} \mathrm{C}$ within 6 seconds. Typical dwell time is 4 seconds at $250^{\circ} \mathrm{C}$.
A mildly-activated flux will eliminate the need for removal of corrosive residues in most applications.

## Repairing soldered joints

Fix the component by first soldering two diagonallyopposite end leads. Use only a low voltage soldering iron (less than 24 V ) applied to the flat part of the lead. Contact time must be limited to 10 seconds at up to $300^{\circ} \mathrm{C}$. When using a dedicated tool, all other leads can be soldered in one operation within 2 to 5 seconds between 270 and $320^{\circ} \mathrm{C}$.

## DEFINITIONS

| Data sheet status |  |
| :--- | :--- |
| Objective specification | This data sheet contains target or goal specifications for product development. |
| Preliminary specification | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification | This data sheet contains final product specifications. |
| Limiting values | Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or <br> more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation <br> of the device at these or at any other conditions above those given in the Characteristics sections of the specification <br> is not implied. Exposure to limiting values for extended periods may affect device reliability. |
| Application information |  |
| Where application information is given, it is advisory and does not form part of the specification. |  |

## LIFE SUPPORT APPLICATIONS

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