

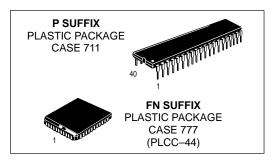
Color Television Composite Video Overlay Synchronizer

The MC1378 is a bipolar composite video overlay encoder and microcomputer synchronizer. The MC1378 contains the complete encoder function of the MC1377, i.e., quadrature color modulators, RGB matrix, and blanking level clamps, plus a complete complement of synchronizers to lock a microcomputer–based video source to any remote video source. The MC1378 can be used as a local system timing and encoding source, but it is most valuable when used to lock the microcomputer source to a remotely originated video signal.

- Contains All Needed Reference Oscillators
- Can Be Operated in PAL or NTSC Mode, 625 or 525 Line
- Wideband, Full-Fidelity Color Encoding
- Local or Remote Modes of Operation
- Minimal External Components
- Designed to Operate from 5.0 V supply
- Will Work with non standard Video



SEMICONDUCTOR TECHNICAL DATA

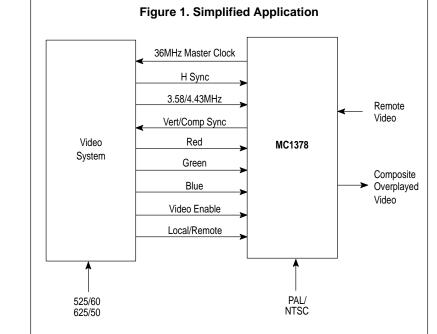


PIN CONNECTIONS			
Local/Rem.	1 (1)	(44) 40	H. Sync In
H. PLL Filter	2 (2)	(43) 39	Comp. Sync Out
н. vсо { [3 (3)	(42) 38	V. Out/Sync In
1	4 (4)	(41) 37	Clock PLL Filter
Burst Gate Out	5 (5)	(40) 36	Clock V _{CC}
PAL/NTSC Mode	6 (7)	(38) 35	Clock Output
Ground	7 (8)	(37) 34	Clock Ground
3.58/4.43 ln	8 (9)	(36) 33	Clock VCO
Chroma PLL Filter	9 (10)	(35) 32	
Chroma VCO	10 (11)	(34) 31	Killer Filter
	11 (12)	(33) 30	Quad. Loop Filter
R-Y Clamp	12 (13)	(32) 29	PAL Indent. Cap
B–Y Clamp	13 (14)	(31) 28	Vcc
R Input	14 (15)	(30) 27	Comp. Vid. Out
G Input	15 (16)	(29) 26	Ground
B Input	16 (18)	(27) 25	Overlay Enable
–Y Output	17 (19)	(26) 24	Rem. Vid. In
Chroma Out	18 (20)	(25) 23	ACC Filter
Loc. Vid. Clamp	19 (21)	(24) 22	–Y Input
Chroma In	20 (22)	(23) 21	Rem. Vid. Clamp
* () PLCC Pin Assignments			

ORDERING INFORMATION

Device	Operating Temperature Range	Package	
MC1378P	$T_{\Delta} = 0^{\circ} \text{ to } +70^{\circ}\text{C}$	Plastic DIP	
MC1378FN		PLCC-44	

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	VCC	6.0	Vdc
Operating Temperature	т _А	0 to +70	°C
Storage Temperature	T _{stg}	-65 to +150	°C
Junction Temperature	T _{J(max)}	150	°C
Power Dissipation, Package Derate above 25°C	PD	1.25 10	W mW/°C

RECOMMENDED OPERATING CONDITIONS

Condition	Pin	Value	Unit
Supply Voltage	28, 36	5.4 ± 0.25	Vdc
RGB Input for 100% Saturation	14, 15, 16	1.0	Vpp
Color Oscillator Input Level	8	0.5	Vpp
Video Input, Positive	24	1.0	Vpp

ELECTRICAL CHARACTERISTICS ($V_{CC} = 5.0 \text{ V}$, $T_A = 25^{\circ}C$, circuit of Figure 4 or 5)

Characteristics		Pin	Min	Тур	Max	Unit
Supply Current		28, 36	-	100	_	mAdc
Video Output, Open Circuit, Positive		27	-	2.0	9.4	V _{pp}
Modulation Angle $(R - Y)$ to $(B - Y)$		_	87	90	93	Degrees
RGB Input Impedance		14, 15, 16	-	10	-	kΩ
Local/Remote Switch (TTL)	High Low	1	-	Remote Local	-	-
Horizontal Sync Input, Negative Going	(TTL)	40	-	4.3	_	V _{pp}
Vertical Sync Output, Negative Going, Remote Mode	(TTL)	38	-	4.3	-	V _{pp}
Composite Sync Output, Negative Going	(TTL)	39	-	4.3	-	V _{pp}
Burst Gate Output, Positive Going	(TTL)	5	-	4.3	_	V _{pp}

Description of Operation – Refer to Figures 3, 4

Remote Mode	Local Mode		
The incoming remote video signal (Pin 24) supplies all synchronizing information. A discussion of the function of the phase detectors helps to clarify the lockup method:	The MC1378 and a video system combine to provide a fully synchronized standard signal source. In this case, composite sync must be supplied by the video system or other time base system. In the MC1378 the phase detectors operate as follows:		
PD1 — locks the internally counted–down 4 MHz horizontal VCO to the incoming horizontal sync. It is fast acting, to follow VCR source fluctuations.	PD1 — locks the internally counted–down 4 MHz horizontal VCO to a Horizontal Sync signal (at Pin 40) from the video system (counted down from 36 MHz)		
PD2 — locks the 36 MHz clock VCO, which is divided down by the video system, to the divided down horizontal VCO.	PD2 — not used in LOCAL MODE. PD3 — not used in LOCAL MODE.		
PD3 — is a gated phase detector which locks the 14 MHz crystal oscillator, divided by 4, to the incoming color burst.	PD4 — active, but providing an arbitrary phase shift setting between the color oscillator and the output burst phase.		
 PD4 — controls an internal phase shifter to assure that the outgoing color burst is the same phase as incoming burst at PD3. PD5 — not used in REMOTE MODE 	PD5 — locks the 36 MHz clock VCO (which is divided down by the video system) to the 14 MHz (crystal) color oscillator. The 14 MHz is, therefore, the system standard in LOCAL MODE, and is not DC controlled.		
Vertical lock is obtained by continuously resetting the sync generator in the video system with separated vertical sync from the MC1378, Pin 38. This signal is TTL level vertical block sync, negative going. The horizontal sync from the video system to Pin 40 is also TTL level with sync negative going. The local/remote switch, Pin 1, is in local mode when grounded, remote mode when taken to 5.0 V. The overlay control, Pin 25, has an analog characteristic, centered about 1.0 V, which allows fading from local to remote.	COMPOSITE VIDEO GENERATION The color encoding at the RGB signals is done exactly as in the MC1377. Composite chroma is looped out at Pins 18 and 20 to allow the designer to choose band shaping. Luminance is similarly brought out (Pins 17 and 22) to permit installation of the appropriate delay. Composite sync output, Pin 39, and burst gate output, Pin 5, are provided for convenience only.		

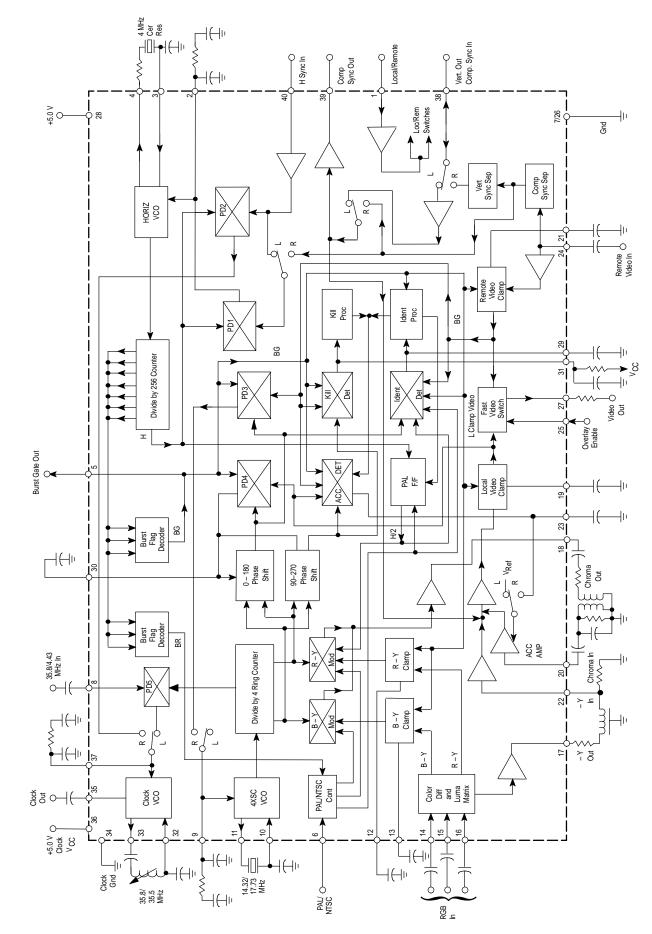
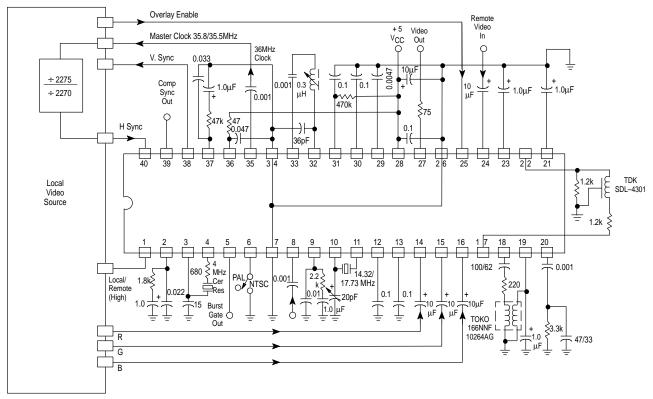


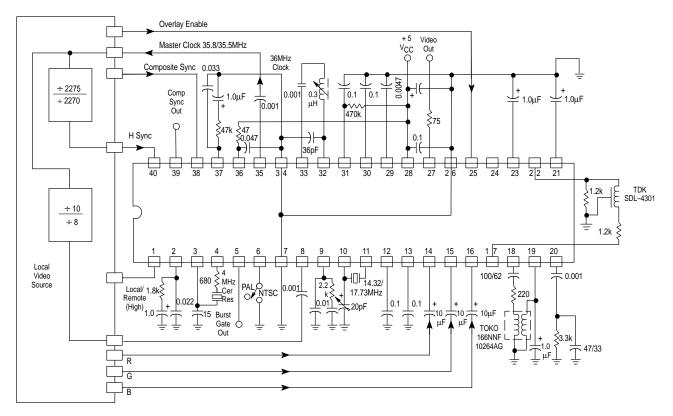
Figure 2. Representative Block Diagram

MOTOROLA ANALOG IC DEVICE DATA

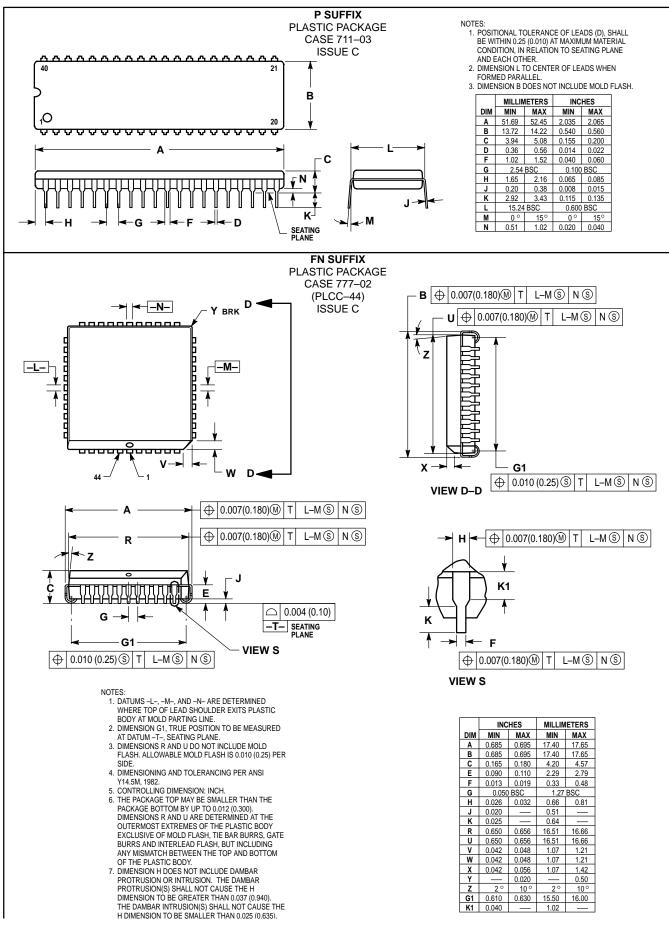








MC1378 OUTLINE DIMENSIONS



MOTOROLA ANALOG IC DEVICE DATA

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