TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC4028AP, TC74HC4028AF

BCD-to-Decimal Decoder

The TC74HC4028A is a high speed CMOS BCD-to-DECIMAL DECODER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

A BCD code applied to the four inputs (A-D) sets a high level at one of ten decoded outputs. A illegal BCD code such as eleven thru fifteen sets all outputs low. This device can be used as 3-to-8 LINE DECODER when input D is held high.

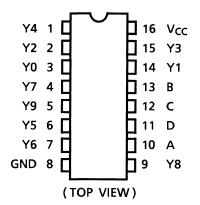
This device is useful for code conversion, address decoding, memory selection, multiplexing, or readout decoding.

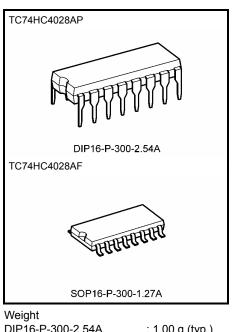
All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 18$ ns (typ.) at V_{CC} = 5 V •
- Low power dissipation: $I_{CC} = 4 \mu A (max)$ at $Ta = 25^{\circ}C$ ٠
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min) •
- Output drive capability: 10 LSTTL loads .
- Symmetrical output impedance: |IOH| = IOL = 4 mA (min) •
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$ •
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 4028B.

Pin Assignment



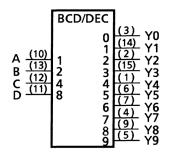


Weight	
DIP16-P-300-2.54A	
SOP16-P-300-1.27A	

: 1.00 g	(typ.)
: 0.18 g	(typ.)

TOSHIBA

IEC Logic Symbol

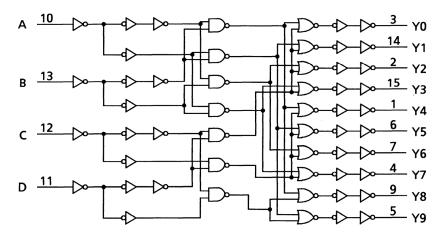


Truth Table

	Inp	outs			Outputs									
D	С	В	А	Y0	Y1	Y2	Y3	Y4	Y5	Y6	Y7	Y8	Y9	Output
L	L	L	L	Н	L	L	L	L	L	L	L	L	L	Y0
L	L	L	Н	L	Н	L	L	L	L	L	L	L	L	Y1
L	L	н	L	L	L	Н	L	L	L	L	L	L	L	Y2
L	L	Н	Н	L	L	L	Н	L	L	L	L	L	L	Y3
L	н	L	L	L	L	L	L	Н	L	L	L	L	L	Y4
L	Н	L	Н	L	L	L	L	L	Н	L	L	L	L	Y5
L	н	н	L	L	L	L	L	L	L	Н	L	L	L	Y6
L	Н	Н	Н	L	L	L	L	L	L	L	Н	L	L	Y7
Н	L	L	L	L	L	L	L	L	L	L	L	н	L	Y8
Н	L	L	Н	L	L	L	L	L	L	L	L	L	Н	Y9
Н	Х	н	Х	L	L	L	L	L	L	L	L	L	L	None
Н	Н	Х	Х	L	L	L	L	L	L	L	L	L	L	None

X: Don't care

System Diagram



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Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	±20	mA
Output diode current	I _{OK}	±20	mA
DC output current	IOUT	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	–65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65° C. From Ta = 65 to 85° C a derating factor of -10 mW/°C shall be applied until 300 mW.

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
		0 to 1000 (V _{CC} = 2.0 V)	
Input rise and fall time	t _r , t _f	0 to 500 (V_{CC} = 4.5 V)	ns
		0 to 400 (V _{CC} = 6.0 V)	

Operating Ranges (Note)

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			-	Ta = 25°C)	Ta = - 85	Unit		
					Min	Тур.	Max	Min	Max	C int	
				2.0	1.50		_	1.50	_		
High-level input voltage	VIH		—	4.5	3.15		—	3.15	—	V	
				6.0	4.20	—	—	4.20	—		
				2.0	_		0.50		0.50		
Low-level input voltage	V _{IL}	—		4.5	_	—	1.35	_	1.35	V	
Voltago				6.0		—	1.80		1.80		
	V _{OH}	V _{IN} = V _{IH} or V _{IL}	I _{OH} = -20 μA	2.0	1.9	2.0	_	1.9	_		
				4.5	4.4	4.5	—	4.4	—		
High-level output voltage				6.0	5.9	6.0	—	5.9	—	V	
			$I_{OH} = -4 \text{ mA}$	4.5	4.18	4.31	_	4.13	_		
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	—	5.63	—		
				2.0	_	0.0	0.1	_	0.1		
		VIN	$I_{OL} = 20 \ \mu A$	4.5	_	0.0	0.1	_	0.1		
Low-level output voltage	VOL	= VIH or		6.0		0.0	0.1		0.1	V	
Ŭ		VIL	$I_{OL} = 4 \text{ mA}$	4.5	_	0.17	0.26	_	0.33		
			$I_{OL} = 5.2 \text{ mA}$	6.0		0.18	0.26		0.33		
Input leakage current	IIN	V _{IN} = V _{CC} or GND		6.0	_	_	±0.1	_	±1.0	μA	
Quiescent supply current	Icc	$V_{IN} = V_C$	_C or GND	6.0			4.0	_	40.0	μA	

AC Characteristics (C_L = 15 pF, V_{CC} = 5 V, Ta = 25°C, input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	tтLH			4	8	ns
	t _{THL}			•	Ű	110
Propagation delay time	t _{pLH}	_	_	18	34	ns
	t _{pHL}			10	04	110

AC Characteristics (C_L = 50 pF, input: $t_r = t_f = 6 \text{ ns}$)

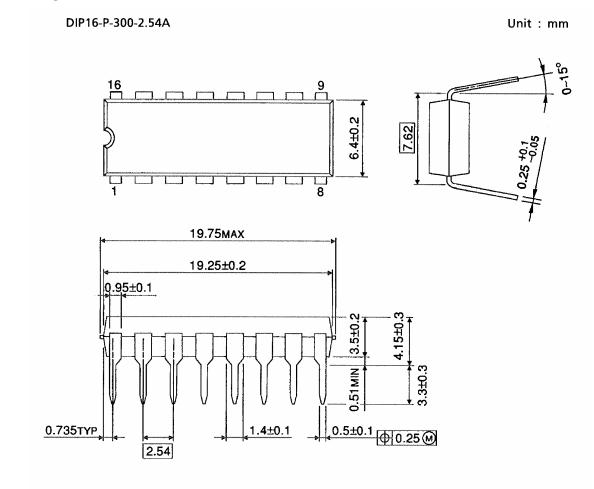
Characteristics	Symbol	Test Condition		-	Га = 25°С	;	Ta = - 85	Unit	
	Symbol		V _{CC} (V)	Min	Тур.	Max	Min	Max	Onic
	+		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	—	4.5	—	8	15	—	19	ns
t _{THL}	ΓΤΗL		6.0	—	7	13	—	16	
Propagation delay	4		2.0	_	80	180	_	225	
time	t _{pLH}	_	4.5	_	22	36	_	45	ns
(A, B, C, D-Y)	t _{pHL}		6.0	—	18	31	—	38	
Input capacitance	C _{IN}			_	5	10	_	10	pF
Power dissipation capacitance	C _{PD} (Note)	_			44				pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 I_{CC} (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions



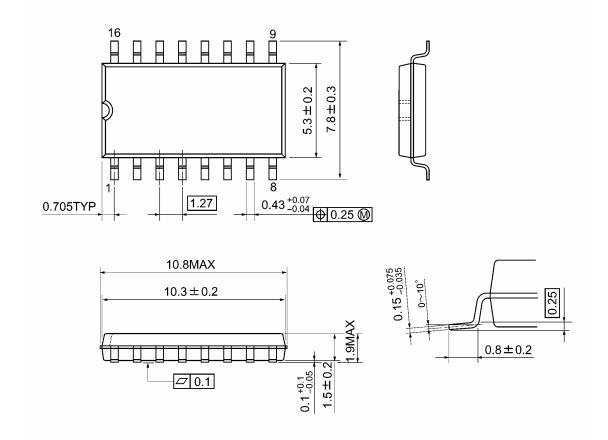
Weight: 1.00 g (typ.)

6

Package Dimensions

SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

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20070701-EN GENERAL

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