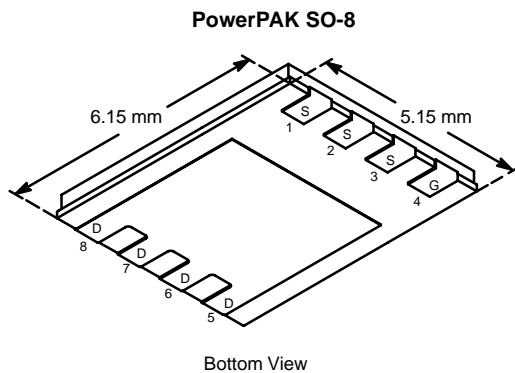


N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
30	0.0026 @ $V_{GS} = 10$ V	29
	0.0035 @ $V_{GS} = 4.5$ V	25



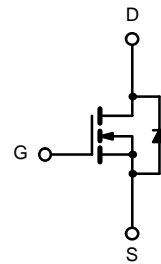
Ordering Information: Si7442DP-T1

FEATURES

- TrenchFET® Power MOSFET
- PWM Optimized
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile
- 100% R_g Tested

APPLICATIONS

- DC/DC Converters
 - Low-Side MOSFET in Synchronous Buck in Desktops
- Secondary Synchronous Rectifier
- Load Switch



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)					
Parameter	Symbol	10 secs	Steady State	Unit	
Drain-Source Voltage	V_{DS}	30		V	
Gate-Source Voltage	V_{GS}	± 12			
Continuous Drain Current ($T_J = 150^\circ\text{C}$) ^a	I_D	$T_A = 25^\circ\text{C}$	29	18	A
		$T_A = 70^\circ\text{C}$	25	14	
Pulsed Drain Current (10 μs Pulse Width)	I_{DM}	60			
Continuous Source Current (Diode Conduction) ^a	I_S	4.5	1.6		
Avalanche Current	I_{AS}	70		A	
Single Pulse Avalanche Energy		E_{AS}	245		mJ
Maximum Power Dissipation ^a	P_D	$T_A = 25^\circ\text{C}$	5.4	1.9	W
		$T_A = 70^\circ\text{C}$	3.4	1.2	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150		$^\circ\text{C}$	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 10$ sec	18	23	$^\circ\text{C/W}$
		Steady State	50	65	
Maximum Junction-to-Case (Drain)	R_{thJC}	1.0	1.5		

Notes

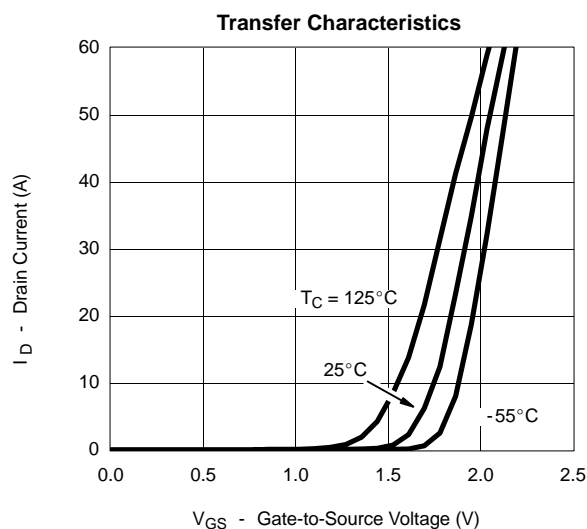
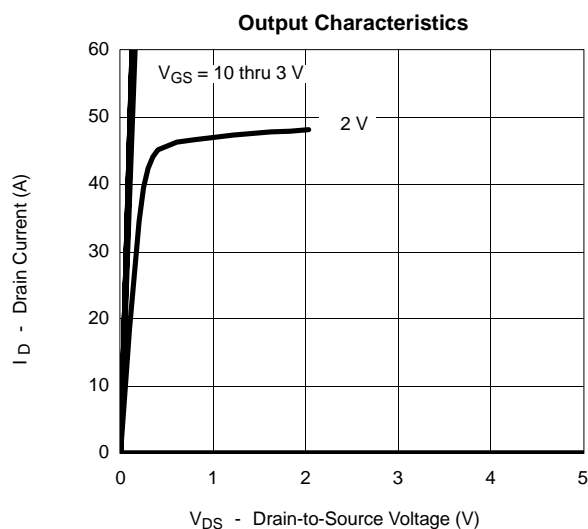
a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

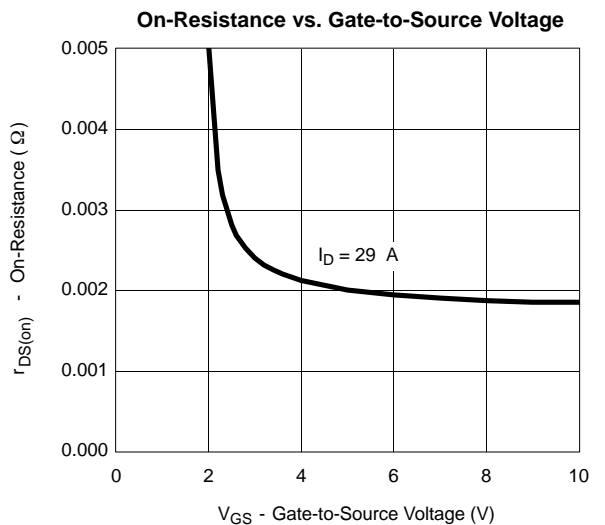
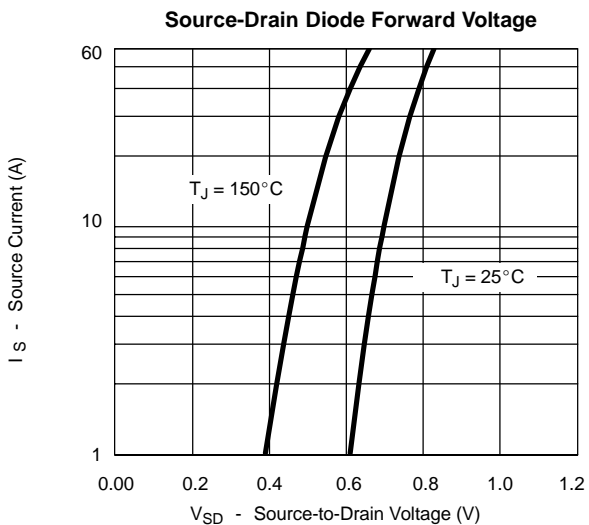
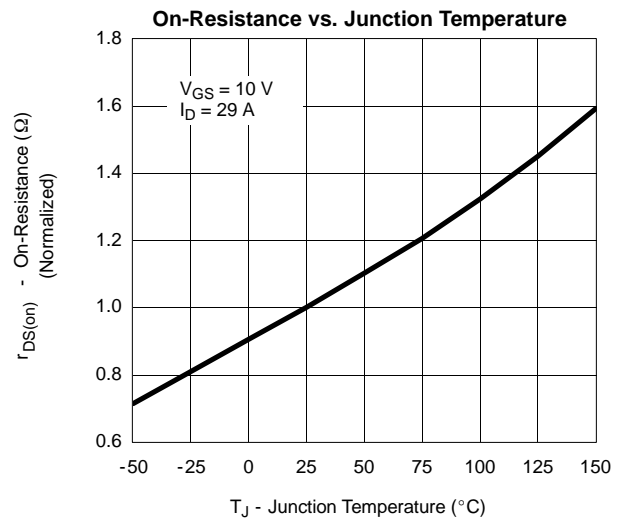
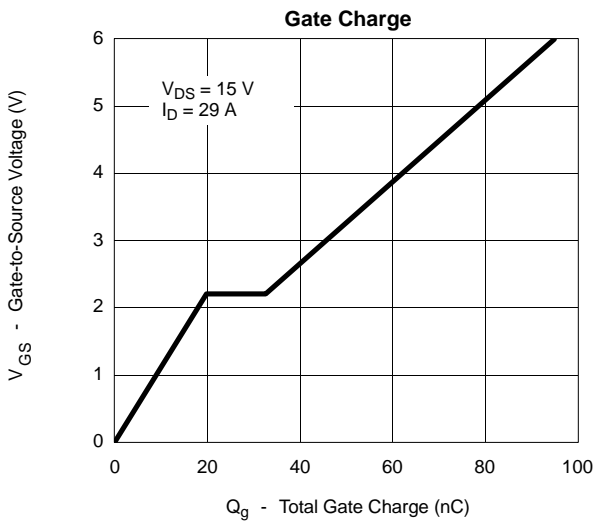
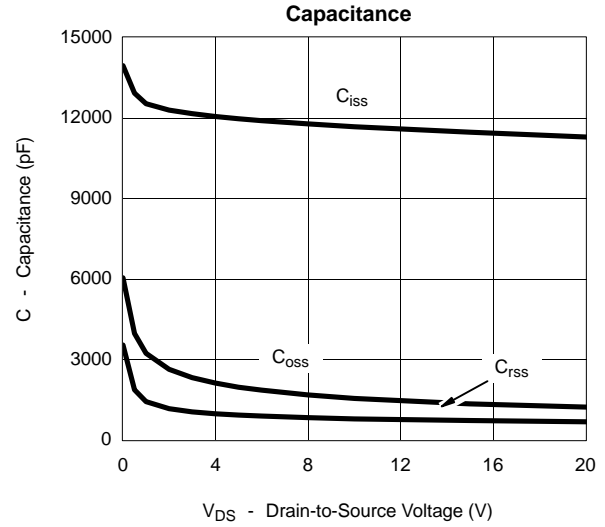
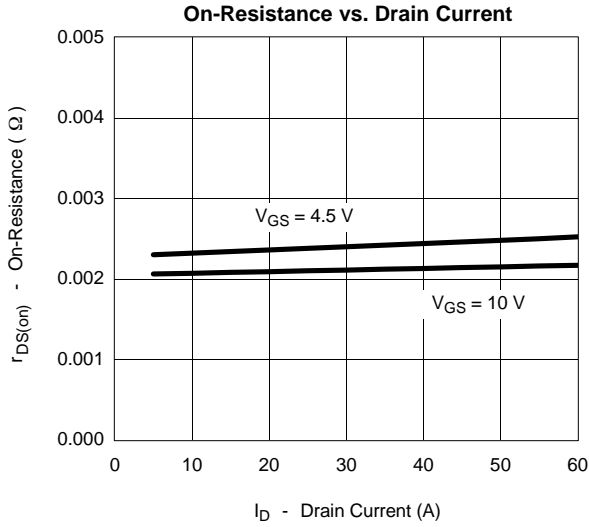
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	0.6		1.5	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
		V _{DS} = 24 V, V _{GS} = 0 V, T _J = 55 °C			5	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = 10 V, I _D = 29 A		0.0021	0.0026	Ω
		V _{GS} = 4.5 V, I _D = 25 A		0.0026	0.0035	
Forward Transconductance ^a	g _{fs}	V _{DS} = 6 V, I _D = 29 A		130		S
Diode Forward Voltage ^a	V _{SD}	I _S = 4.5 A, V _{GS} = 0 V		0.68	1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 29 A		70	110	nC
Gate-Source Charge	Q _{gs}			19.8		
Gate-Drain Charge	Q _{gd}			12.8		
Gate Resistance	R _g		0.5	1.2	2.0	Ω
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 15 Ω I _D ≅ 1 A, V _{GEN} = 10 V, R _G = 6 Ω		31	50	ns
Rise Time	t _r			18	30	
Turn-Off Delay Time	t _{d(off)}			270	400	
Fall Time	t _f			75	115	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 2.9 A, di/dt = 100 A/μs		55	80	

Notes

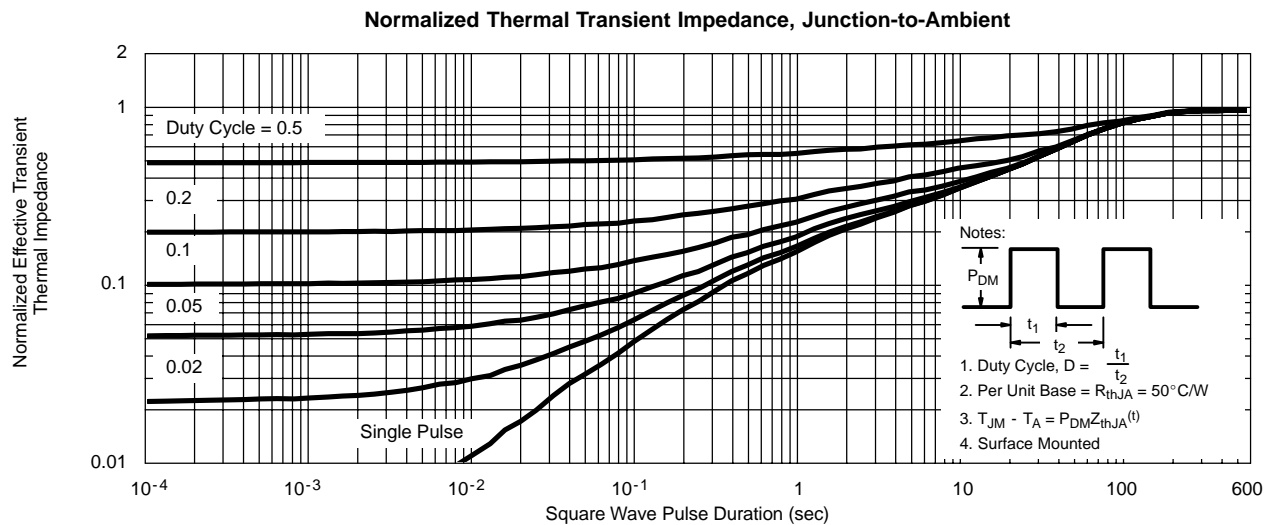
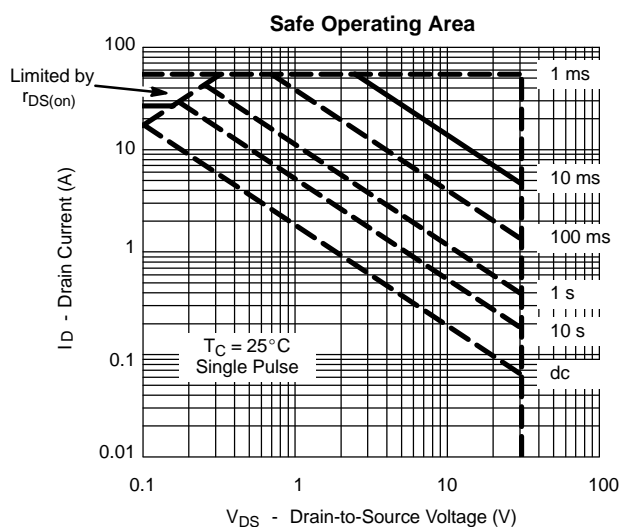
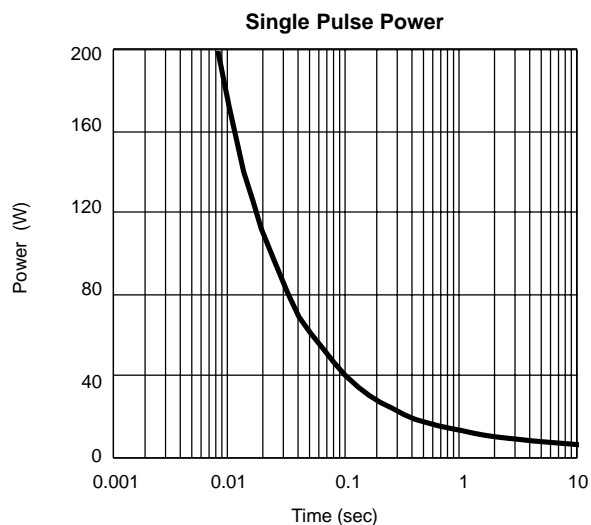
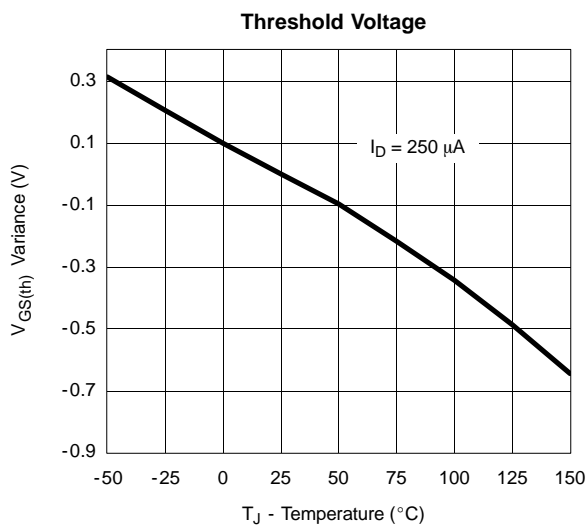
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

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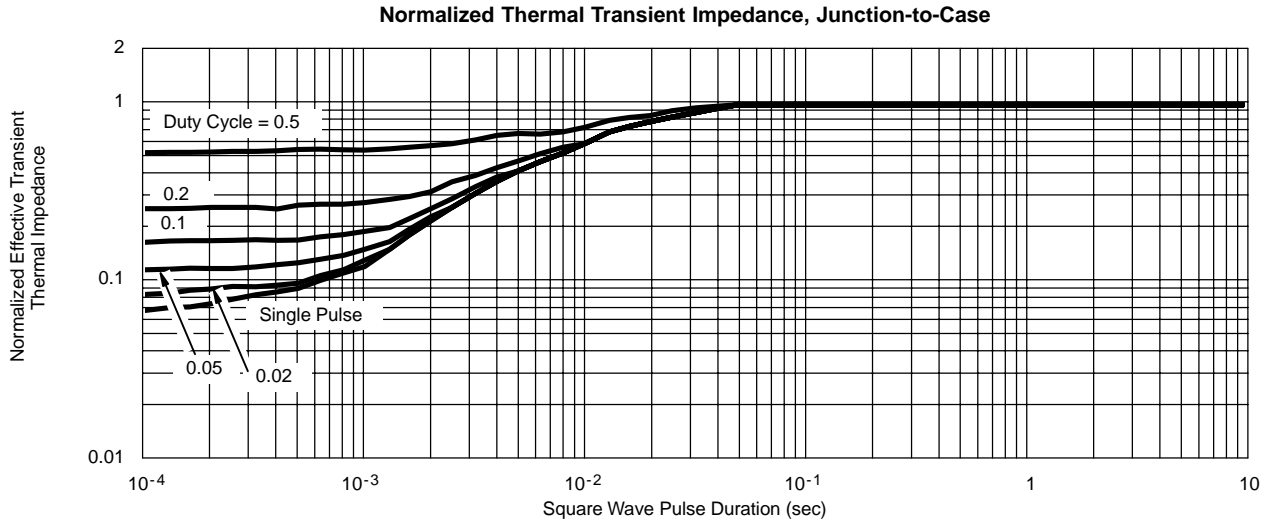


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





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