TOSHIBA **TA2150FN**

TENTATIVE

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

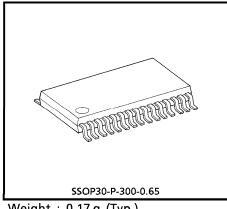
TA2150FN

RF AMPLIFIER FOR DIGITAL SERVO CD SYSTEM

TA2150FN is a 3-beam type PUH compatible RF Amplifier for Digital Servo to be used in the CD system. In combination with a CMOS single chip processor TC9462F/TC9495F, a CD system can be composed very simply.

FEATURES

- Built-in amplifier for reference (VRO, 2VRO) supply.
- Built-in Auto Laser Power Control circuit.
- Built-in RF amplifier.
- Built-in AGC amplifier.
- Built-in focus error amp and tracking error amp.
- Built-in sub-beam adder signal amplifier.
- Built-in gain change circuit for CD-RW.
- Capable of tracking balance control with TC9462F/TC9495F.
- Capable of RF gain adjustment circuit with TC9462F/TC9495F.
- Built-in signal amplifier for track counter.
- Capable of 4times speed operation.
- 30 pin mini flat package.



Weight: 0.17 g (Typ.)

The information contained herein is subject to change without notice.

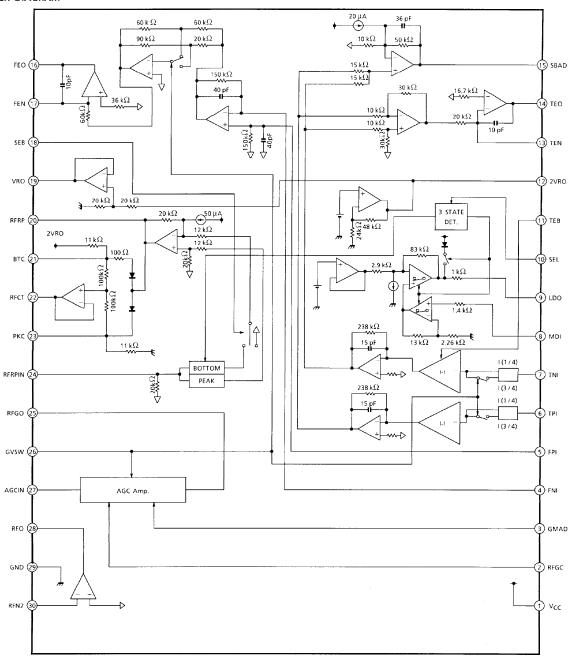
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BLOCK DIAGRAM



TA2150FN-2

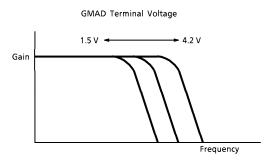
PIN FUNCTION

				1
PIN No.	SYMBOL	1/0	FUNCTION DESCRIPTION	REMARKS
1	v_{CC}	_	Power supply input terminal.	_
2	RFGC	I	RF amplitude adjustment control signal input terminal. Controlled by 3-PWM signals. (PWM carrier = 88.2 kHz)	3 signals input. (2VRO, VRO, GND)
3	GMAD		Open loop gain adjustment terminal for AGC amp.	(Note 1)
4	FNI	I	Main beam I-V amp input terminal.	Connected to pin diode output B + D (through resistor).
5	FPI	I	Main beam I-V amp input terminal.	Connected to pin diode output A + C (through resistor).
6	TPI	I	Sub beam I-V amp input terminal.	Connected to pin diode output F.
7	TNI	I	Sub beam I-V amp input terminal.	Connected to pin diode output E.
8	MDI	I	Monitor photo diode amp input terminal.	Connected to monitor photo diode.
9	LDO	0	Laser diode amp input terminal.	Connected to laser diode control circuit.
10	SEL	I	Laser diode control signal input terminal and APC circuit ON/OFF control signal terminal. SEL APC LEVEL CIRCUIT GND OFF Connected to VCC through resister (1 kΩ) Hiz ON Control signal output Low VCC ON Control signal output High	3 signals input. (V _{CC} , Hiz, GND)
11	TEB	I	Tracking error balance adjustment signal input terminal. Controlled by 3-PWM signal. (PWM carrier = 88.2 kHz)	3 signals input. (2VRO, VRO, GND)
12	2VRO	0	Reference voltage (2VRO) output terminal. $2VRO = 4.2 V$ when $V_{CC} = 5 V$	_
13	TEN	I	TE amp negative input terminal.	Connected to TEO through feedback resistor.
14	TEO	0	TE error signal output terminal.	_
15	SBAD	0	Sub beam adder signal output terminal.	_
16	FEO	0	Focus error signal output terminal.	_
17	FEN	I	FE amp negative input terminal.	Connected to FEO through feedback resistor.
18	SEB	I	RFRP output circuit switching terminal. SEB LEVEL BOTTOM PEAK DETECTION DETECTION GND ON ON ON VCC OFF ON	Low (GND) is for normal use.

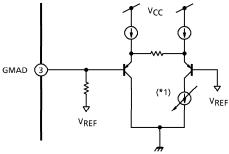
PIN FUNCTION

PIN No.	SYMBOL	1/0	FUNCTION DESCRIPTION	REMARKS
19	VRO	0	Reference signal (VRO) output terminal. $VRO = 2.1 V$ when $V_{CC} = 5 V$	_
20	RFRP	0	Track count signal output terminal.	_
21	ВТС	I	Time constant adjustment terminal for bottom detection.	Adjusted by capacitance.
22	RFCT	0	RFRP signal center level output terminal.	_
23	PKC	I	Time constant adjustment terminal for peak detection.	Adjusted by capacitance.
24	RFRPIN	l	Input terminal for track count signal output amp.	_
25	RFGO	0	Output terminal for RF signal amplitude adjustment amp.	_
26	GVSW	I	Amp (AGC, FE, TE) gain switching terminal. GVSW MODE GND CD-RW Hiz Normal VCC Normal	Low (GND) is for 5 times gain.
27	AGCIN	I	Input terminal for RF signal amplitude adjustment amp.	Connected to RFO through capacitance.
28	RFO	0	Output terminal RF signal amp.	
29	GND	_	Ground terminal.	_
30	RFN2	I	Input terminal for RF signal amp.	Connected to pin-diode output A + B + C + D (through resistor).

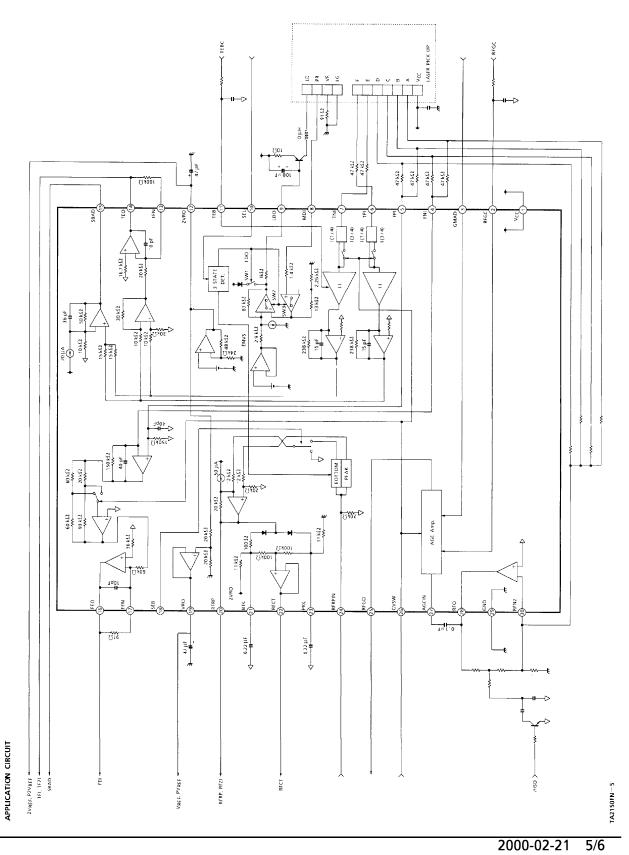
(Note 1) : Pin No.3 (GMAD) is gm adjustment terminal for AGC amp by applying a voltage (between 1.5 V and 4.2 V).
 If Pin No.3 (GMAD) is open, voltage of this terminal is fixed VRO by IC interior. Characteristic of frequency (open-loop characteristic) and voltage is as below.

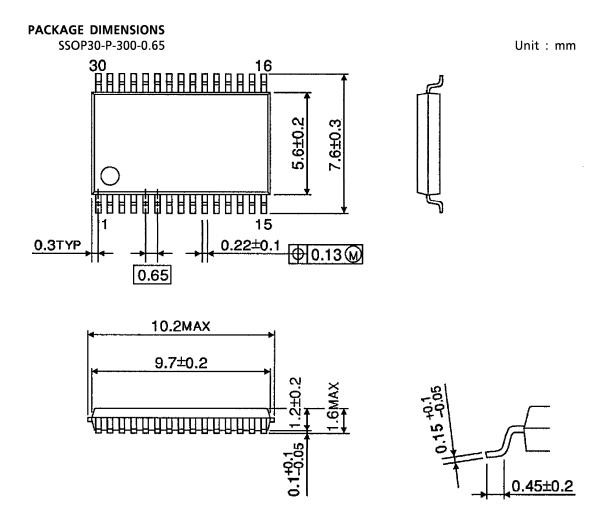


By changing a voltage (Pin No.3) between 1.5 V and 4.2 V, frequency band width is changed.



(*1) : Current is changed by 3 pin (GMAD) voltage.





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