



SMCGLCE6.5 thru SMCGLCE170A, e3
SMCJLCE6.5 thru SMCJLCE170A, e3

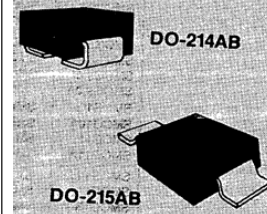
**1500 WATT LOW CAPACITANCE
 SURFACE MOUNT TRANSIENT
 VOLTAGE SUPPRESSOR**

DESCRIPTION

This surface mount Transient Voltage Suppressor (TVS) product family includes a rectifier diode element in series and opposite direction to achieve low capacitance below 100 pF. They are also available as RoHS Compliant with an e3 suffix. The low TVS capacitance may be used for protecting higher frequency applications in inductive switching environments or electrical systems involving secondary lightning effects per IEC61000-4-5 as well as RTCA/DO-160D or ARINC 429 for airborne avionics. They also protect from ESD and EFT per IEC61000-4-2 and IEC61000-4-4. If bipolar transient capability is required, two of these low capacitance TVS devices may be used in parallel and opposite directions (anti-parallel) for complete ac protection (Figure 6).

IMPORTANT: For the most current data, consult MICROSEMI's website: <http://www.microsemi.com>

APPEARANCE



FEATURES

- Available in standoff voltage range of 6.5 to 200 V
- Low capacitance of 100 pF or less
- Molding compound flammability rating: UL94V-0
- Two different terminations available in C-bend (modified J-Bend with DO-214AB) or Gull-wing (DO-215AB)
- Options for screening in accordance with MIL-PRF-19500 for JAN, JANTX, JANTXV, and JANS are available by adding MQ, MX, MV, or MSP prefixes respectively to part numbers
- Optional 100% screening for avionics grade is available by adding MA prefix to part number for 100% temperature cycle -55°C to 125°C (10X) as well as surge (3X) and 24 hours HTRB with post test V_{BR} & I_R
- RoHS Compliant devices available by adding an "e3" suffix

APPLICATIONS / BENEFITS

- 1500 Watts of Peak Pulse Power at 10/1000 μ s
- Protection for aircraft fast data rate lines per select level waveforms in RTCA/DO-160D & ARINC 429
- Low capacitance for high speed data line interfaces
- IEC61000-4-2 ESD 15 kV (air), 8 kV (contact)
- IEC61000-4-5 (Lightning) as further detailed in LCE6.5 thru LCE170A data sheet
- T1/E1 Line Cards
- Base Stations
- WAN Interfaces
- XDSL Interfaces
- CSU/DSU Equipment

MAXIMUM RATINGS

- 1500 Watts of Peak Pulse Power dissipation at 25°C with repetition rate of 0.01% or less*
 - Clamping Factor: 1.4 @ Full Rated power
1.30 @ 50% Rated power
 - $t_{clamping}$ (0 volts to $V_{(BR)}$ min): Less than 5×10^{-9} seconds
 - Operating and Storage temperatures: -65 to +150°C
 - Steady State power dissipation: 5.0W @ $T_L = 50^\circ\text{C}$
 - THERMAL RESISTANCE: 20°C/W (typical junction to lead (tab) at mounting plane)
- * When pulse testing, do not pulse in opposite direction (see "Schematic Applications" section herein and Figures 5 & 6 for further protection in both directions)

MECHANICAL AND PACKAGING

- CASE: Molded, surface mountable
- TERMINALS: Gull-wing or C-bend (modified J-bend) tin-lead or RoHS compliant annealed matte-tin plating solderable per MIL-STD-750, method 2026
- POLARITY: Cathode indicated by band
- MARKING: Part number without prefix (e.g. LCE6.5A, LCE6.5Ae3, LCE33, LCE33Ae3, etc.
- TAPE & REEL option: Standard per EIA-481-B with 16 mm tape, 750 per 7 inch reel or 2500 per 13 inch reel (add "TR" suffix to part number)

ELECTRICAL CHARACTERISTICS @ 25°C

MICROSEMI Part Number	MICROSEMI Part Number	Reverse Stand-Off Voltage V_{WM}	Breakdown Voltage V_{BR} @ $I_{(BR)}$			Maximum Reverse Leakage @ V_{WM} I_R μA	Maximum Clamping Voltage @ I_{PP} V_C Volts	Maximum Stand-Off Current I_{PP} @ 10/1000 Amps	Maximum Capacitance @ 0 Volts, $f = 1$ MHz	V_{WIB} Working Inverse Blocking Voltage Volts	I_{IB} Inverse Blocking Leakage Current mA	V_{PIB} Peak Inverse Blocking Voltage Volts
			MIN	MAX	mA							
Gull-Wing "G" Bend Lead	Modified "J" Bend Lead	Volts										
SMCGLCE6.5	SMCJLCE6.5	6.5	7.22	8.82	10	1000	12.3	100	75	1	100	
SMCGLCE6.5A	SMCJLCE6.5A	6.5	7.22	7.98	10	1000	11.2	100	75	1	100	
SMCGLCE7.0	SMCJLCE7.0	7.0	7.78	9.51	10	500	13.3	100	75	1	100	
SMCGLCE7.0A	SMCJLCE7.0A	7.0	7.78	8.60	10	500	12.0	100	75	1	100	
SMCGLCE7.5	SMCJLCE7.5	7.5	8.33	10.2	10	250	14.3	100	75	1	100	
SMCGLCE7.5A	SMCJLCE7.5A	7.5	8.33	9.21	10	250	12.9	100	75	1	100	
SMCGLCE8.0	SMCJLCE8.0	8.0	8.89	10.9	1	100	15.0	100	75	1	100	
SMCGLCE8.0A	SMCJLCE8.0A	8.0	8.89	9.83	1	100	13.6	100	75	1	100	
SMCGLCE8.5	SMCJLCE8.5	8.5	9.44	11.5	1	50	15.9	100	75	1	100	
SMCGLCE8.5A	SMCJLCE8.5A	8.5	9.44	10.4	1	50	14.4	100	75	1	100	
SMCGLCE9.0	SMCJLCE9.0	9.0	10.0	12.2	1	10	16.9	89	100	75	1	100
SMCGLCE9.0A	SMCJLCE9.0A	9.0	10.0	11.1	1	10	15.4	97	100	75	1	100

GRAPHS

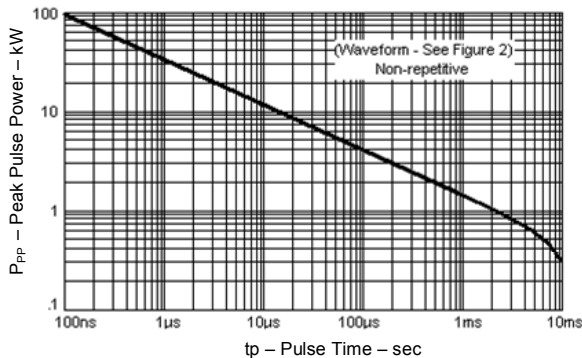


FIGURE 1 PEAK PULSE POWER vs. PULSE TIME

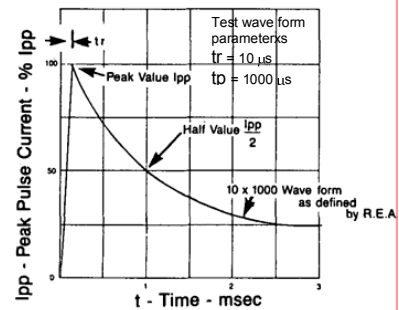


FIGURE 2 PULSE WAVEFORM

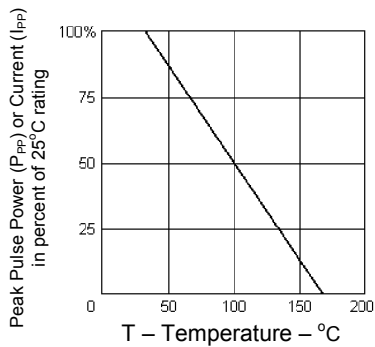


FIGURE 3 DERATING CURVE

SCHEMATIC APPLICATIONS

The TVS low capacitance device configuration is shown in Figure 4. As a further option for unidirectional applications, an additional low capacitance rectifier diode may be used in parallel in the same polarity direction as the TVS as shown in Figure 5. In applications where random high voltage transients occur, this will prevent reverse transients from damaging the internal low capacitance rectifier diode and also provide a low voltage conducting direction. The added rectifier diode should be of similar low capacitance and also have a higher reverse voltage rating than the TVS clamping voltage V_C . The Microsemi recommended rectifier part number for the application in Figure 5 is the "SMBJLCR80" or "SMBGLCR80" depending on the terminal configuration desired. If using two (2) low capacitance TVS devices in anti-parallel for bidirectional applications, this added protective feature for both directions (including the reverse of each rectifier diode) is inherently provided in Figure 6. The unidirectional and bidirectional configurations in Figure 5 and 6 will both result in twice the capacitance of Figure 4.

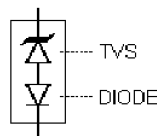


FIGURE 4
TVS with internal low capacitance rectifier diode

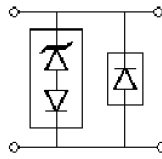


FIGURE 5
Optional Unidirectional configuration (TVS and separate rectifier diode in parallel)

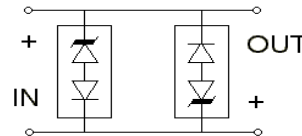


FIGURE 6
Optional Bidirectional configuration (two TVS devices in anti-parallel)

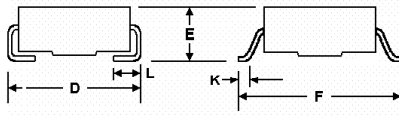
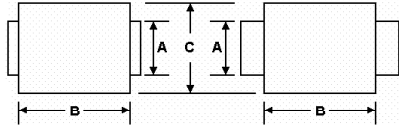


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www.Microsemi.COM

PACKAGE DIMENSIONS



DO-214AB
(SMCJ)

DO-215AB
(SMCG)

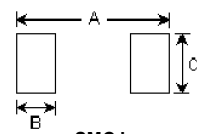
DIMENSIONS IN INCHES

	A	B	C	D	E	F	K	L
MIN	.115	.260	.220	.305	.077	.380	.025	.30
MAX	.121	.280	.245	.320	.104	.400	.040	.060

DIMENSIONS IN MILLIMETERS

	A	B	C	D	E	F	K	L
MIN	2.92	6.60	5.59	7.75	1.95	9.65	0.635	0.760
MAX	3.07	7.11	6.22	8.13	2.65	10.16	1.016	1.520

PAD LAYOUT



SMCJ

	INCHES	mm
A	.390	9.90
B	.110	2.79
C	.150	3.81

SMCG

	INCHES	mm
A	0.510	12.95
B	0.110	2.79
C	0.150	3.81

SMCGLCE/SMCJLCE