# <u>TOSHIBA</u>

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74LCX32F,TC74LCX32FN,TC74LCX32FT,TC74LCX32FK

## Low-Voltage Quad 2-Input OR Gate with 5-V Tolerant Inputs and Outputs

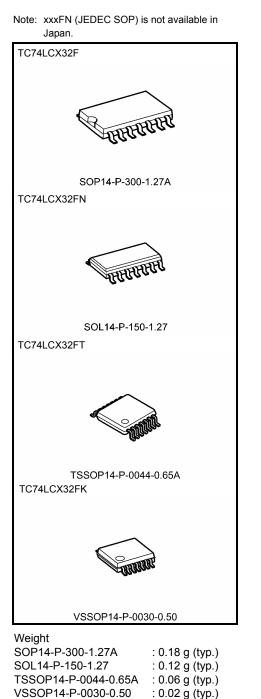
The TC74LCX32 is a high-performance CMOS 2-input OR gate. Designed for use in 3.3-V systems, it achieves high-speed operation while maintaining the CMOS low-power dissipation.

The device is designed for low-voltage (3.3 V)  $V_{\rm CC}$  applications, but it could be used to interface to 5-V supply environment for inputs.

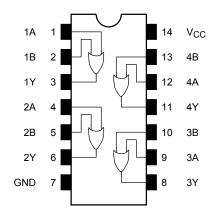
All inputs are equipped with protection circuits against static discharge.

## Features

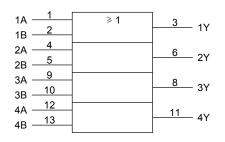
- Low-voltage operation: VCC = 2.0 to 3.6 V
- High-speed operation:  $t_{pd} = 5.5 \text{ ns} (max) (V_{CC} = 3.0 \text{ to } 3.6 \text{ V})$
- Output current:  $|I_{OH}|/I_{OL} = 24 \text{ mA} (\text{min}) (V_{CC} = 3.0 \text{ V})$
- Latch-up performance: -500 mA
- Available in JEDEC SOP, JEITA SOP, TSSOP and VSSOP (US)
- Power-down protection is provided on all inputs and outputs
- Pin and function compatible with the 74 series (74AC/VHC/HC/F/ALS/LS etc.) 32 type



## Pin Assignment (top view)



## **IEC Logic Symbol**



#### **Truth Table**

Inp	uts	Outputs	
А	В	Y	
L	L	L	
L	Н	н	
Н	L	Н	
Н	Н	Н	

## Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V	
DC input voltage	V <sub>IN</sub>	–0.5 to 7.0	V	
		-0.5 to 7.0 (Note 2)		
DC output voltage	Vout	-0.5 to V <sub>CC</sub> + 0.5 (Note 3)	V	
Input diode current	I <sub>IK</sub>	-50	mA	
Output diode current	IOK	±50 (Note 4)	mA	
DC output current	I <sub>OUT</sub>	±50	mA	
Power dissipation	PD	180	mW	
DC V <sub>CC</sub> /ground current	I <sub>CC</sub> /I <sub>GND</sub>	±100	mA	
Storage temperature	T <sub>stg</sub>	-65 to 150	°C	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2:  $V_{CC} = 0 V$ 

Note 3: High or low state. I<sub>OUT</sub> absolute maximum rating must be observed.

Note 4:  $V_{OUT} < GND, V_{OUT} > V_{CC}$ 

## **Operating Ranges (Note 1)**

Characteristics	Symbol	Rating	Unit
Power supply voltage	Vcc	2.0 to 3.6	V
r ower supply voltage	VCC	1.5 to 3.6 (Note 2)	v
Input voltage	V <sub>IN</sub>	0 to 5.5	V
Output voltage	V <sub>OUT</sub>	0 to 5.5 (Note 3)	V
Output voltage		0 to V <sub>CC</sub> (Note 4)	v
Output current	lou/lou	±24 (Note 5)	mA
Output current	IOH/IOL	±12 (Note 6)	mA
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 10 (Note 7)	ns/V

Note 1: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Note 2: Data retention only

Note 3:  $V_{CC} = 0 V$ 

Note 4: High or low state

Note 5:  $V_{CC}=3.0 \mbox{ to } 3.6 \mbox{ V}$ 

Note 6:  $V_{CC} = 2.7$  to 3.0 V

Note 7:  $V_{IN}=0.8$  to 2.0 V,  $V_{CC}=3.0$  V

## **Electrical Characteristics**

#### DC Characteristics (Ta = -40 to 85°C)

Characteristi	ics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Max	Unit
	H-level	VIH		_	2.7 to 3.6	2.0		v
Input voltage	L-level	VIL			2.7 to 3.6		0.8	v
				I <sub>OH</sub> = -100 μA	2.7 to 3.6	V <sub>CC</sub> - 0.2	_	V
	H-level	VOH	$V_{IN} = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -12 \text{ mA}$	2.7	2.2	_	
				I <sub>OH</sub> = -18 mA	3.0	2.4	_	
Output voltage				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	
		el V <sub>OL</sub>	$V_{IN} = V_{IL}$	$I_{OL} = 100 \ \mu A$	2.7 to 3.6		0.2	
	Lloval			$I_{OL} = 12 \text{ mA}$	2.7		0.4	
	L-IEVEI			$I_{OL} = 16 \text{ mA}$	3.0	_	0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	_	0.55	
Input leakage current		I <sub>IN</sub>	$V_{\mbox{\rm IN}}=0$ to 5.5 V		2.7 to 3.6		±5.0	μA
Power-off leakage curr	ent	I <sub>OFF</sub>	$V_{IN}/V_{OUT} = 5.5 V$		0		10.0	μA
Quiescent supply current		ICC	$V_{IN} = V_{CC}$ or GND		2.7 to 3.6		10.0	
Quiescent supply current	V <sub>IN</sub> = 3.6 to 5.5 V		2.7 to 3.6		±10.0	μA		
Increase in Icc per inpu	ut	∆lcc	$V_{IH} = V_{CC} - 0.6 V$		2.7 to 3.6	_	500	

#### AC Characteristics (Ta = -40 to $85^{\circ}$ C)

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Max	Unit
Propagation delay time	t <sub>pLH</sub>	Figure 1, Figure 2	2.7	_	6.2	ns
	t <sub>pHL</sub>		$\textbf{3.3}\pm\textbf{0.3}$	1.5	5.5	
Output to output skew	t <sub>osLH</sub>	(Note)	2.7		_	
	t <sub>osHL</sub>	(NOLE)	$\textbf{3.3}\pm\textbf{0.3}$		1.0	ns

Note: Parameter guaranteed by design.

 $(t_{OSLH} = |t_{pLHm} - t_{pLHn}|, t_{OSHL} = |t_{pHLm} - t_{pHLn}|)$ 

#### Dynamic Switching Characteristics (Ta = 25°C, input: $t_r = t_f = 2.5$ ns, $C_L = 50$ pF, $R_L = 500 \Omega$ )

Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Quiet output maximum dynamic $V_{OL}$	VOLP	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3	0.8	V
Quiet output minimum dynamic $V_{OL}$	V <sub>OLV</sub>	$V_{IH} = 3.3 V, V_{IL} = 0 V$	3.3	0.8	V

#### **Capacitive Characteristics (Ta = 25°C)**

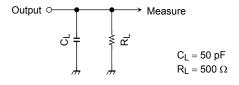
Characteristics	Symbol	Test Condition	V <sub>CC</sub> (V)	Тур.	Unit
Input capacitance	C <sub>IN</sub>	_	3.3	7	pF
Output capacitance	C <sub>OUT</sub>	_	0	8	pF
Power dissipation capacitance	CPD	f <sub>IN</sub> = 10 MHz (Note	3.3	25	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

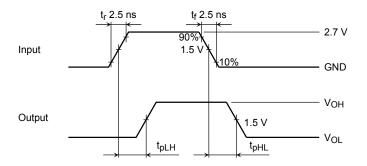
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/4$  (per gate)

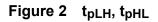
## **AC Test Circuit**





## **AC Waveform**



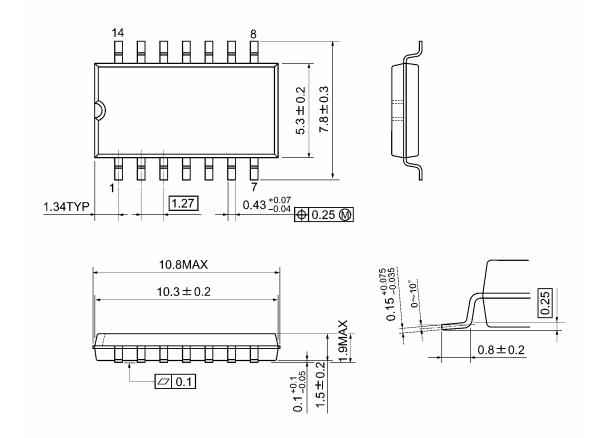


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## **Package Dimensions**

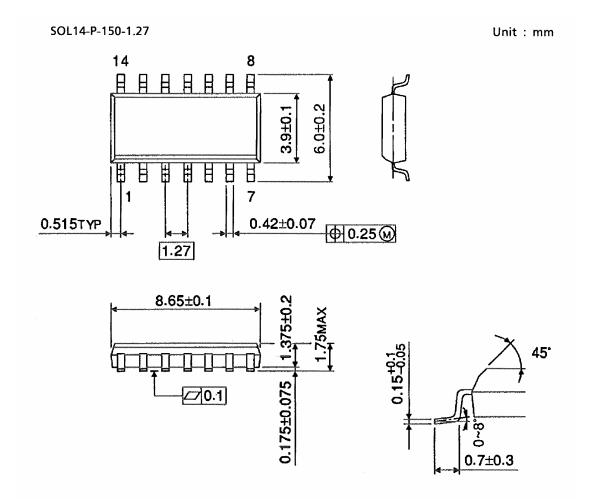
SOP14-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

## Package Dimensions (Note)



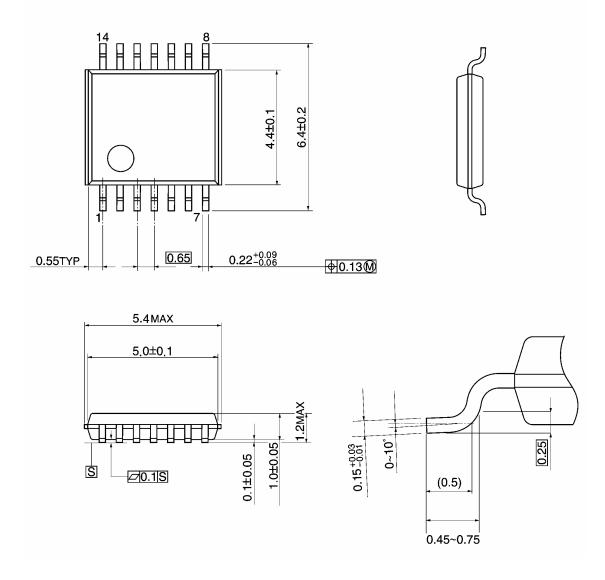
Note: This package is not available in japan.

Weight: 0.12 g (typ.)

## **Package Dimensions**

TSSOP14-P-0044-0.65A

Unit: mm



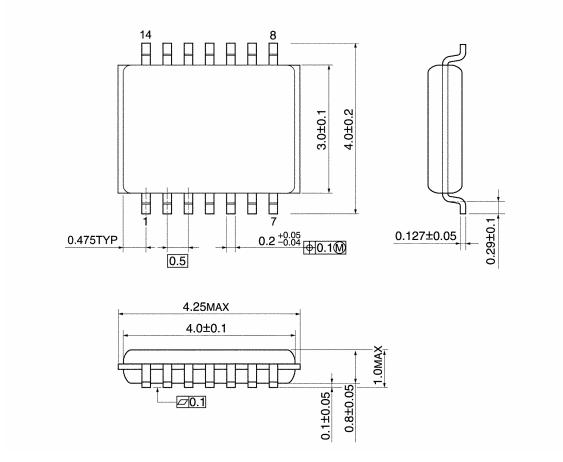
Weight: 0.06 g (typ.)

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## **Package Dimensions**

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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20070701-EN GENERAL

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