

HD74LS393

Dual 4-bit Binary Counters

REJ03D0486-0200 Rev.2.00 Feb.18.2005

This circuit contains eight master-slave flip-flops and additional gating to implement two individual four-bit counters. The HD74LS393 comprises two independent four-bit binary counters each having a clear and a clock input.

N-bit binary counter can be implemented with each package providing the capability of divide-by-258.

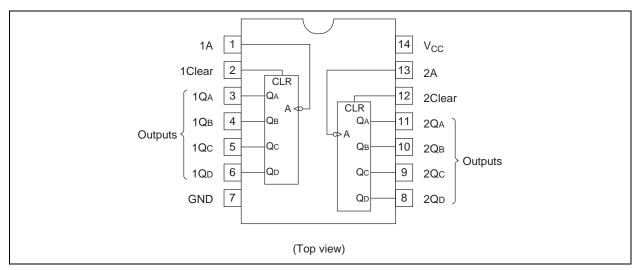
Features

Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS393P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS393FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS393RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement

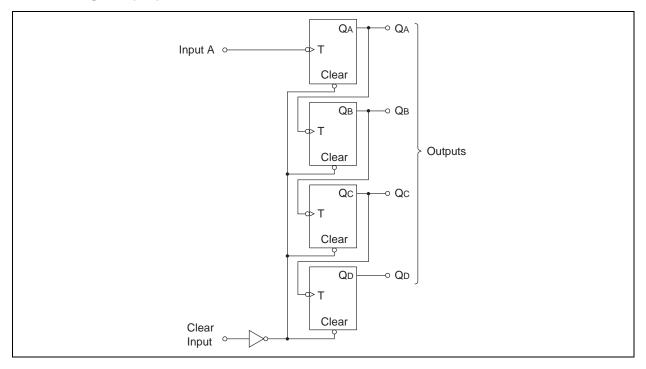


Function Table

Count	Output							
Count	Q_D	Q _C	Q _B	Q _A				
0	L	L	L	L				
1	L	L	L	Н				
2	L	L	Н	L				
3	L	L	Н	Н				
4	L	Н	L	L				
5	L	Н	L	Н				
6	L	Н	Н	L				
7	L	Н	Н	Н				
8	Н	L	L	L				
9	Н	L	L	Н				
10	Н	L	Н	L				
11	Н	L	Н	Н				
12	Н	Н	L	L				
13	Н	Н	L	Н				
14	Н	Н	Н	L				
15	Н	Н	Н	Н				

H; high level, L; low level

Block Diagram (1/2)



Absolute Maximum Ratings

Item		Symbol	Ratings	Unit	
Supply voltage		V _{CC}	7	V	
Input voltage	Clear	V _{IN}	7	V	
Input voltage	Α	V _{IN}	5.5	V	
Power dissipation		P _T	400	mW	
Storage temperature		Tstg	-65 to +150	°C	
Operating temperature		Topr	-20 to +75	°C	

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item		Symbol	Min	Тур	Max	Unit
Supply voltage		V _{CC}	4.75	5.00	5.25	V
Output surrent		I _{OH}	_	_	-400	μΑ
Output current	Output current		_	_	8	mA
Operating temperature		Topr	-20	_	75	°C
Count frequency	A input	f_{count}	0	_	25	MHz
A input high or low		t _w	20	_	_	ns
	Clear high		20	_	_	
Clear setup time		t _{su}	25↓	_	_	ns

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item		Symbol	min.	typ.*	max.	Unit	Condition		
Input voltage		V _{IH}	2.0	_	_	V			
		V _{IL}	_	_	8.0	V			
		V _{OH}	2.7	_	_	V	$V_{CC} = 4.75 \; V, V_{IH} = 2 \; V, V_{IL} = 0.8 \; V, \\ I_{OH} = -400 \; \mu A$		
Output voltag	е	\/	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V},$		
		V _{OL}	_	_	0.5	v	$I_{OL} = 8 \text{ mA}$ $V_{IH} = 2 \text{ V}, V_{IL} = 0.8 \text{ V}$		
	Clear	I _{IH}	_	_	20	^	V _{CC} = 5.25 V, V _I = 2.7 V		
	Input A		_	_	100	μΑ			
Input	Clear	- I _{IL}	_	_	-0.4	mA	V _{CC} = 5.25 V, V _I = 0.4 V		
current	Input A		_	_	-1.6	IIIA	V _{CC} = 5.25 V, V ₁ = 0.4 V		
	Clear	l _l	_	_	0.1	mA	V _I = 7 V V _{CC} = 5.25 V		
	Input A	11	_	_	0.2	IIIA	V _I = 5.5 V V _{CC} = 5.25 V		
Short-circuit output current		Ios	-20	_	-100	mA	V _{CC} = 5.25 V		
Supply current		I _{CC} **	_	15	26	mA	V _{CC} = 5.25 V		
Input clamp voltage		V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$		

Notes: $^*V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$



 $^{^{**}}$ I_{CC} is measured with all outputs open, both clear inputs grounded following momentary connection to 4.5 V, and all other inputs grounded.

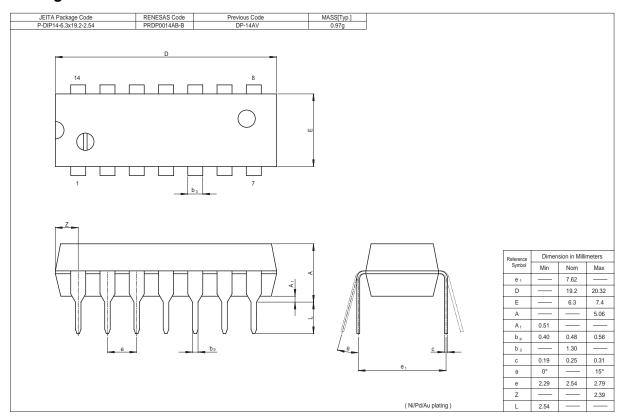
Switching Characteristics

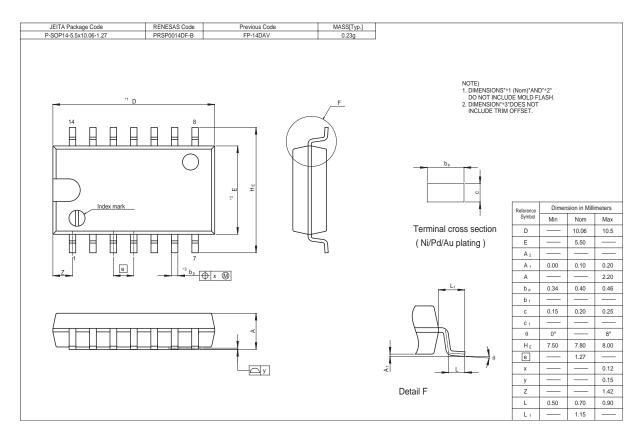
 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

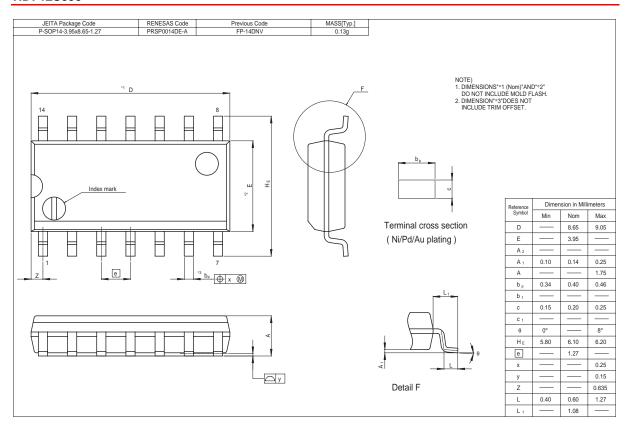
Item	Symbol	Inputs	Outputs	min.	typ.	max.	Unit	Condition
Maximum count frequency	$f_{\sf max}$	Α	Q_A	25	35	_	MHz	
	t _{PLH}	Α	Q _A	_	12	20	ns ns	$C_L = 15 \text{ pF},$ $R_L = 2 \text{ k}\Omega$
	t _{PHL}	A		_	13	20		
Propagation delay time	t _{PLH}	А		_	40	60		
	t _{PHL}			_	40	60		
	t _{PHL}	Clear	Any	_	24	39	ns	

Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

Package Dimensions







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Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.

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Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001