

M62216FP/GP

Low Voltage Operation STEP-UP DC/DC Converter

REJ03D0845-0201
Rev.2.01
Nov 14, 2007

Description

The M62216FP is designed as low voltage operation STEP-UP DC/DC converter.

This IC can operate very low input voltage (over 0.9 V) and low power dissipation. (circuit current is less than 850 μ A)

So, this IC suitable for power supply of portable system that using low voltage battery. (DRY battery, rechargeable battery)

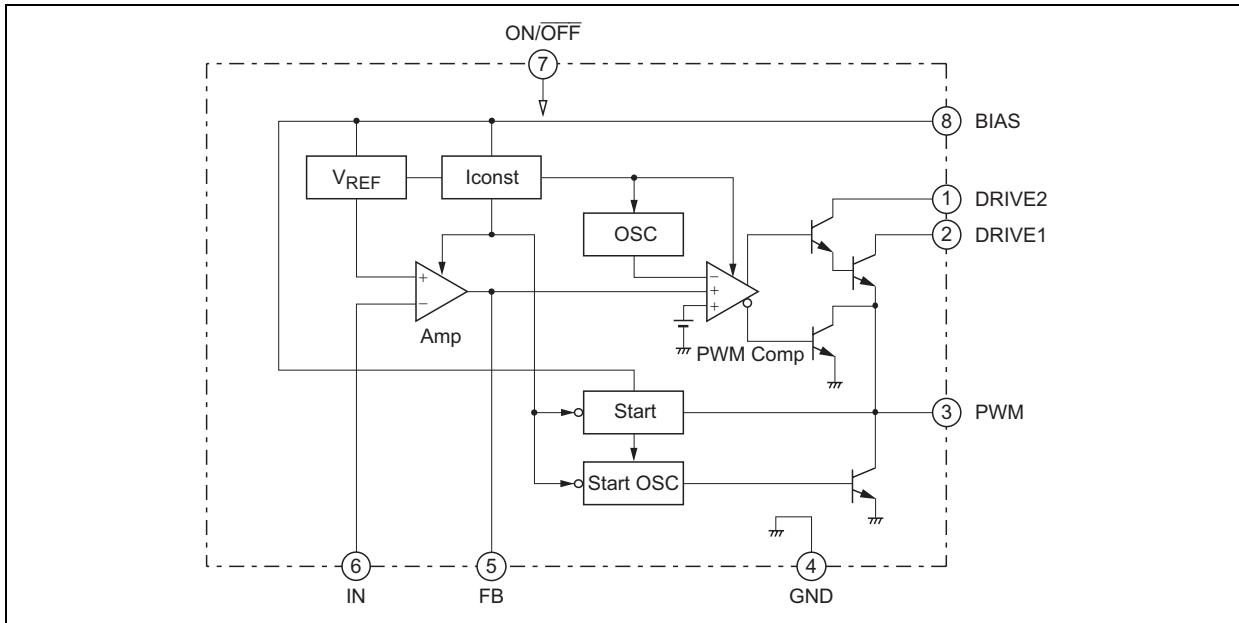
Features

- Pre-drive type PWM output (Pre-drive only)
- Low voltage operation..... $V_{IN} = 0.9$ V min.
- Low current dissipation..... $I_B = 850 \mu A$ typ.
- Pre-drive output current can be adjusted
- Built-in ON/OFF Function..... $I_B(OFF) = 35 \mu A$ typ.
- Application for STEP-DOWN Converter can be used

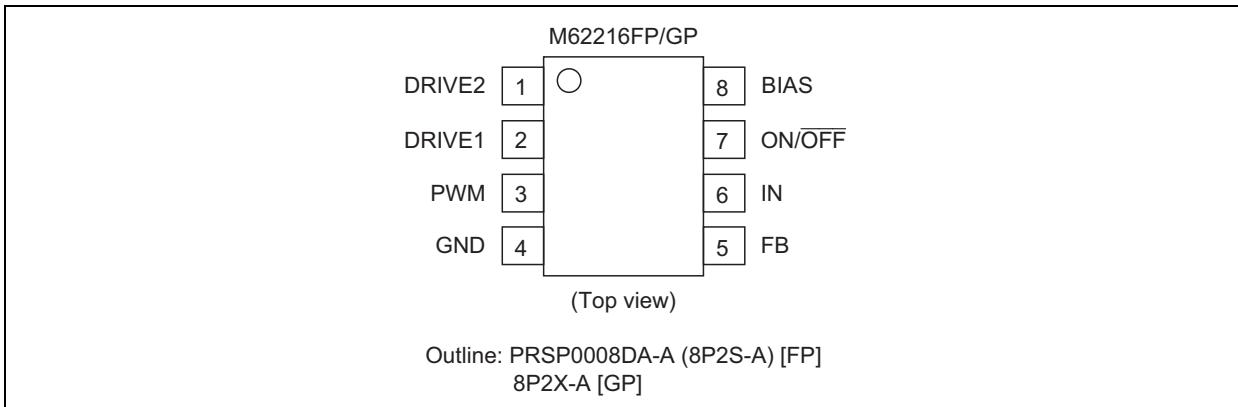
Application

DC/DC Converter for portable sets of battery used

Block Diagram



Pin Arrangement



Absolute Maximum Ratings

(Ta = 25°C, unless otherwise noted)

Item	Symbol	Ratings	Units	Conditions
Input voltage	V _{IN}	15.5	V	
Bias terminal supply voltage	V _{BIAS}	15.5	V	
Drive1 terminal supply voltage	V _{DRIVE1}	15.5	V	
Drive2 terminal supply voltage	V _{DRIVE2}	15.5	V	
Drive1 terminal input current	I _{DRIVE1}	100	mA	
Drive2 terminal Input current	I _{DRIVE2}	10	mA	
Power dissipation	Pd	440 (FP) 250 (GP)	mW	Ta = 25°C
Operating temperature	Topr	-20 to +85	°C	
Storage temperature	Tstg	-40 to +150	°C	

Electrical Characteristics

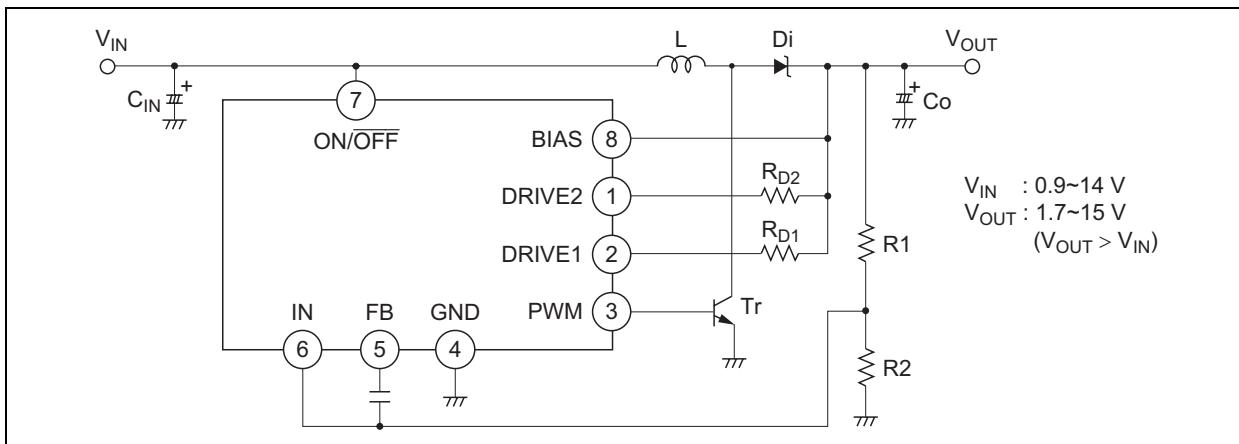
(Ta = 25°C, V_{IN} = 1.7 V, V_{OUT} = V_{BIAS} = 3.0 V, unless otherwise noted)

Block	Item	Symbol	Limits			Units	Test Condition
			Min	Typ	Max		
All device	Input voltage range	V _{IN}	0.9	—	15	V	
	BIAS voltage setting range* ¹	V _{BIAS}	1.7	—	15	V	
	BIAS current	I _B	—	850	1200	μA	
	BIAS current at off mode	I _{B(OFF)}	—	35	47	μA	
Voltage reference	Reference voltage	V _{REF}	1.20	1.26	1.32	V	Use internal amp as Buffer-amp
	BIAS voltage regulation of VREF	ΔV _{REF}	—	10	30	mV	V _{BIAS} = 1.7 to 15 V
Error Amp.	Input current	I _{IN}	—	20	—	nA	IN = 1 V/IM
	Open loop voltage gain	A _V	—	70	—	dB	f _{IN} = 100 Hz, null amp operation
	FB terminal sink current	I _{FB+}	260	800	—	μA	IN = 1.4 V, FB = 1.25 V/IM
	FB terminal source current	I _{FB-}	30	45	60	μA	IN = 1.1 V, FB = 1.25 V/IM
Osc.	Oscillation frequency	f _{osc}	95	125	155	kHz	PWM terminal monitored
	Maximum on duty	DUTY _{max}	82	87	92	%	PWM terminal monitored, IN = 1.1 V
Output	Saturation voltage between PWM Term. and DRIVE1 Term.	V _{sat1}	—	0.25	0.5	V	I _{DRIVE1} = 50 mA, I _{DRIVE2} = 5 mA
	Saturation voltage between PWM Term. and DRIVE2 Term.	V _{sat2}	—	1.0	1.2	V	
	Leak current of DRIVE1 terminal	I _{L1}	-1	—	1	μA	IN = 1.4 V
	Leak current of DRIVE2 terminal	I _{L2}	-1	—	1	μA	IN = 1.4 V
	Output low voltage of PWM terminal	V _{PWM (L)}	—	0.03	0.3	V	I _{PWM} = 1 mA
ON/OFF	Input current of ON/OFF terminal at on status	I _{ON}	—	2	3	μA	
	Threshold voltage of ON/OFF terminal	V _{TH(ON)}	—	0.65	0.75	V	

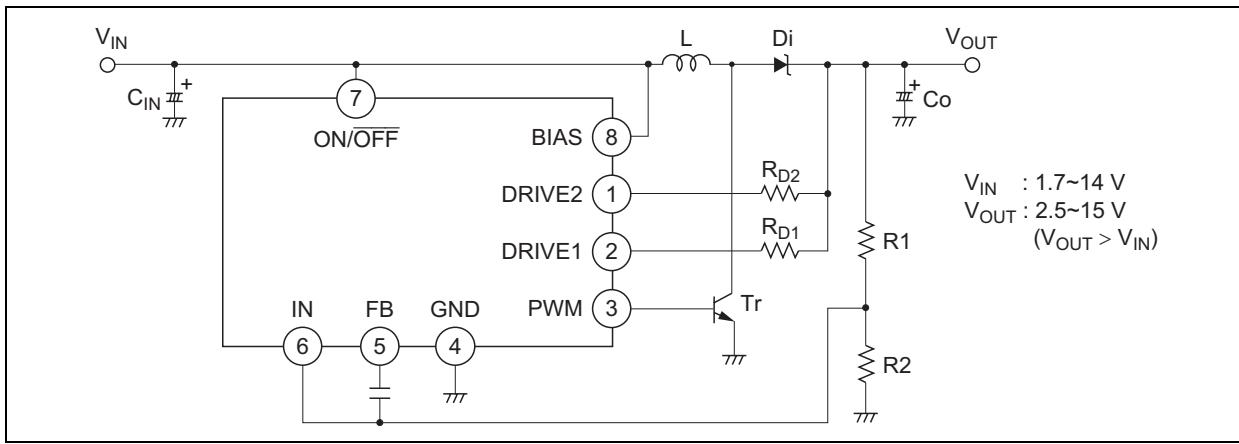
Note: 1. Setting range of BIAS voltage as same as setting range of output voltage.

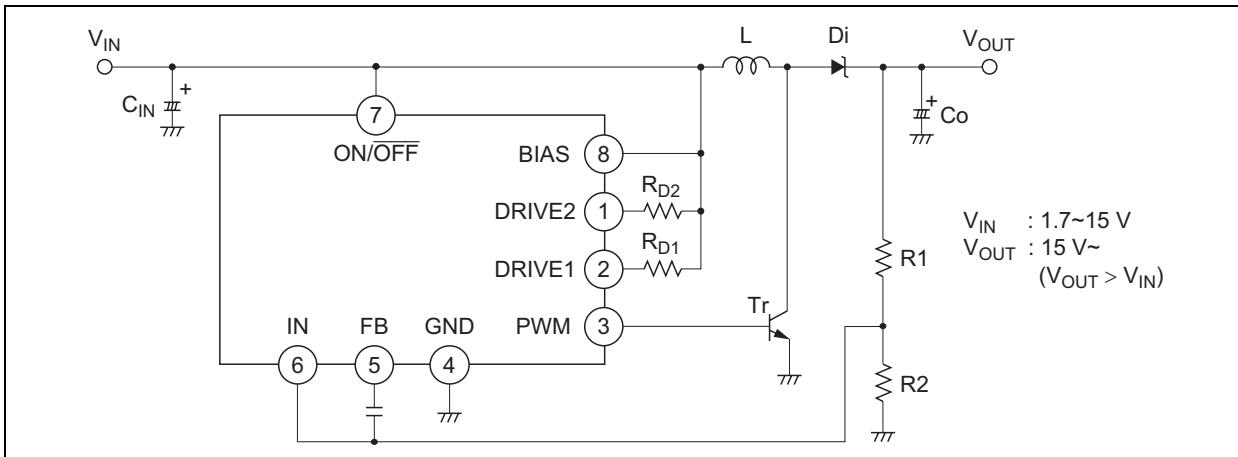
Application Circuit

(1) Standard application circuit

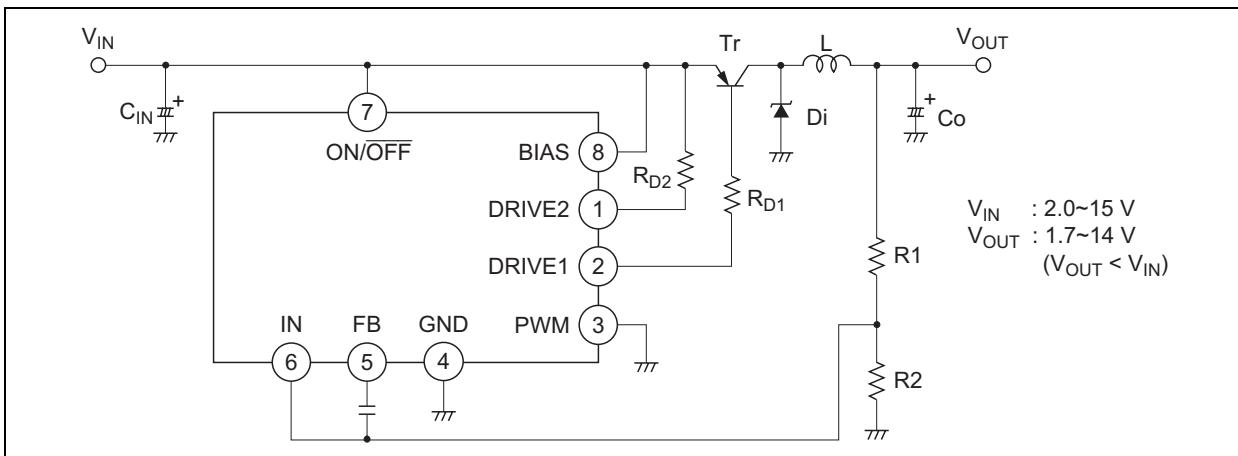


(2) Application circuit 1 ($V_{IN} \geq 1.7\text{ V}$)

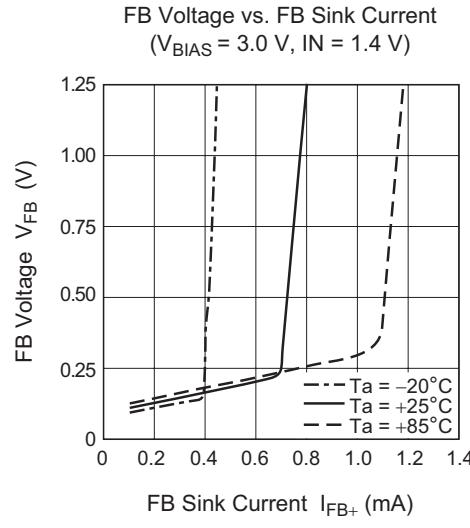
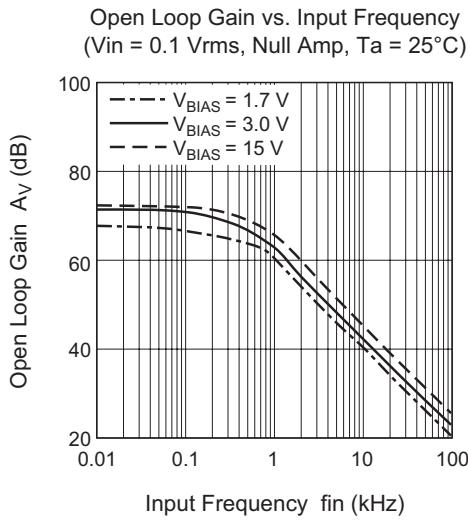
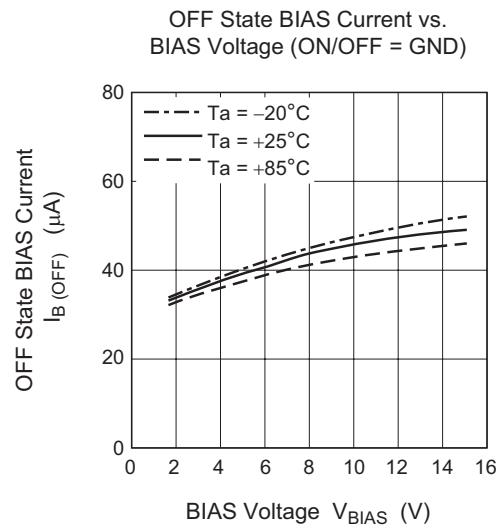
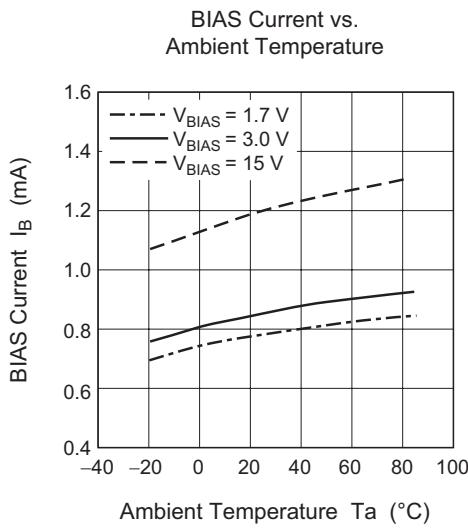
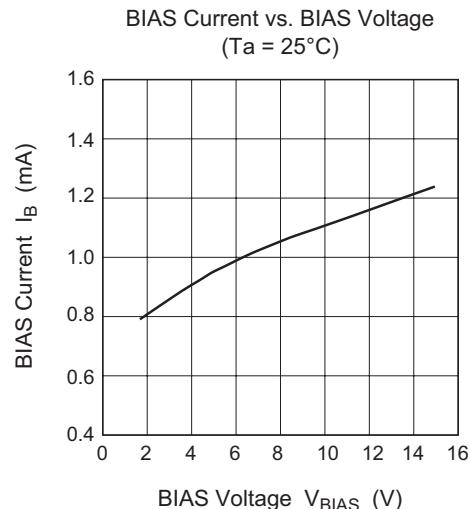
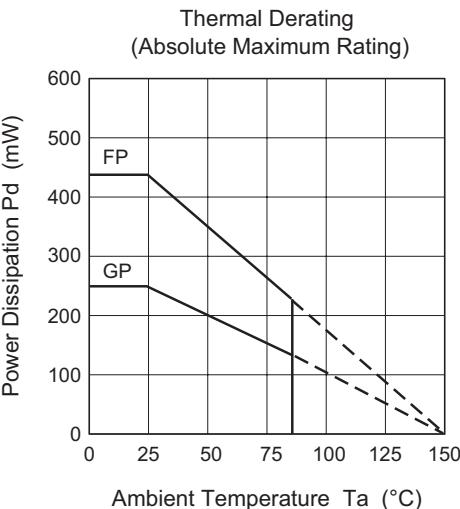


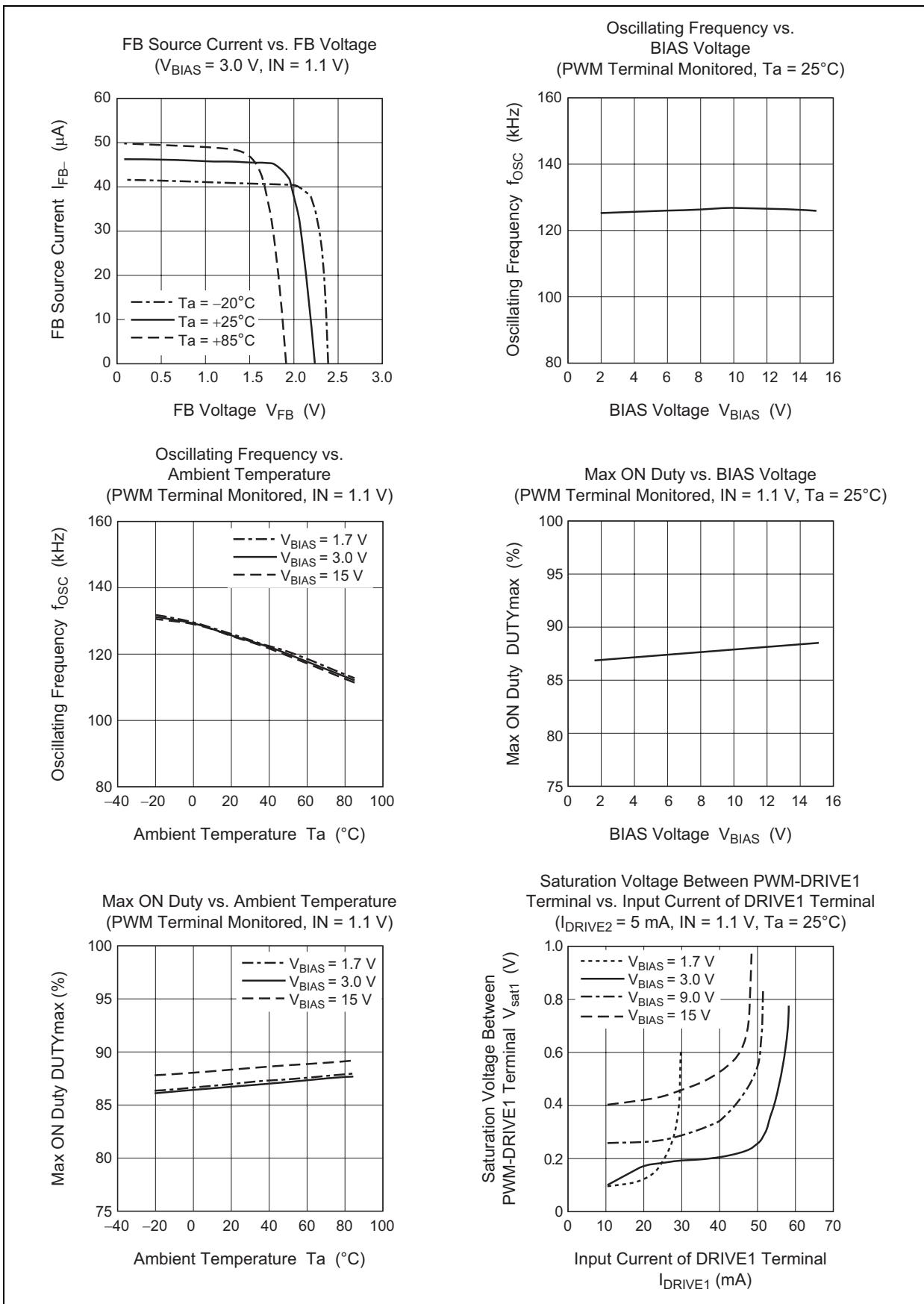
(3) Application circuit 2 ($V_{OUT} > V_{IN}$)

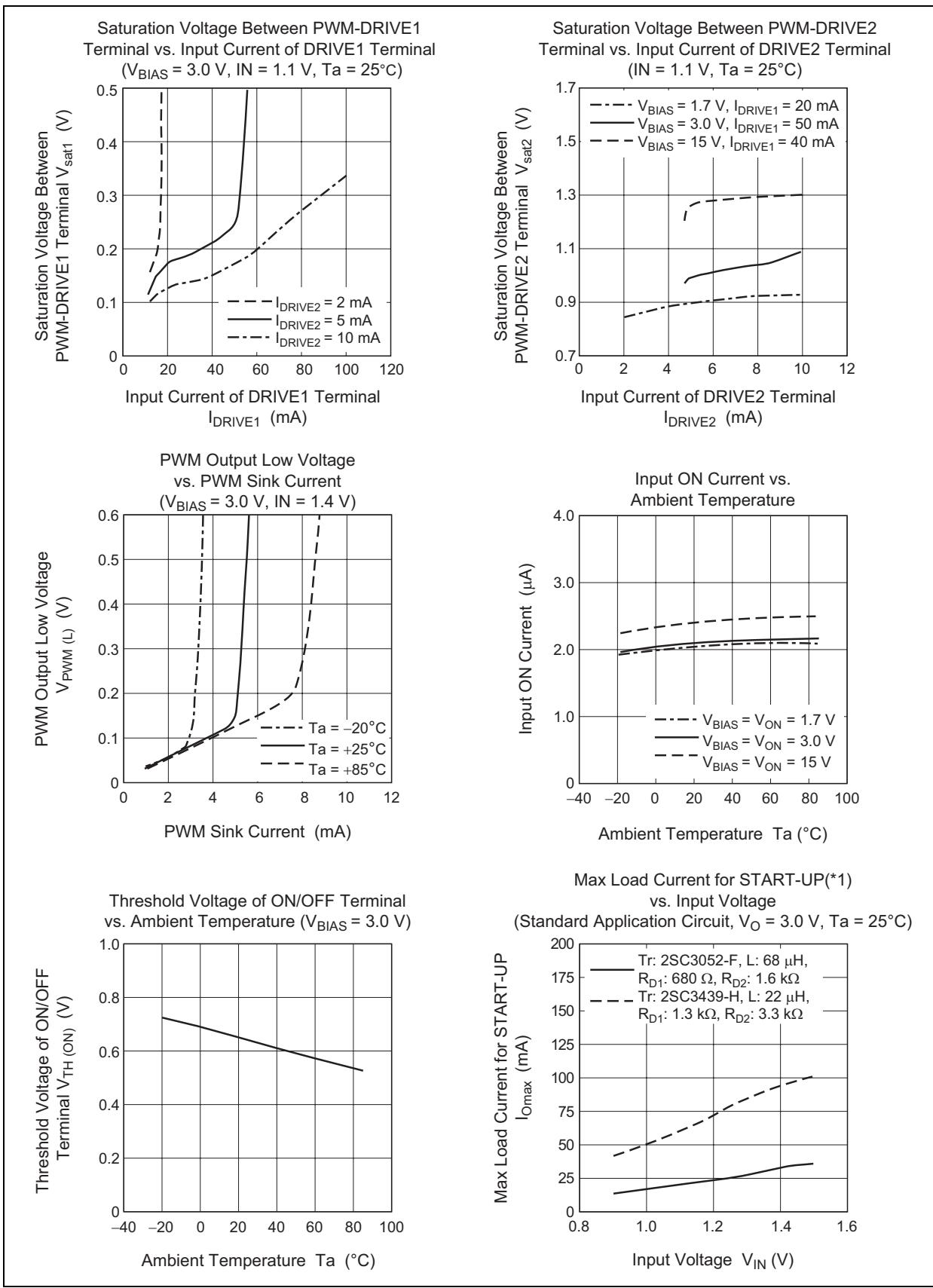
(4) Application circuit for STEP-DOWN circuit

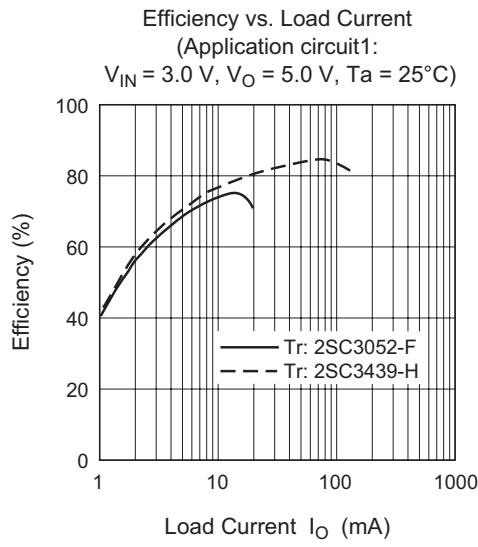
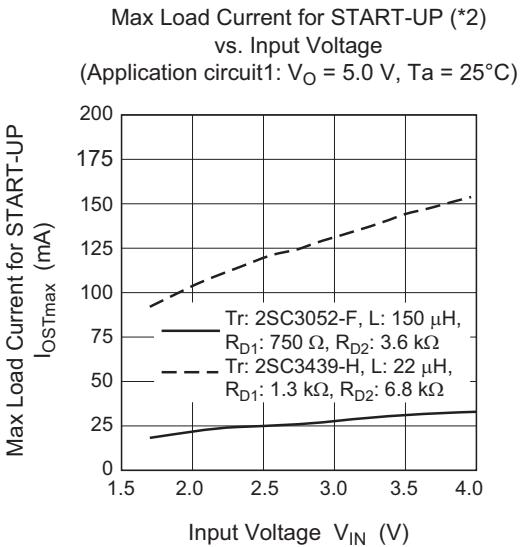
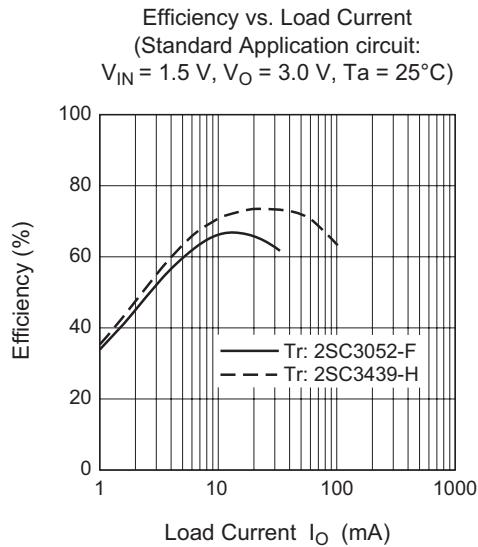


Typical Characteristics









note: 1, 2. These characteristics show the maximum output load current when start-up.
Therefore, output voltage can grow-up to setting voltage less than a curve in the graph when using these external components value.
(2SC3052-F: $h_{FE} = 250$ to 500 , 2SC3439-H: $h_{FE} = 600$ to 1200)

Equation for Constants Calculation

Constants	Standard Application Circuit	Application Circuit 1	Application Circuit 2
$\frac{T_{ON}}{T_{OFF}}$	$\frac{V_O + V_F - V_{IN}}{V_{IN} - V_{CE(sat)}}$	$\frac{V_O + V_F - V_{IN}}{V_{IN} - V_{CE(sat)}}$	$\frac{V_O + V_F - V_{IN}}{V_{IN} - V_{CE(sat)}}$
$T_{ON} + T_{OFF}$	$\frac{1}{f_{OSC}}$	$\frac{1}{f_{OSC}}$	$\frac{1}{f_{OSC}}$
$T_{OFF(MIN)}$	$\frac{T_{ON} + T_{OFF}}{1 + \frac{T_{ON}}{T_{OFF}}}$	$\frac{T_{ON} + T_{OFF}}{1 + \frac{T_{ON}}{T_{OFF}}}$	$\frac{T_{ON} + T_{OFF}}{1 + \frac{T_{ON}}{T_{OFF}}}$
$T_{ON(MAX)}$	$\frac{1}{f_{OSC}} - T_{OFF(MIN)}$	$\frac{1}{f_{OSC}} - T_{OFF(MIN)}$	$\frac{1}{f_{OSC}} - T_{OFF(MIN)}$
I_{pk}	$2 \times \left(1 + \frac{T_{ON}}{T_{OFF}}\right) \times (I_O + I_B)$	$2 \times \left(1 + \frac{T_{ON}}{T_{OFF}}\right) \times I_O$	$2 \times \left(1 + \frac{T_{ON}}{T_{OFF}}\right) \times I_O$
$L(MIN)$	$\frac{(V_{IN} - V_{CE(sat)})^2 \times T_{ON(MAX)}^2 \times f_{OSC}}{2 \times V_O \times (I_O + I_B)}$	$\frac{(V_{IN} - V_{CE(sat)})^2 \times T_{ON(MAX)}^2 \times f_{OSC}}{2 \times V_O \times I_O}$	$\frac{(V_{IN} - V_{CE(sat)})^2 \times T_{ON(MAX)}^2 \times f_{OSC}}{2 \times V_O \times I_O}$
R_1	$\left(\frac{V_O}{V_{REF}} - 1\right) \times R2$	$\left(\frac{V_O}{V_{REF}} - 1\right) \times R2$	$\left(\frac{V_O}{V_{REF}} - 1\right) \times R2$
R_{D1}	$\frac{V_O - (V_{BE} + V_{sat1})}{(I_{pk}/h_{FE}) \times A1}$	$\frac{V_O - (V_{BE} + V_{sat1})}{(I_{pk}/h_{FE}) \times A1}$	$\frac{V_{IN} - (V_{BE} + V_{sat1})}{(I_{pk}/h_{FE}) \times A1}$
R_{D2}	$\frac{V_O - (V_{BE} + V_{sat2})}{(I_{pk}/h_{FE}) \times A2}$	$\frac{V_O - (V_{BE} + V_{sat2})}{(I_{pk}/h_{FE}) \times A2}$	$\frac{V_{IN} - (V_{BE} + V_{sat2})}{(I_{pk}/h_{FE}) \times A2}$

Constants	STEP-DOWN Circuit
$\frac{T_{ON}}{T_{OFF}}$	$\frac{V_O + V_F}{V_{IN} - V_{CE(sat)} - V_O}$
$T_{ON} + T_{OFF}$	$\frac{1}{f_{OSC}}$
$T_{OFF(MIN)}$	$\frac{T_{ON} + T_{OFF}}{1 + \frac{T_{ON}}{T_{OFF}}}$
$T_{ON(MAX)}$	$\frac{1}{f_{OSC}} - T_{OFF(MIN)}$
I_{pk}	$2 \times I_O$
$L(MIN)$	$\frac{(V_{IN} - V_{CE(sat)} - V_O) \times T_{ON(MAX)}}{\Delta I_O}$
R_1	$\left(\frac{V_O}{V_{REF}} - 1\right) \times R2$
R_{D1}	$\frac{V_O - V_{BE} - V_{sat1}}{I_{pk}/h_{FE}}$
R_{D2}	$\frac{V_{IN} - V_{sat2}}{(I_{pk}/h_{FE}) \times A3}$

Note:

- V_F : Forward voltage of external diode.
- $V_{CE(sat)}$: Saturation voltage of external transistor.
- V_{BE} : Voltage between Base - Emitter of external transistor.
- h_{FE} : h_{FE} of external transistor at saturating.
- A1: Ratio of current into DRIVE1 terminal.
(A1 = 0.8~0.9)
- A2: Ratio of current into DRIVE2 terminal.
(A2 = 1 - A1)
- A3: Ratio of current into DRIVE2 terminal.
(A3 = 0.1~0.2)

- Set R_2 to several $k\Omega$ ~ several 10ths $k\Omega$.
- Set current into DRIVE2 terminal more than $100 \mu A$.
 $(I_{pk} / h_{FE}) \times A2 \geq 100 \mu A$, $(I_{pk} / h_{FE}) \times A3 \geq 100 \mu A$
- Set ΔI_O to 1/5 ~ 1/3 of maximum load current
- The maximum rating of current of external parts (transistor, diode and inductor) are 1.5 to 2 times of I_{pk} .

Package Dimensions

JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
P-SOP8-4.4x5-1.27	PRSP0008DA-A	8P2S-A	0.07g

NOTE)
1. DIMENSIONS **1** AND **2** DO NOT INCLUDE MOLD FLASH.
2. DIMENSION **3** DOES NOT INCLUDE TRIM OFFSET.

Reference Symbol	Dimension in Millimeters		
	Min	Nom	Max
D	4.8	5.0	5.2
E	4.2	4.4	4.6
A ₂	—	1.5	—
A ₁	0.05	—	—
A	—	—	1.9
b _p	0.35	0.4	0.5
c	0.13	0.15	0.2
θ	0°	—	10°
H _E	5.9	6.2	6.5
e	1.12	1.27	1.42
y	—	—	0.1
L	0.2	0.4	0.6

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