TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC74AC151P,TC74AC151F,TC74AC151FN

### 8-Channel Multiplexer

The TC74AC151 is an advanced high speed CMOS 8-CHANNEL MULTIPLEXER fabricated with silicon gate and double-layer metal wiring C2MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

One of eight date input signals (D0-D7) is selected by decoding of the three-bit address input (A, B, C). The selected data appears on two outputs: non-inverting (Y) and inverting (W).

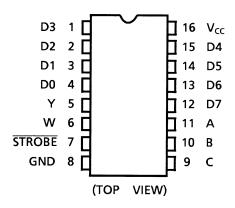
The  $\overline{STROBE}$  input provides two output conditions; a low level on the  $\overline{STROBE}$  input transferrs the selected data to the outputs. A high level on the  $\overline{STROBE}$  input sets the Y output low and the W output high without regard to the data or select input conditions.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

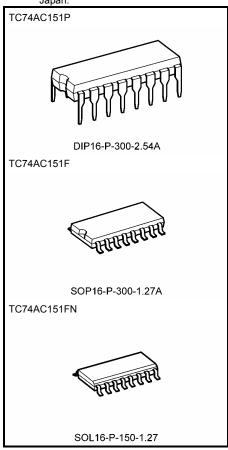
#### **Features**

- High speed:  $t_{pd} = 5.3 \text{ ns (typ.)}$  at  $V_{CC} = 5 \text{ V}$
- Low power dissipation:  $ICC = 8 \mu A \text{ (max)}$  at  $Ta = 25^{\circ}C$
- High noise immunity: VNIH = VNIL = 28% VCC (min)
- Symmetrical output impedance:  $|I_{OH}| = I_{OL} = 24$  mA (min) Capability of driving 50  $\Omega$  transmission lines.
- Balanced propagation delays:  $t_{pLH} \approx t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 5.5 V
- Pin and function compatible with 74F151

#### **Pin Assignment**



Note: xxxFN (JEDEC SOP) is not available in Japan.

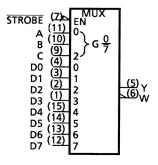


Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

1 2007-10-01

# **IEC Logic Symbol**

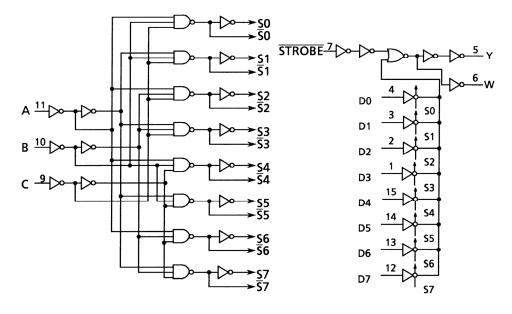


## **Truth Table**

	I	Outputs				
Select			STROBE	Y	W	
С	В	Α	SIROBE	ī	VV	
Х	Х	Х	Н	L	Н	
L	L	L	L	D0	D0	
L	L	Н	L	D1	D1	
L	Н	L	L	D2	D2	
L	Н	Н	L	D3	D̄3	
Н	L	L	L	D4	D̄4	
Н	L	Н	L	D5	D̄5	
Н	Н	L	L	D6	<del>□</del> 6	
Н	Н	Н	L	D7	D7	

X: Don't care

# **System Diagram**



2 2007-10-01

## **Absolute Maximum Ratings (Note 1)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	-0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	$-0.5$ to $V_{CC} + 0.5$	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	lok	±50	mA
DC output current	lout	±50	mA
DC V <sub>CC</sub> /ground current	Icc	±100	mA
Power dissipation	P <sub>D</sub>	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

## **Operating Ranges (Note)**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	−40 to 85	°C	
Input rise and fall time	dt/dV	0 to 100 ( $V_{CC} = 3.3 \pm 0.3 \text{ V}$ )	ns/V	
input noc and fail time	αναν	0 to 20 (V <sub>CC</sub> = 5 $\pm$ 0.5 V)	115/ V	

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.



## **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteristics	Symbol				V <sub>CC</sub>	Min	Тур.	Max	Min	Max	Offic
	V <sub>IH</sub>	_		2.0	1.50	_	_	1.50	_		
High-level input voltage				3.0	2.10	_	_	2.10	_	V	
<u> </u>					5.5	3.85	_	_	3.85	_	
					2.0	_	_	0.50	_	0.50	
Low-level input voltage	$V_{IL}$	_		3.0	_	_	0.90	_	0.90	V	
					5.5	_	_	1.65	_		1.65
	V <sub>ОН</sub>				2.0	1.9	2.0	_	1.9	_	
		V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	$I_{OH} = -50 \mu A$		3.0	2.9	3.0	_	2.9	_	
High-level output				4.5	4.4	4.5	_	4.4	_	V	
voltage			$I_{OH} = -4 \text{ mA}$		3.0	2.58	_	_	2.48	_	V
			$I_{OH} = -24 \text{ mA}$		4.5	3.94	_	_	3.80	_	
			$I_{OH} = -75 \text{ mA}$	(Note)	5.5	_	_	_	3.85	_	
	V <sub>OL</sub>	VIN = VIH or VIL	$I_{OL} = 50 \ \mu A$ $I_{OL} = 12 \ mA$ $I_{OL} = 24 \ mA$		2.0	_	0.0	0.1	_	0.1	
				3.0	_	0.0	0.1	_	0.1		
Low-level output					4.5		0.0	0.1	_	0.1	V
voltage					3.0	_	_	0.36	_	0.44	·
					4.5	_	_	0.36	_	0.44	
			$I_{OL} = 75 \text{ mA}$	(Note)	5.5		_	_		1.65	
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$ or GND		5.5	_	_	±0.1	_	±1.0	μΑ	
Quiescent supply current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	8.0	_	80.0	μΑ	

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

AC Characteristics ( $C_L = 50$  pF,  $R_L = 500 \Omega$ , input:  $t_r = t_f = 3$  ns)

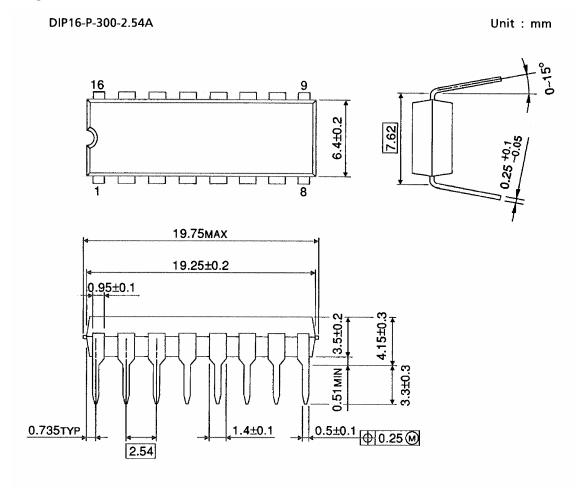
Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	J		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	
Propagation delay time (D-Y, W)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_ _	10.7 6.6	19.3 10.5	1.0 1.0	22.0 12.0	ns
Propagation delay time (A, B, C-Y, W)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_	13.3 8.2	23.7 13.0	1.0 1.0	27.0 14.8	ns
Propagation delay time (ST -Y, W)	t <sub>pLH</sub>	_	$3.3 \pm 0.3$ $5.0 \pm 0.5$	_	8.6 5.6	15.3 9.6	1.0 1.0	18.0 11.0	ns
Input capacitance	C <sub>IN</sub>	_		_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub> (Note)	_		_	68	_	_	_	pF

Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC} (opr) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

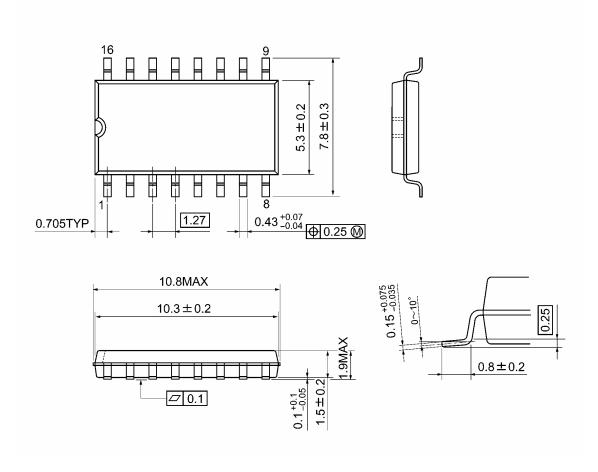
# **Package Dimensions**



Weight: 1.00 g (typ.)

# **Package Dimensions**

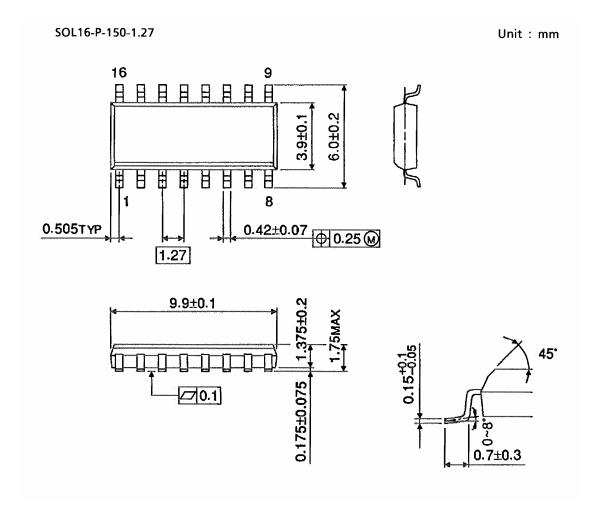
SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)



# **Package Dimensions (Note)**



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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9 2007-10-01