Features

- Industry Standard Architecture
- Low-cost Easy-to-use Software Tools High-speed, Electrically-erasable Programmable Logic Devices - 7.5 ns Maximum Pin-to-pin Delay
- Several Power Saving Options

Device	I _{CC} , Standby	I _{CC} , Active	
ATF22V10B	85 mA	90 mA	
ATF22V10BQ ⁽¹⁾	35 mA	40 mA	
ATF22V10BQL ⁽²⁾	5 mA	20 mA	

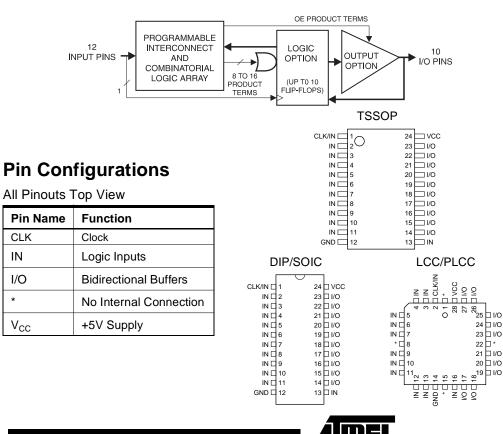
1. The shaded devices are obsolete. Suggested replacement: ATF22V10CQ Notes: The shaded devices are obsolete. Suggested replacement: ATF22V10CQZ

- CMOS and TTL Compatible Inputs and Outputs - Input and I/O Pull-up Resistors
- Advanced Flash Technology
 - Reprogrammable
 - 100% Tested
- High-reliability CMOS Process
 - 20-year Data Retention
 - 100 Erase/Write Cycles
 - 2,000V ESD Protection
 - 200 mA Latchup Immunity
- Full Military, Commercial, and Industrial Temperature Ranges
- **Dual-in-line and Surface Mount Packages in Standard Pinouts**
- PCI Compliant

IN

*

Logic Diagram



Highperformance **EE PLD**

ATF22V10B

Rev. 0250L-PLD-06/03



Description

The ATF22V10B is a high-performance CMOS (electrically-erasable) programmable logic device (PLD) which utilizes Atmel's proven electrically-erasable Flash memory technology. Speeds down to 7.5 ns and power dissipation as low as 10 mA are offered. All speed ranges are specified over the full 5V \pm 10% range for military and industrial

Absolute Maximum Ratings*

Temperature Under Bias55°C to +125°C
Storage Temperature65°C to +150°C
Voltage on Any Pin with Respect to Ground2.0V to +7.0V ⁽¹⁾
Voltage on Input Pins with Respect to Ground During Programming2.0V to +14.0V ⁽¹⁾
Programming Voltage with Respect to Ground2.0V to +14.0V ⁽¹⁾

temperature ranges, and 5V \pm 5% for commercial temperature ranges.

Several low-power options allow selection of the best solution for various types of power-limited applications. Each of these options significantly reduces total system power and enhances system reliability.

- *NOTICE: Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.
- Note: 1. Minimum voltage is -0.6V DC, which may undershoot to -2.0V for pulses of less than 20 ns. Maximum output pin voltage is V_{CC} + 0.75V DC, which may overshoot to 7.0V for pulses of less than 20 ns.

DC and AC Operating Conditions

	Commercial	Industrial	Military
Operating Temperature	0°C - 70°C (Ambient)	-40°C - 85°C (Ambient)	-55°C - 125°C (Case)
V _{CC} Power Supply	5V ± 5%	5V ± 10%	5V ± 10%

Note: 1. The shaded devices are obsolete.

ATF22V10B

DC Characteristics

Symbol	Parameter	Condition			Min	Тур	Max	Units
I _{IL}	Input or I/O Low Leakage Current	$0 \le V_{IN} \le V_{IL}$ (Max)				-35	-100	μA
I _{IH}	Input or I/O High Leakage Current	$3.5 \le V_{IN} \le V_{CC}$					10	μA
	Power Supply Current,	V _{CC} = Max,		Com.		85	120	mA
I _{cc}	Standby	V _{IN} = Max, Outputs Open	B-7	Ind., Mil.		85	140	mA
			B-10	Com./Ind.		85/85	120/140	mA
				Mil.		85	140	mA
			B-15	Com./Ind.		65/65	90/115	mA
		V _{CC} = Max,	D-15	Mil.		65	115	mA
I _{CC}	Power Supply Current, Standby	V _{IN} = Max,	B-25	Com.		65	90	mA
olandoy		Outputs Open	D-20	Ind., Mil.		65	115	mA
			BQ-15	Com.		35	55	mA
			BQL-20, -25	Com.		5	10	mA
				Ind., Mil.		5	15	mA
			B-7 B-10	Com.		90	120	mA
				Mil., Ind.		90	145	mA
				Com./Ind.		90/90	120/145	mA
				Mil.		90	145	mA
		V _{CC} = Max,	B-15	Com./Ind.		65/65	90/120	mA
I _{CC2}	Clocked Power Supply Current	Outputs Open, f = 15 MHz		Mil.		65	120	mA
	Supply Sullent		B-25	Com.		65	90	mA
				Ind., Mil.		65	120	mA
			BQ-15	Com.		40	60	mA
				Com.		20	50	mA
			BQL-20, -25	Ind., Mil.		20	70	mA
I _{OS} ⁽¹⁾	Output Short Circuit Current	V _{OUT} = 0.5V		l			-130	mA
V _{IL}	Input Low Voltage				-0.5		0.8	V
V _{IH}	Input High Voltage				2.0		V _{CC} + 0.75	V
		$V_{IN} = V_{IH} \text{ or } V_{IL},$	I _{OL} = 16 mA	Com., Ind.			0.5	V
V _{OL}	Output Low Voltage	$V_{\rm CC} = Min$	I _{OL} = 12 mA	Mil.			0.5	V
V _{OH}	Output High Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL},$ $V_{CC} = Min$	I _{OH} = -4.0 mA		2.4			V

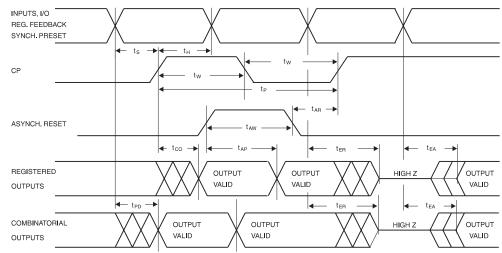
Notes: 1. Not more than one output at a time should be shorted. Duration of short circuit test should not exceed 30 sec.

2. The shaded devices are obsolete.





AC Waveforms⁽¹⁾



Note: 1. Timing measurement reference is 1.5V. Input AC driving levels are 0.0V and 3.0V, unless otherwise specified.

AC Characteristics⁽¹⁾

			7		10	-1	15	-2	20	-2	25	
Symbol	Parameter	Min	Мах	Min	Мах	Min	Max	Min	Max	Min	Мах	Units
t _{PD}	Input or Feedback to Combinatorial Output	3	7.5	3	10	3	15	3	20	3	25	ns
t _{co}	Clock to Output	2	4.5 ⁽²⁾	2	6.5	2	8	2	12	2	15	ns
t _{CF}	Clock to Feedback		2.5		2.5		2.5		8		13	ns
t _S	Input or Feedback Setup Time	3.5		4.5		10			14	15		ns
t _H	Hold Time	0		0		0		0		0		ns
	External Feedback 1/(t _S + t _{CO})	125 ⁽³⁾		90		55.5		38.5		33.3		MHz
f _{MAX}	No Feedback $1/(t_{S} + t_{CF})$			142		69		45.5		40		MHz
				142		83.3				38.5		MHz
t _W	Clock Width (t_{WL} and t_{WH})	3		3.5		6		10		13		ns
t _{EA}	Input or I/O to Output Enable	3	7.5	3	10	3	15	3	20	3	25	ns
t _{ER}	Input or I/O to Output Disable	3	7.5	3	9	3	15	3	20	3	25	ns
t _{AP}	Input or I/O to Asynchronous Reset of Register	3	10	3	12	3	20	3	22	3	25	ns
t _{AW}	Asynchronous Reset Width	7		8		15		20		25		ns
t _{AR}	Asynchronous Reset Recovery Time	5		6		10		20		25		ns
t _{SP}	Setup Time, Synchronous Preset	4.5		6		10		14		15		ns
t _{SPR}	Synchronous Preset to Clock Recovery Time	5		8		10		14		15		ns

Notes: 1. See ordering information for valid part numbers.

ATF22V10B

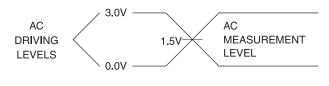
2. 5.5 ns for DIP package devices.

3. 111 MHz for DIP package devices.

4. The shaded devices are obsolete.

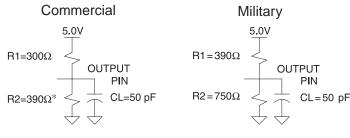
Downloaded from Elcodis.com electronic components distributor

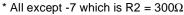
Input Test Waveforms and Measurement Levels



t_R, t_F < 3 ns

Output Test Loads





Pin Capacitance

 $f = 1 \text{ MHz}, T = 25^{\circ}C^{(1)}$

	Тур	Мах	Units	Conditions
C _{IN}	5	8	pF	V _{IN} = 0V
C _{OUT}	6	8	pF	V _{OUT} = 0V

Note: 1. Typical values for nominal supply voltage. This parameter is only sampled and is not 100% tested.

Power-up Reset

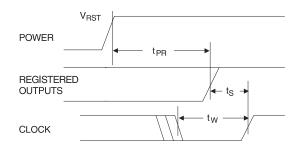
The registers in the ATF22V10Bs are designed to reset during power-up. At a point delayed slightly from V_{CC} crossing V_{RST}, all registers will be reset to the low state. The output state will depend on the polarity of the output buffer.

This feature is critical for state machine initialization. However, due to the asynchronous nature of reset and the uncertainty of how V_{CC} actually rises in the system, the following conditions are required:

- 1. The V_{CC} rise must be monotonic,
- 2. After reset occurs, all input and feedback setup times must be met before driving the clock pin high, and
- 3. The clock must remain stable during t_{PR} .

Preload of Registered Outputs

The ATF22V10B's registers are provided with circuitry to allow loading of each register with either a high or a low. This feature will simplify testing since any state can be forced into the registers to control test sequencing. A JEDEC file with preload is generated when a source file with vectors is compiled. Once downloaded, the JEDEC file preload sequence will be done automatically by most of the approved programmers after the programming.



Parameter	Description	Тур	Max	Units	
t _{PR}	Power-up Reset Time	600	1,000	ns	
V _{RST}	Power-up Reset Voltage	3.8	4.5	V	

Security Fuse Usage

A single fuse is provided to prevent unauthorized copying of the ATF22V10B fuse patterns. Once programmed, fuse verify and preload are inhibited. However, the 64-bit User Signature remains accessible.

The security fuse should be programmed last, as its effect is immediate.





Electronic Signature Word

There are 64 bits of programmable memory that are always available to the user, even if the device is secured. These bits can be used for user-specific data.

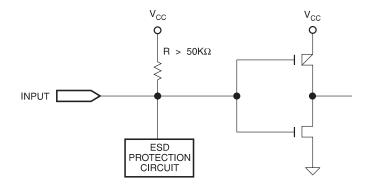
Programming/Erasing

Programming/erasing is performed using standard PLD programmers. See *CMOS PLD Programming Hardware and Software Support* for information on software/programming.

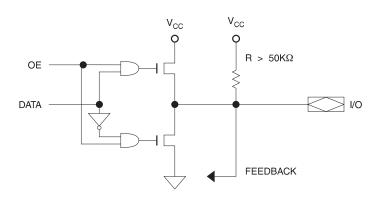
Input and I/O Pull-ups

All ATF22V10B family members have internal input and I/O pull-up resistors. Therefore, whenever inputs or I/Os are not being driven externally, they will float to V_{CC} . This ensures that all logic array inputs are at known states. These are relatively weak active pull-ups that can easily be overdriven by TTL-compatible drivers (see input and I/O diagrams below).

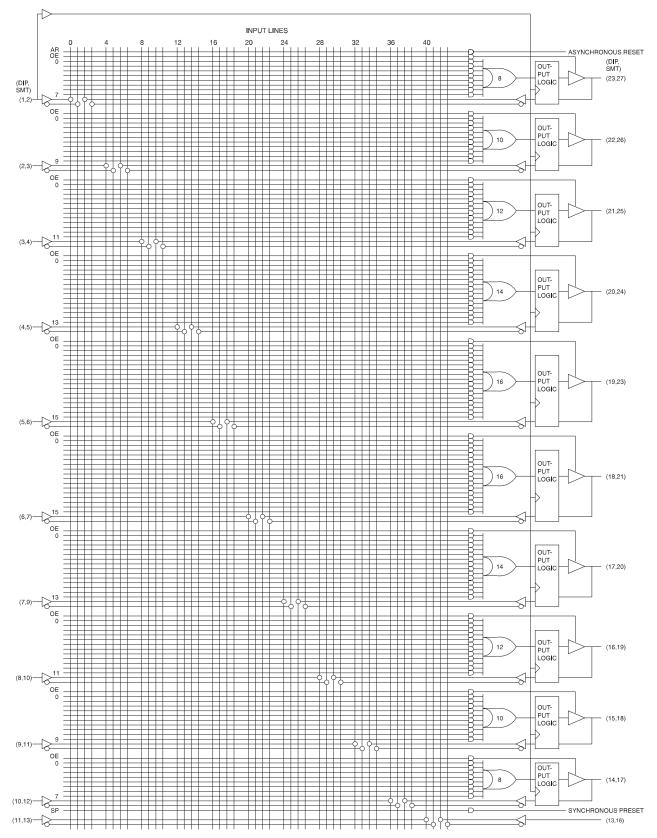
Input Diagram



I/O Diagram

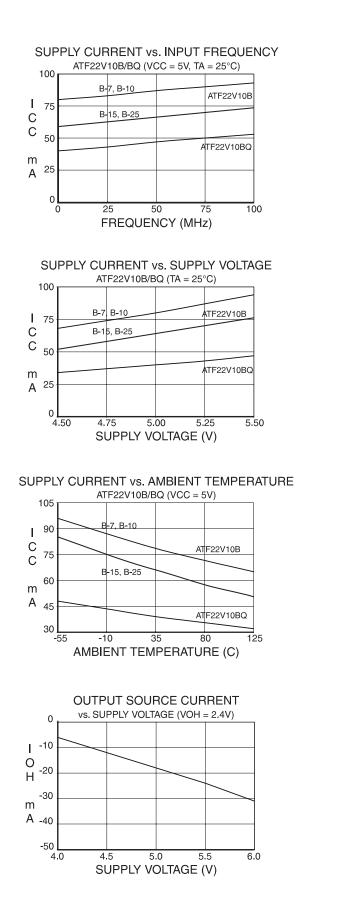


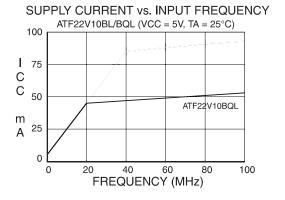
Functional Logic Diagram ATF22V10B



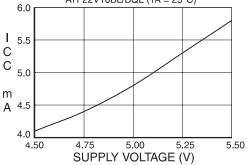




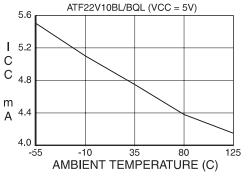


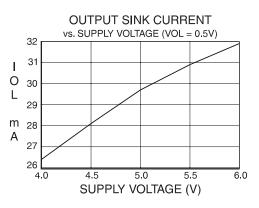


SUPPLY CURRENT vs. SUPPLY VOLTAGE ATF22V10BL/BQL (TA = 25°C)



SUPPLY CURRENT vs. AMBIENT TEMPERATURE

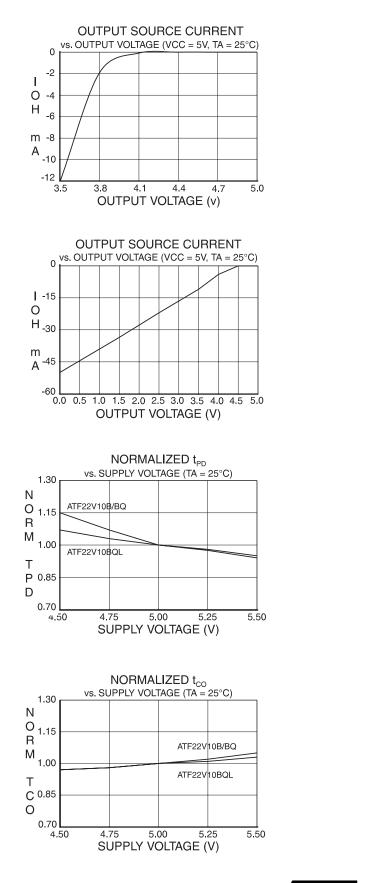


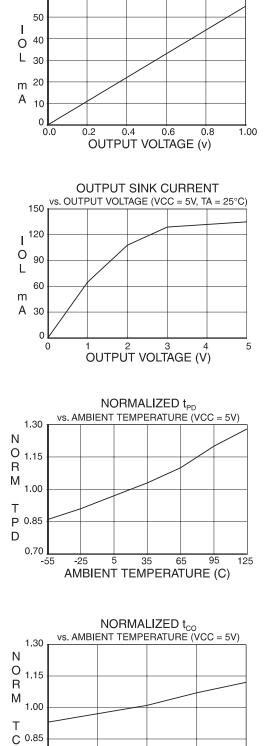


OUTPUT SINK CURRENT

vs. OUTPUT VOLTAGE (VCC = 5V, TA = 25°C)

60





0

0.70

-55

-10

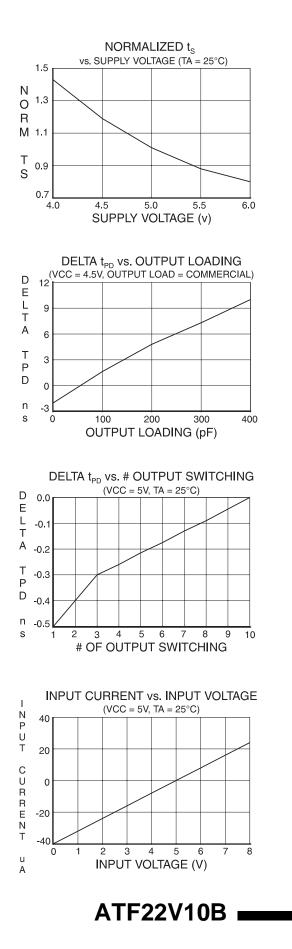
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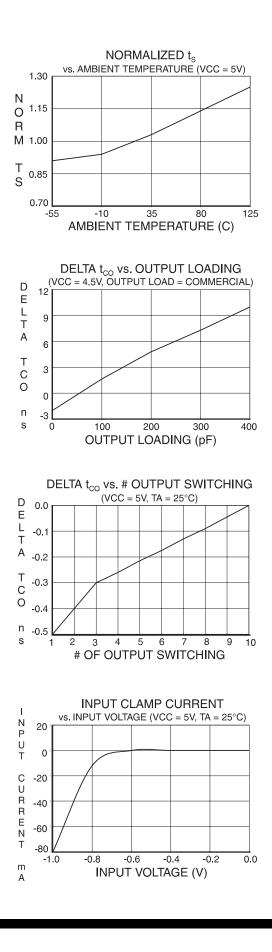
AMBIENT TEMPERATURE (C)

80

125







ATF22V10B Ordering Information

t _{PD} (ns)	t _s (ns)	t _{co} (ns)	Ordering Code	Package	Operation Range	
7.5	3.5	4.5	ATF22V10B-7JC ⁽¹⁾ ATF22V10B-7PC ⁽¹⁾ ATF22V10B-7SC ⁽¹⁾ ATF22V10B-7XC ⁽¹⁾	28J 24P3 24S 24X	Commercial (0°C to 70°C)	
10 4.5	4.5	6.5	ATF22V10B-10JC ⁽¹⁾ ATF22V10B-10PC ⁽¹⁾ ATF22V10B-10SC ⁽¹⁾ ATF22V10B-10XC ⁽¹⁾	28J 24P3 24S 24X	Commercial (0°C to 70°C)	
			ATF22V10B-10JI ⁽¹⁾ ATF22V10B-10PI ⁽¹⁾ ATF22V10B-10SI ⁽¹⁾ ATF22V10B-10XI ⁽¹⁾	28J 24P3 24S 24X	Industrial (-40°C to 85°C)	
			ATF22V10B-10GM/883 ATF22V10B-10NM/883	24D3 28L	Military/883C (-55°C to 125°C) Class B, Fully Compliant	
			5962-89841 06LA 5962-89841 063X	24D3 28L	Military (-55°C to 125°C) Class B, Fully Compliant	
15	15 10 8	10	8	ATF22V10B-15JC ⁽¹⁾ ATF22V10B-15PC ⁽¹⁾ ATF22V10B-15SC ⁽¹⁾ ATF22V10B-15XC ⁽¹⁾	28J 24P3 24S 24X	Commercial (0°C to 70°C)
			ATF22V10B-15JI ⁽¹⁾ ATF22V10B-15PI ⁽¹⁾ ATF22V10B-15SI ⁽¹⁾ ATF22V10B-15XI ⁽¹⁾	28J 24P3 24S 24X	Industrial (-40°C to 85°C)	
			ATF22V10B-15GM/883 ATF22V10B-15NM/883	24D3 28L	Military/883C (-55°C to 125°C) Class B, Fully Compliant	
			5962-89841 03LA 5962-89841 033X	24D3 28L	Military (-55°C to 125°C) Class B, Fully Compliant	
25 15	15	ATF22V10B-25JC ATF22V10B-25PC ATF22V10B-25SC ATF22V10B-25XC	28J 24P3 24S 24X	Commercial (0°C to 70°C)		
			ATF22V10B-25JI ATF22V10B-25PI ATF22V10B-25SI ATF22V10B-25XI	28J 24P3 24S 24X	Industrial (-40°C to 85°C)	

Notes: 1. Recommend ATF22V10C versions.

2. The shaded devices are obsolete.



ATF22V10BQ(L) Ordering Information

t _{PD} (ns)	t _s (ns)	t _{co} (ns)	Ordering Code	Package	Operation Range
15	10	8	ATF22V10BQ-15JC ⁽¹⁾	28J	Commercial
			ATF22V10BQ-15PC ⁽¹⁾	24P3	(0°C to 70°C)
			ATF22V10BQ-15SC ⁽¹⁾	24S	
			ATF22V10BQ-15XC ⁽¹⁾	24X	
20	14	12	ATF22V10BQL-20JC ⁽¹⁾	28J	Commercial
			ATF22V10BQL-20PC ⁽¹⁾	24P3	(0°C to 70°C)
			ATF22V10BQL-20SC ⁽¹⁾	24S	
			ATF22V10BQL-20XC ⁽¹⁾	24X	
			ATF22V10BQL-20JI ⁽¹⁾	28J	Industrial
			ATF22V10BQL-20PI ⁽¹⁾	24P3	(-40°C to 85°C)
			ATF22V10BQL-20SI ⁽¹⁾	24S	
			ATF22V10BQL-20XI ⁽¹⁾	24X	
			ATF22V10BQL-20GM/883	24D3	Military/883C
			ATF22V10BQL-20NM/883	28L	(-55°C to 125°C)
					Class B, Fully Compliant
			5962-89841 14 LA	24D3	Military
			5962-89841 14 3X	28L	(-55°C to 125°C)
					Class B, Fully Compliant
25	15	15	ATF22V10BQL-25JC	28J	Commercial
			ATF22V10BQL-25PC	24P3	(0°C to 70°C)
			ATF22V10BQL-25SC	24S	
			ATF22V10BQL-25XC	24X	
			ATF22V10BQL-25JI	28J	Industrial
			ATF22V10BQL-25PI	24P3	(-40°C to 85°C)
			ATF22V10BQL-25SI	24S	
			ATF22V10BQL-25XI	24X	
			ATF22V10BQL-25GM/883	24D3	Military/883C
			ATF22V10BQL-25NM/883	28L	(-55°C to 125°C)
					Class B, Fully Compliant
			5962-89841 13 LA	24D3	Military
			5962-89841 13 3X	28L	(-55°C to 125°C)
					Class B, Fully Compliant

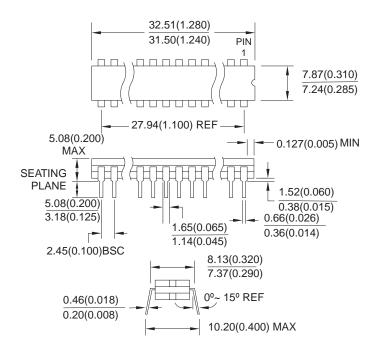
Notes: 1. Recommend ATF22V10CQ and ATF22V10CQZ

2. The shaded devices are obsolete.

Package Information

24D3

24D3, 24-lead, 0.300"Wide. Non-windowed, Ceramic Dual Inline Parkage (Cerdip) Dimensions in Millimeters and (Inches)* MIL-STD-1835 D-9 CONFIG A (Glass Sealed)



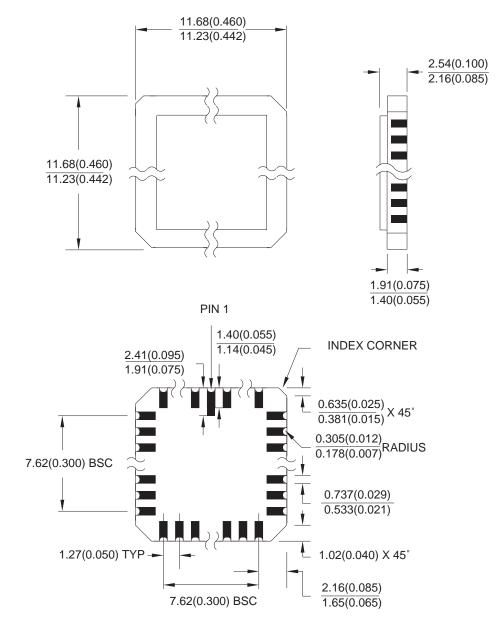
*Controlling dimension: Inches

REV. A 04/11/2001





28L, 28-pad, Non-windowed, Ceramic lid, Leadless Chip Carrier (LCC) Dimensions in Millimeters and (Inches)* MIL-STD-1835 C-4



*Controlling dimension: Inches



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