

BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC494$

SWITCHING REGULATOR CONTROL CIRCUIT

DESCRIPTION

The μ PC494 is an inverter control unit which provides all the control circuitry for PWM type switching regulators. Included in this device is the voltage reference, dual error amplifiers, oscillator, pulse width modulator, pulse steering flip flop, dual alternating output switches and dead-time control.

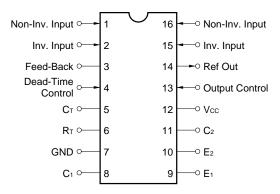
FEATURES

- Complete PWM power control circuit.
- Adjustable dead-time (0 to 100%).
- No double pulsing of same output during load transient condition.
- Dual error amplifiers have wide common mode input voltage capability (-0.3 V to Vcc-2 V).
- Circuit architecture provides easy synchronization.
- Uncommitted outputs for 250-mA sink or source.
- With miss-operation prevention circuit for low level supply voltage.
- Full pin-compatible TL494C.

★ ORDERING INFORMATION

Part Number	Package
μPC494C	16-pin plastic DIP (7.62 mm (300))
μ PC494G	16-pin plastic SOP (9.53 mm (375))
μ PC494GS	16-pin plastic SOP (7.62 mm (300))

★ PIN CONFIGURATION (Top View)



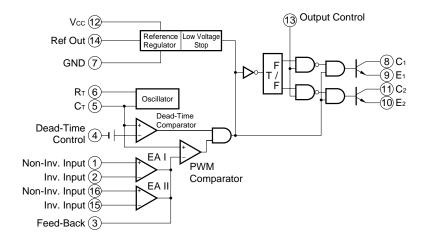
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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

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BLOCK DIAGRAM



★ ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise noted)

Characteristics	Symbol	μPC494C	μPC494G	μPC494GS	Unit
Supply Voltage	Vcc	-0.3 to +41	-0.3 to +41	-0.3 to +41	V
Error Amplifier Input Voltage	Vісм	-0.3 to Vcc + 0.3	-0.3 to Vcc + 0.3	-0.3 to Vcc + 0.3	V
Output Voltage	Vcer	-0.3 to +41	-0.3 to +41	-0.3 to +41	V
Output Current	lc	250	250	250	mA
Total Power Dissipation	P⊤	1000	780 ^{Note}	650 ^{Note}	mW
Operating Ambient Temperature	TA	-20 to +85	-20 to +85	-20 to +85	°C
Storage Temperature	T_{stg}	-65 to +150	-65 to +150	-65 to +150	°C

Note With 25 cm² x 1.6 mm glass-epoxy substrate.

★ Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

RECOMMENDED OPERATING CONDITIONS

Characteristics	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	Vcc	7		40	V
Output Voltage	Vcer	-0.3		40	V
Output Current	lc			200	mA
Error Amplifier Sink Current	Іоамр			-0.3	mA
Timing Capacitor	Ст	0.47		10000	nF
Timing Resistance	R⊤	1.8		500	kΩ
Oscillation Frequency	fosc	1		300	kHz
Operating Temperature	Topt	-20		+70	°C

★ Caution The recommended operating range may be exceeded without causing any problems provided that the absolute maximum ratings are not exceeded. However, if the device is operated in a way that exceeds the recommended operating conditions, the margin between the actual conditions of use and the absolute maximum ratings is small, and therefore thorough evaluation is necessary. The recommended operating conditions do not imply that the device can be used with all values at their maximum values.

★ ELECTRICAL SPECIFICATIONS (Vcc = 15 V, f = 10 kHz, -20 ≤ TA ≤ +70°C, unless otherwise noted)

(1/2)

Block	Characteristic	cs	Symbol	Conditions	MIN.	TYP.	MAX.	(1/2) Unit
Reference	Output Voltage		V _{REF}	IREF = 1 mA, T _A = 25°C	4.75	5	5.25	V
Section	Line Regulation		REGIN	7 V ≤ Vcc ≤ 40 V.	7.70	8	25	mV
				IREF = 1 mA, T _A = 25°C				
	Load Regulation		REG∟	$1 \text{ mA} \leq I_{REF} \leq 10 \text{ mA},$ $T_A = 25^{\circ}\text{C}$		1	15	mV
	Tomporature Coefficient		ΔVref /ΔT	$-20^{\circ}\text{C} \le \text{T}_{A} \le +85^{\circ}\text{C},$		0.01	0.03	%/°C
		Temperature Coefficient		-20°C ≤ TA ≤ +85°C, IREF = 1 mA		0.01	0.03	76/°C
	Short Circuit Output Current Note1		Ishort	VREF = 0 V		50		mA
Oscillator Section	Frequency		fosc	$C_T = 0.01 \ \mu F,$ $R_T = 12 \ k\Omega, T_A = 25^{\circ}C$		10		kHz
	Standard Deviation of Frequency Note2			7 V ≤ V _{CC} ≤ 40 V, T _A = 25°C, C _T , R _T , const.		10		%
	Frequency Change with			0°C ≤ T _A ≤ 70°C,		1	2	%
	Temperature			$C_T = 0.01 \ \mu F,$ $R_T = 12 \ k\Omega$				
	Frequency Change with Voltage			7 V ≤ V _{CC} ≤ 40 V, T _A = 25°C,		1		%
				$C_T = 0.01 \ \mu F, R_T = 12 \ k\Omega$				
Dead-	Input Bias Current			0 V ≤ V₁ ≤ 5.25 V		-2	-10	μΑ
Time	Maximum Duty Cycle (Each Output)			V _I = 0 V	45	49		%
Control Section	Input Threshold Voltage 1 Input Threshold Voltage 2		V _{TH1}	Output pulse 0% duty cycle		3	3.3	V
			V _{TH2}	Output pulse maximum duty cycle	0			V
Error	Input Offset Voltage		Vio	VOAMP = 2.5 V		2	10	mV
Amplifier	Input Offset Current		lio	VOAMP = 2.5 V		25	250	nA
Section	Input Bias Current			VOAMP = 2.5 V		0.2	1	μΑ
	Common Mode	Low	Vісм	7 V ≤ Vcc ≤ 40 V	-0.3			V
	Input Voltage	High			Vcc-2			
	Open Loop Voltage Amplification		Av	Voamp = 0.5 to 3.5 V, T _A = 25°C	60	80		dB
	Unity Gain Bandwidth			T _A = 25°C	500	830		kHz
	Common Mode Rejection Radio		CMR	Vcc = 40 V, T _A = 25°C	65	80		dB
	Output Sink Current			VOAMP = 0.7 V	0.3	0.7		mA
	Output Source Current			VOAMP = 3.5 V	-2	-10		mA
PWM Section	Input Threshold Voltage	(3-pin)		Output pulse 0% duty cycle, see Figure 1.		4	4.5	V
	Input Sink Current			V _(pin 3) = 0.7 V	0.3	0.7		mA
	input offic outfort		1	<u> </u>	1	1	1	1

Remark The TYP. values are values at $T_A = 25$ °C, except for the characteristics of temperature.

(2/2)

Block	Characteristi	cs	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Output Section	Collector Cut-off Curren	nt	ICER	Vce = 40 V, Vcc = 40 V, Common Emitter			100	μΑ
	Emitter Cut-off Current			Vcc = Vc = 40 V, Ve = 0 V, Emitter Follower			-100	μΑ
	Collector Saturation Voltage	Common Emitter	VCE(sat)	Ic = 200 mA, V _E = 0 V		0.95	1.3	V
		Emitter Follower	Vce(on)	I _E = -200 mA, V _C = 15 V		1.6	2.5	V
	Output Voltage Rise Time	Common Emitter	t _{r1}	$Vcc = 15 \text{ V}, \text{ R}_L = 150 \Omega,$ $I_C \cong 100 \text{ mA}, \text{ T}_A = 25^{\circ}\text{C},$		100	200	ns
	Output Voltage Fall Time		t _{f1}	see Figure 1.		70	200	ns
	Output Voltage Rise Time	Emitter Follower	tr2	$V_C = 15 \text{ V}, \text{ RL} = 150 \ \Omega,$ $I_E \cong 100 \text{ mA}, \text{ TA} = 25^{\circ}\text{C},$		100	200	ns
	Output Voltage Fall Time		tr2	see Figure 1.		70	200	ns
Total Device	Standby Current		Icc(s.B)	Vcc = 15 V, all other pins open.		8	12.5	mA
	Bias Current		Ісс(ві)	$V_{(pin 4)} = 2 V,$ see Figure 1 .		10		mA

Remark The TYP. values are values at $T_A = 25$ °C, except for the characteristics of temperature.

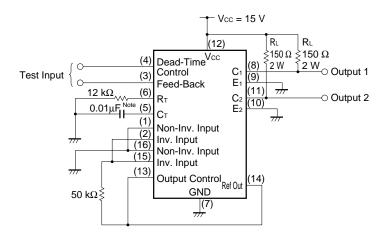
- **Notes 1.** The short circuit output current flows for no more than 1 second. Repeat operation is possible if the internal heat accumulation is not within a harmful range.
 - 2. Standard deviation is a measure of the statistical distribution about the mean as derived from the formula;

$$\sigma = \sqrt{\frac{\sum\limits_{\sum}^{N}(X_{n} - \overline{X})^{2}}{N - 1}}$$

Calculation expression of frequency fosc is as follows;

$$\label{eq:fosc} \text{fosc} \cong \frac{1}{-0.817 \; \text{RT} \cdot \text{CT} + 1.42 \cdot 10^{-6}} \; \; (\text{Hz}) \qquad \qquad [\text{RT}] = \Omega, \; [\text{CT}] = F$$

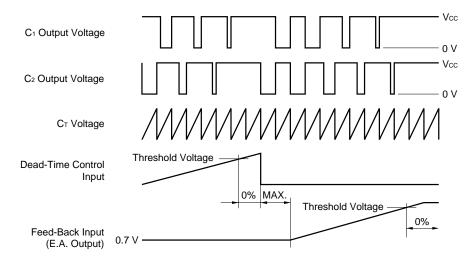
Figure1. Test Circuit



Note Recommend film capacitor.

★ Caution When the emitter follower is output, connect C₁ and C₂ to Vcc and E₁ and E₂ to GND via RL.

Figure 2. Voltage Waveform

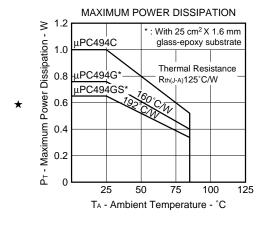


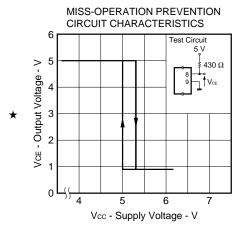
Connection of Output Control Pin (Pin No.13)

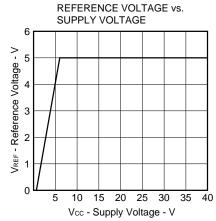
Output Control Input (Pin No.13)	Operation Mode
At Ref Out	Normal push-pull operation
Grounded	Single-ended or parallel output

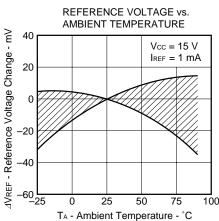
TYPICAL PERFORMANCE CHARACTERISTICS

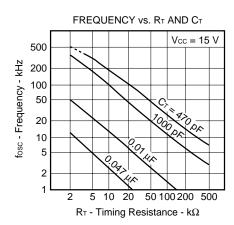
★ (Unless otherwise specified, T_A = 25°C, Vcc = 15 V, Reference)

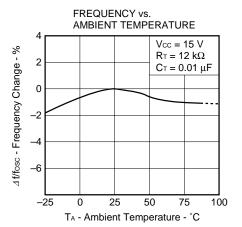


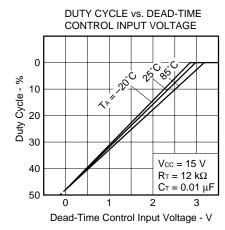


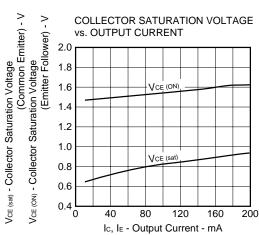


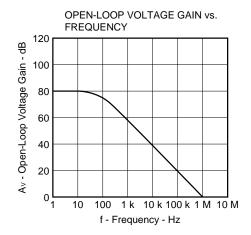


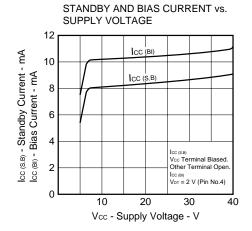




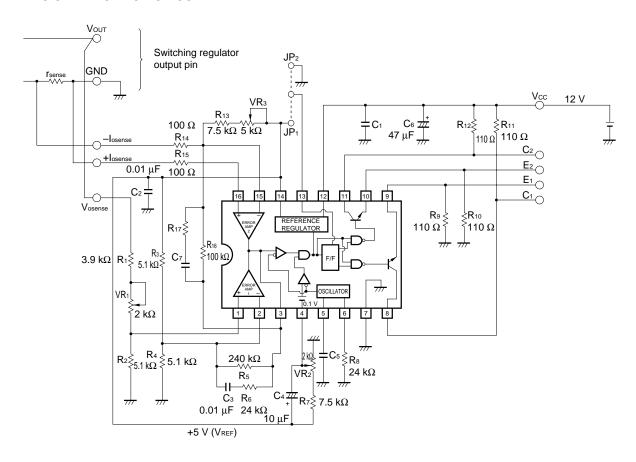








BASIC APPLICATION CIRCUIT

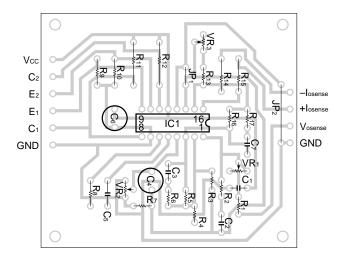


★ Remark fosc ≅ 40 kHz, C₅ = 1000 pF (Recommend film capacitor)

CONNECTION DIAGRAM

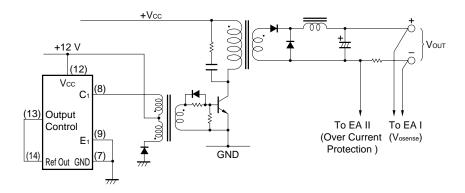
Operation Mode	Output Control Input (Pin No.13)	Output Mode	Output Voltage Waveform
Push-pull operation	At Ref-out (JP1 Wired)	Open collector (R ₉ , R ₁₀ 0Ω) Emitter follower (R ₁₁ , R ₁₂ 0Ω)	C ₁
Single-ended or parallel output	Grounded (JP2 Wired)	Open collector (R ₉ , R ₁₀ 0Ω) Emitter follower (R ₁₁ , R ₁₂ 0Ω)	C ₁ , C ₂

Printed Pattern (Example of μ PC494C) (Pattern side, Actual size)

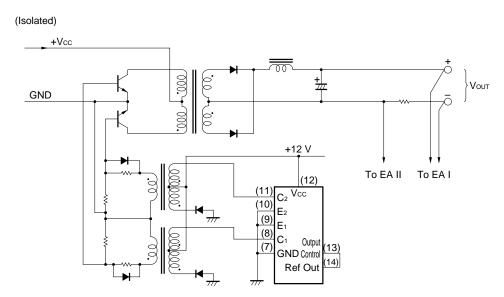


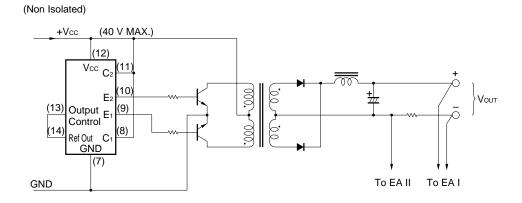
TYPICAL EXAMPLE OF APPLICATION CIRCUITS

1) Forward Type



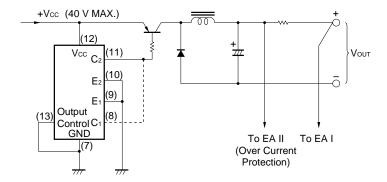
2) Push-pull Type





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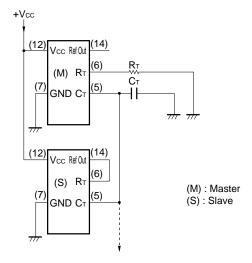
3) Step-down Chopper



Remark The dotted line indicates the connection in case of large current.

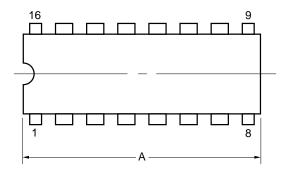
SYNCRONIZED OPERATION

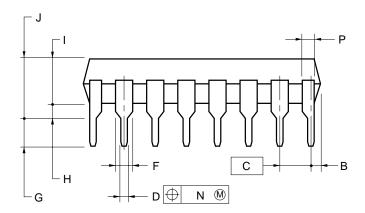
If synchronized operation is needed, muster-slave circuit can be used. This circuit is shown below. Initially, R_T terminal of slave IC is connected to pin 14(Ref Out) and internal oscillator is stopped.

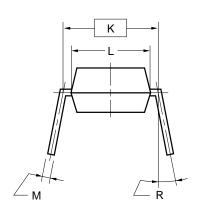


★ PACKAGE DRAWINGS (Unit:mm)

16-PIN PLASTIC DIP (7.62mm(300))







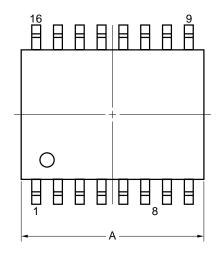
NOTES

- 1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
- 2. Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS
Α	20.32 MAX.
В	1.27 MAX.
С	2.54 (T.P.)
D	0.50±0.10
F	1.1 MIN.
G	3.5±0.3
Н	0.51 MIN.
ı	4.31 MAX.
J	5.08 MAX.
K	7.62 (T.P.)
L	6.5
М	$0.25^{+0.10}_{-0.05}$
N	0.25
Р	1.1 MIN.
R	0~15°
	D4CC 400 200D C

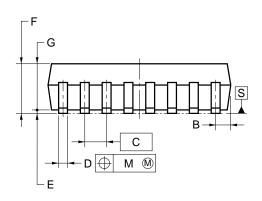
P16C-100-300B-2

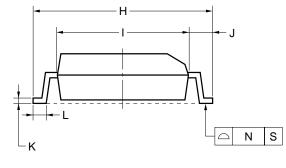
16-PIN PLASTIC SOP (9.53 mm (375))



detail of lead end







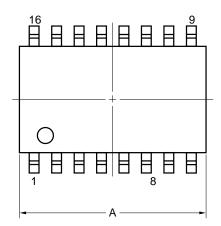
NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

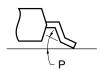
ITEM	MILLIMETERS
Α	10.0±0.2
В	0.78 MAX.
С	1.27 (T.P.)
D	$0.42^{+0.08}_{-0.07}$
E	0.125±0.075
F	2.77 MAX.
G	2.47±0.1
Н	10.3±0.3
I	7.2
J	1.6
K	$0.17^{+0.08}_{-0.07}$
L	0.8±0.2
М	0.12
N	0.15
Р	3°+7° -3°

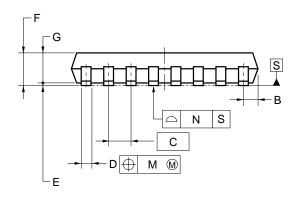
P16GM-50-375B-6

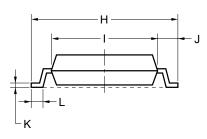
16-PIN PLASTIC SOP (7.62 mm (300))



detail of lead end







NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
Α	10.2±0.2
В	0.78 MAX.
С	1.27 (T.P.)
D	$0.42^{+0.08}_{-0.07}$
Е	0.1±0.1
F	1.65±0.15
G	1.55
Н	7.7±0.3
- 1	5.6±0.2
J	1.1±0.2
K	$0.22^{+0.08}_{-0.07}$
L	0.6±0.2
М	0.12
N	0.10
Р	3°+7°

P16GM-50-300B-6

★ RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (C10535E).

Type of Through-hole Device

 μ PC494C: 16-pin plastic DIP (7.62 mm (300))

Process	Conditions
Wave Soldering	Solder temperature: 260°C or below,
(only to leads)	Flow time: 10 seconds or less.
Partial Heating Method	Pin temperature: 300°C or below,
	Heat time: 3 seconds or less (per each lead).

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

Type of Surface Mount Device

 μ PC494G: 16-pin plastic SOP (9.53 mm (375)) μ PC494GS: 16-pin plastic SOP (7.62 mm (300))

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 230°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 1 time.	IR30-00-1
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 1 time.	VP15-00-1
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	-

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

NEC μ PC494

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NEC μ PC494

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