TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC7240AP,TC74HC7240AF,TC74HC7244AP,TC74HC7244AF

Octal Bus Buffer (with schmitt trigger inputs)

TC74HC7240AP/AF Inverted, 3-State Outputs TC74HC7244AP/AF Non-Inverted, 3-State

Outputs

The TC74HC7240A/7244A are high speed CMOS OCTAL BUS BUFFERs with silicon gate C^2MOS technology.

They achieve the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

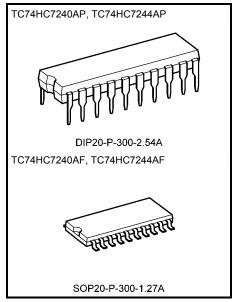
The TC74HC7240A/7244A have same pin configuration and function as the TC74HC240A/244A. And they have a hystereis characterictics with each input, so TC74HC7240A/7244A can be used as a line receiver, etc.

They have two active low output enables.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

Features

- High speed: $t_{pd} = 15 \text{ ns (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $ICC = 4 \mu A$ (max) at Ta = 25°C
- High noise immunity: $V_H = 1.1 \text{ V (typ.)}$ at $V_{CC} = 5 \text{ V}$
- Output drive capability: 15 LSTTL loads
- Symmetrical output impedance: |IOH| = IOL = 6 mA (min)
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: VCC (opr) = 2 to 6 V
- Pin and function compatible with 74LS240/244

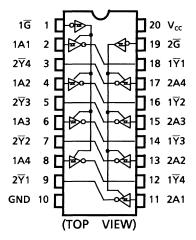


Weight

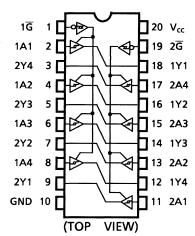
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.)

Pin Assignment

TC74HC7240A



TC74HC7244A

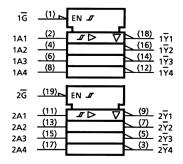


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IEC Logic Symbol

TC74HC7240A



TC74HC7244A

1G	(1)	EN 🗷		
1A1 1A2 1A3 1A4	(2) (4) (6) (8)	I ▷	₹	(18) (16) (14) (12) (12) (12)
2G	(19)	EN 🌃		
2A1 2A2 2A3 2A4	(11) (13) (15) (17)	# ▷	▼	(9) 2Y1 (7) 2Y2 (5) 2Y3 (3) 2Y4

Truth Table

Inp	uts	Outputs			
G	An	Yn	\overline{Y}_n		
L	L	L	Н		
L	Н	Н	L		
Н	Х	Z	Z		

 Δ : For TC74HC7240A only

X: Don't care

Z: High impedance

Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	–0.5 to 7	V
DC input voltage	VIN	-0.5 to V _{CC} + 0.5	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	lıK	±20	mA
Output diode current	lok	±20	mA
DC output current	lout	±35	mA
DC V _{CC} /ground current	Icc	±75	mA
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW
Storage temperature	T _{stg}	−65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.



Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2 to 6	V
Input voltage	V _{IN}	0 to V _{CC}	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
	.,			V _{CC} (V)	Min	Тур.	Max	Min	Max	
				2.0	1.0	1.25	1.5	1.0	1.5	
Positive threshold voltage	VP		_	4.5	2.3	2.7	3.15	2.3	3.15	V
				6.0	3.0	3.5	4.2	3.0	4.2	
				2.0	0.3	0.65	0.9	0.3	0.9	
Negative threshold voltage	V _N		_	4.5	1.13	1.6	2.0	1.13	2.0	V
				6.0	1.5	2.3	2.6	1.5	2.6	
				2.0	0.3	0.6	1.0	0.3	1.0	
Hysteresis voltage	V _H	_		4.5	0.6	1.1	1.4	0.6	1.4	V
				6.0	0.8	1.2	1.7	0.8	1.7	
	Voн	V _{IN} = V _{IH} or V _{IL}		2.0	1.9	2.0	_	1.9	_	
			$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	_	4.4	_	
High-level output voltage				6.0	5.9	6.0	_	5.9	_	V
			$I_{OH} = -6 \text{ mA}$	4.5	4.18	4.31	_	4.13	_	
			$I_{OH} = -7.8 \text{ mA}$	6.0	5.68	5.80	—	5.63	_	
	VOL VIN = VIH or V	V _{IN} = V _{IH} or V _{IL}		2.0	_	0.0	0.1		0.1	
			$I_{OL}=20~\mu A$	4.5	_	0.0	0.1	_	0.1	
Low-level output voltage				6.0	_	0.0	0.1	_	0.1	V
			$I_{OL} = 6 \text{ mA}$	4.5	_	0.17	0.26	_	0.33	
			$I_{OL} = 7.8 \text{ mA}$	6.0	_	0.18	0.26		0.33	
3-state output off-state current	I _{OZ}	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $V_{OUT} = V_{CC} \text{ or GND}$		6.0	_	_	±0.5		±5.0	μА
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0			±0.1		±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or	V _{IN} = V _{CC} or GND		_	_	4.0	_	40.0	μА



AC Characteristics (input: $t_r = t_f = 6$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit	
Characteriotics	Cymbol		CL (pF)	V _{CC} (V)	Min	Тур.	Max	Min	Max	O I II C
	t			2.0	_	25	60	_	75	
Output transition time	t _{TLH} t _{THL}	_	50	4.5	_	7	12		15	ns
	ЧHL			6.0		6	10	_	13	
				2.0	_	50	125		155	
			50	4.5	_	15	25		31	
Propagation delay	t_{pLH}	_		6.0	_	13	21	_	26	ns
time	t_{pHL}			2.0	_	67	165		205	110
			150	4.5	_	20	33	_	41	
				6.0	_	17	28	_	35	
	^t pZL ^t pZH	$R_L = 1 \text{ k}\Omega$	50	2.0	_	68	150	_	190	
				4.5	_	21	30	_	38	
Output enable time				6.0	_	16	26	_	32	ns
Cutput chable time			150	2.0	_	84	165	_	230	
				4.5	_	26	37	_	46	
				6.0	_	20	31	_	39	
	t_pLZ			2.0	_	48	150	_	190	
Output disable time		$R_L = 1 \text{ k}\Omega$	50	4.5	_	21	30	_	38	ns
	t _{pHZ}			6.0	_	19	26	_	32	
Input capacitance	C _{IN}	_	-			5	10		10	pF
Output capacitance	C _{OUT}	_	-			10	_		_	pF
Power dissipation	C _{PD}	TC74HC7240A			-	33	_	_	_	pF
capacitance	(Note)	TC74HC7244A				34	_	_	_	þΓ

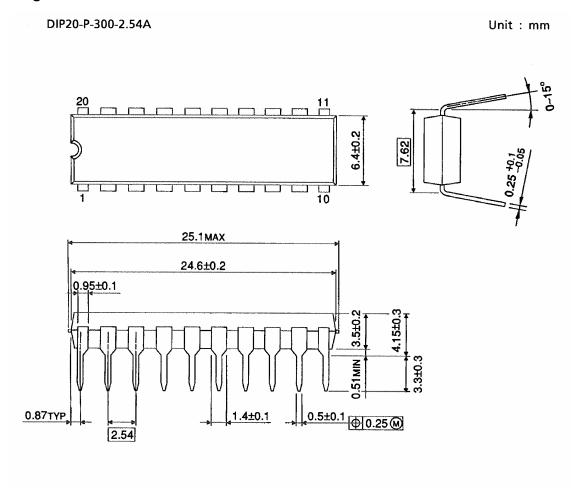
Note: CPD is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

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Average operating current can be obtained by the equation:

$$I_{CC}$$
 (opr) = $C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8$ (per bit)

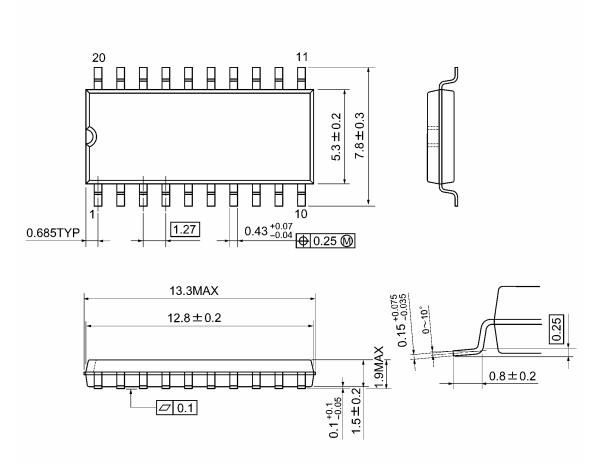
Package Dimensions



Weight: 1.30 g (typ.)

Package Dimensions

SOP20-P-300-1.27A Unit: mm



Weight: 0.22 g (typ.)

RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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