

# High Efficiency, Low Quiescent, Triple output DC-DC Converter

## General Descriptions

The RT9267 is a triple output DC-DC converter IC including two adaptive PWM controllers, one low dropout regulator, and one voltage detector. RT9267 is capable of delivering hundreds of mA output current and consumes only 40µA quiescent current for the whole IC.

The adaptive PWM controllers are configured for boost applications with built-in 2A NMOS for each channel. The proprietary adaptive PWM loop provides PWM operation for heavier output loading conditions and PFM for lighter loading, with seamless auto-transition. The 500kHz switching rate reduces the size of external passive components.

The low dropout linear regulator (LDO) is designed with a built-in 300mA PMOS, providing 1mV/mA low dropout voltage with respect to variant output current values.

The voltage detector provides the supervisory function with open collector pull-low output. All the 4 function units are adjustable, and each can be set with two resistors in divider connection. The reference voltage for each channel is set to 1.25V for the two adaptive PWM controllers, and 0.86V for the LDO and the voltage detector.

The RT9267 is in TSSOP 16 pin package which fits space-limited hand held devices well.

## Ordering Information

RT9267□ □

- Package type  
C : TSSOP-16
- Operating temperature range  
C: Commercial standard

## Features

- Two Channels of Adaptive PWM Controllers
- One Channel of Low Dropout Regulator
- One Voltage Detector
- Low Start-Up Voltage 1.0V
- Low Quiescent Current 40µA for the Whole IC
- High Switching Rate 550KHz
- Built-In 2A Switching NMOS
- Two Separated Chip Enable Control Pins
- 2% Accuracy for All Channels
- Minimized External Components
- Small TSSOP-16 Package

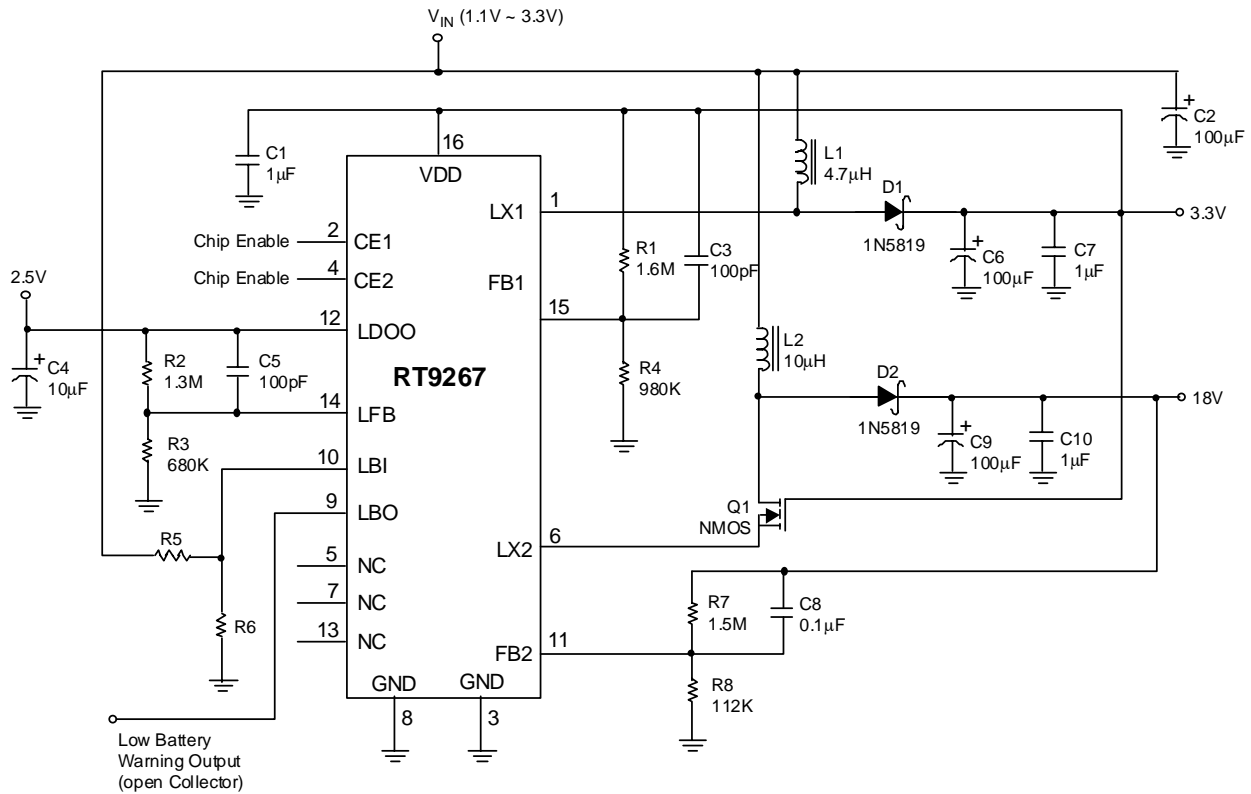
## Applications

- PDA
- Portable Instrument
- Wireless Equipment
- DSC
- LCD Back Bias Circuit
- RF-Tags

## Pin Configurations

Part Number	Pin Configurations
RT9267CC (Plastic TSSOP-16)	<p style="text-align: center;">TOP VIEW</p> <p style="text-align: center;">                     LX1 [1]                      16] VDD                      CE1 [2]                      15] FB1                      GND [3]                      14] LFB                      CE2 [4]                      13] NC                      NC [5]                        12] LDO                      LX2 [6]                      11] FB2                      NC [7]                        10] LBI                      GND [8]                      9] LBO                 </p>

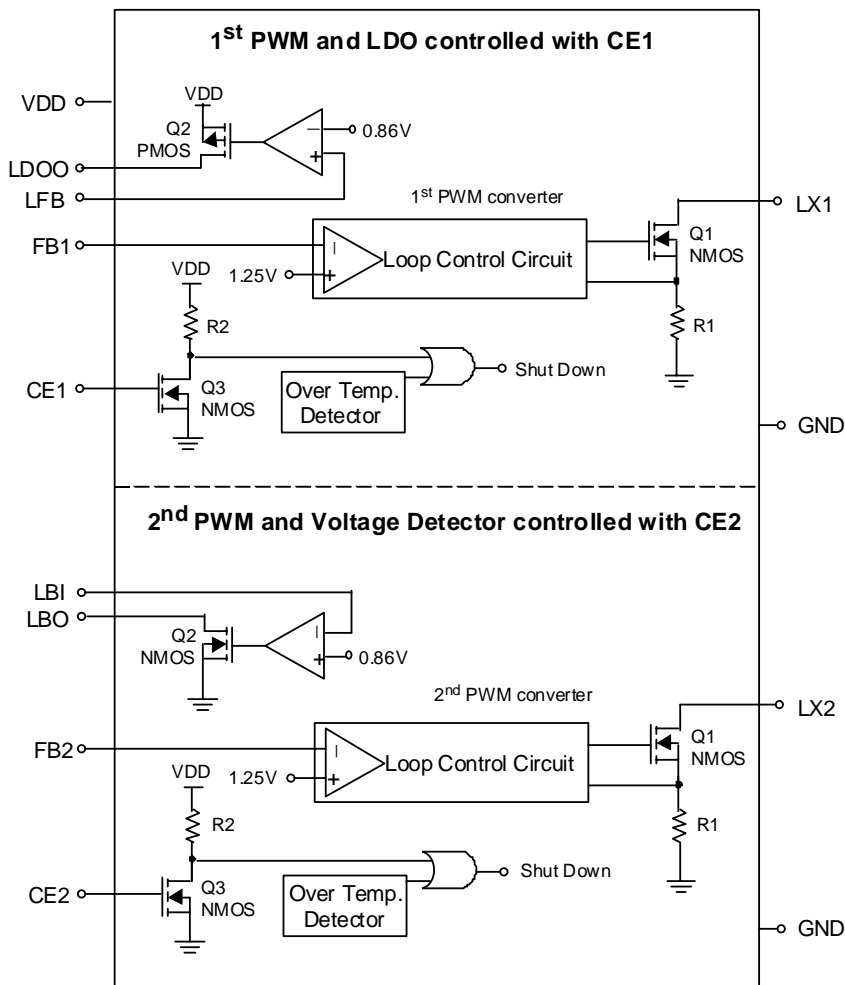
Typical Application Circuit



Pin Description

Pin No.	Pin Name	Pin Function
1	LX1	The switching pin for 1 <sup>st</sup> PWM converter
2	CE1	Enable pin for 1 <sup>st</sup> PWM converter and LDO. High = enable, Low = shutdown
3	GND	Ground (Both Pin3 and Pin8 should be connected)
4	CE2	Enable pin for 2 <sup>nd</sup> PWM converter and voltage detector. High = enable, Low = shutdown
5	NC	Not connected
6	LX2	The switching pin for 2 <sup>nd</sup> PWM converter
7	NC	Not connected
8	GND	Ground (Both Pin3 and Pin8 should be connected)
9	LBO	NMOS open drain output of the voltage detector. This pin will internally pulled low when the voltage at LBI pin below 0.86V.
10	LBI	Input pin for voltage detector. The trip point is 0.86V.
11	FB2	Feedback input pin for 2 <sup>nd</sup> PWM converter. Internal V <sub>REF</sub> for the error amplifier is 1.25V.
12	LDOO	Voltage output pin for the LDO.
13	NC	Not connected
14	LFB	Feedback pin for the LDO. Internal V <sub>REF</sub> for the error amplifier is 0.86V
15	FB1	Feedback input pin for 1 <sup>st</sup> PWM converter. Internal V <sub>REF</sub> for the error amplifier is 1.25V
16	VDD	Input positive power pin of RT9267, and also the voltage input pin for LDO.

**Function Block Diagram**



**Absolute Maximum Ratings**

• Supply Voltage	-----	-0.3V to 7V
• LX Pin Switch Voltage	-----	-0.3V to (VDD + 0.8V)
• LDO Output Voltage	-----	-0.3V to (VDD + 0.3V)
• Other I/O Pin Voltages	-----	-0.3V to (VDD + 0.3V)
• LX Pin Switch Current	-----	2.5A
• EXT Pin Driver Current	-----	30mA
• LBO Current	-----	30mA
• Power Dissipation, P <sub>D</sub> @ T <sub>A</sub> = 25°C		
TSSOP-16	-----	600mW
• Operating Junction Temperature	-----	150°C
• Storage Temperature Range	-----	-65°C ~ +150°C

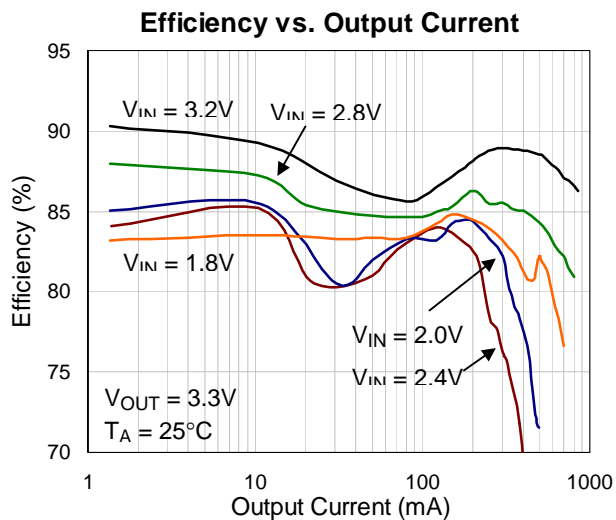
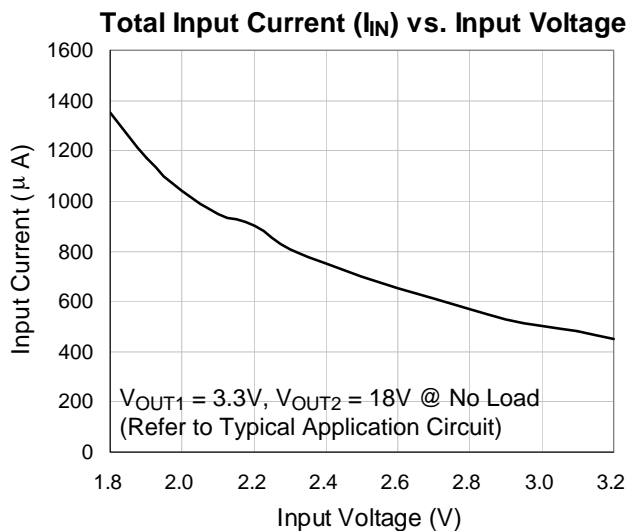
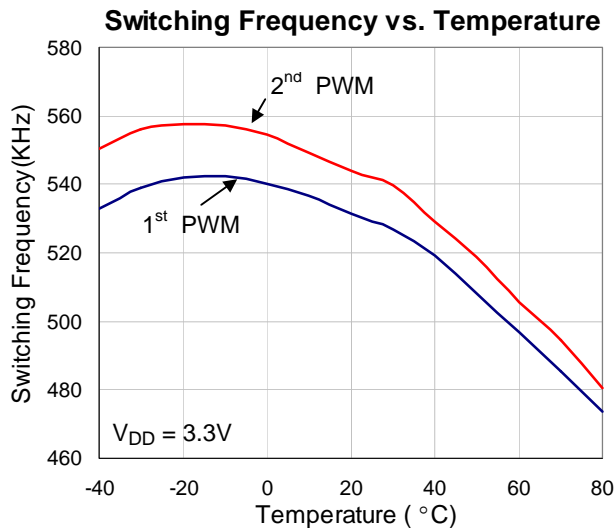
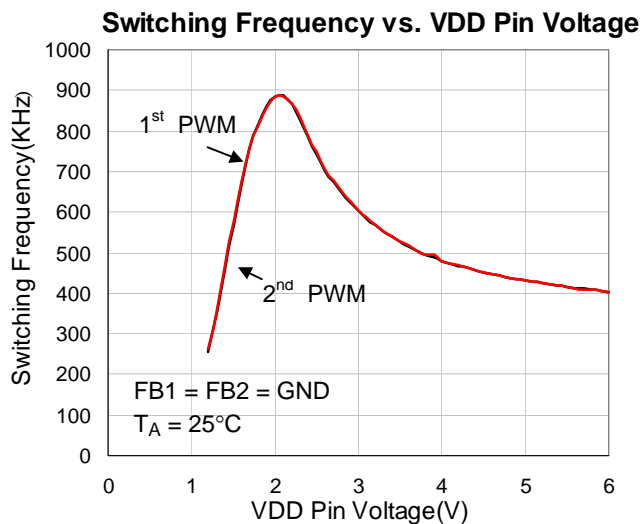
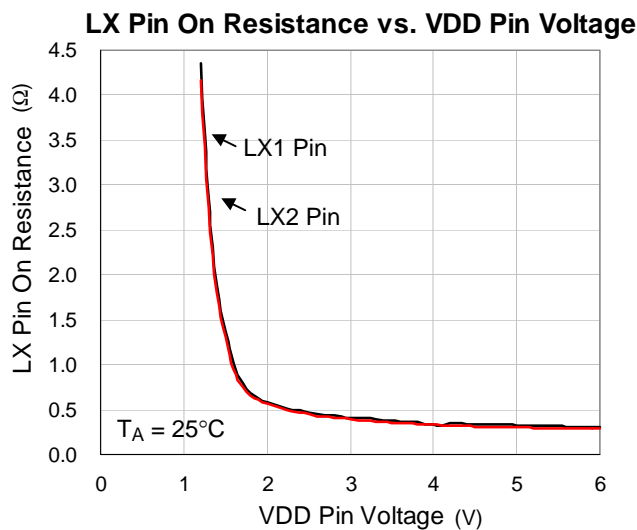
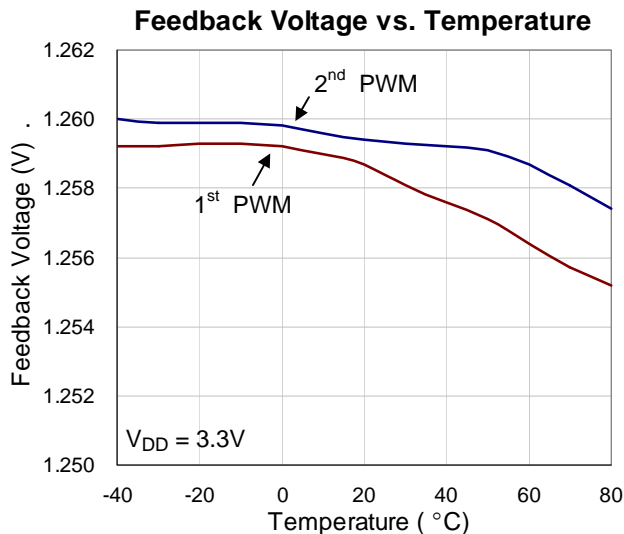
## Electrical Characteristics

( $V_{IN} = 5V$ ,  $C_{IN} = C_{OUT} = 1\mu F$ ,  $T_A = 25^\circ C$ , unless otherwise specified)

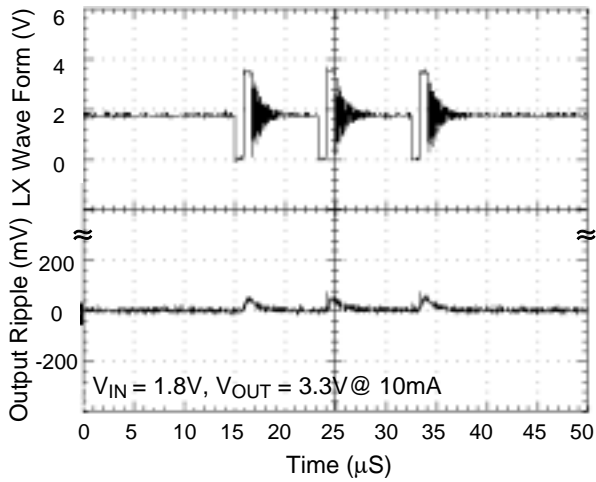
Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Section for Adaptive PWM Converters</b>						
Start-Up Voltage	$V_{ST}$	$I_L = 1mA$	--	0.98	1.05	V
Operating VDD Range	$V_{DD}$	Start-up to $I_{DD} > 250\mu A$	0.8	--	6.5 <sup>*</sup>	V
Feedback Reference Voltage	$V_{REF}$	Close Loop, $V_{DD} = 3.3V$	1.225	1.25	1.275	V
Switching Rate	$F_S$	$V_{DD} = 3.3V$	--	550	--	kHz
Maximum Duty	$D_{MAX}$	$V_{DD} = 3.3V$	--	92	--	%
LX ON Resistance		$V_{DD} = 3.3V$	--	0.25	--	$\Omega$
Current Limit Setting	$I_{LIMIT}$	$V_{DD} = 3.3V$	--	2	--	A
No Load Current ( $V_{IN}$ )	$I_{NO\ LOAD}$	$V_{IN} = 1.5V$ , $V_{OUT} = 3.3V$	--	47	--	$\mu A$
Switch-off Current (VDD)	$I_{SWITCH\ OFF}$	$V_{IN} = 6V$	--	17	--	$\mu A$
Line Regulation	$\Delta V_{LINE}$	$V_{IN} = 1.5 \sim 2.5V$ , $I_L = 1mA$	--	10	--	mV/V
Load Regulation	$\Delta V_{LOAD}$	$V_{IN} = 2.5V$ , $I_L = 1 \sim 100mA$	--	0.25	--	mV/mA
<b>Section for LDO</b>						
LDO PMOS ON Resistance		$V_{DD} = 3.3V$	--	1	1.5	$\Omega$
LDO Drop Out Voltage	$V_{DROP}$	$V_{DD} = 3.3V$ , $I_L = 100mA$	--	100	--	mV
Feedback Reference Voltage for LDO	$V_{REF}$	Close Loop, $V_{DD} = 3.3V$	0.843	0.86	0.877	V
<b>Section for Voltage Detector</b>						
LBO ON Resistance		$V_{DD} = 3.3V$	--	40	--	$\Omega$
LBI Pin Trip Point		$V_{DD} = 3.3V$	0.843	0.86	0.877	V
<b>Section for Whole Chip Property</b>						
Operating VDD Range	$V_{DD}$	Start-up to $I_{DD1} > 250\mu A$	0.8	--	6	V
Shutdown Current ( $V_{IN}$ )	$I_{OFF}$	$CE1 = CE2 = 0$ , $V_{IN} = 4.5V$	--	0.1	1	$\mu A$
CE Pin Trip Level		$V_{DD} = 3.3V$	0.4	1.0	1.4	V
Temperature Stability for FB, LFB, LBI	$T_S$	Guaranteed by Design	--	40	--	ppm/ $^\circ C$
Thermal Shutdown	$T_{SD}$		--	165	--	$^\circ C$

\* Note: The CE pin shall be tied to VDD pin and inhibit to act the ON/OFF state whenever the VDD pin voltage may reach to 5.5V or above, in case that VDD pin is conducted from VIN.

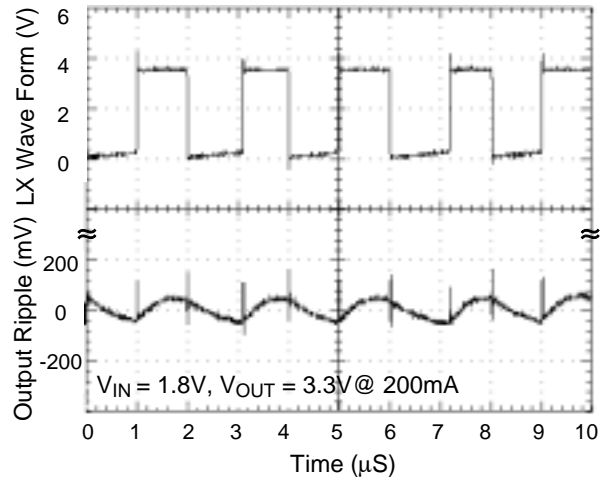
**Typical Operating Characteristics**



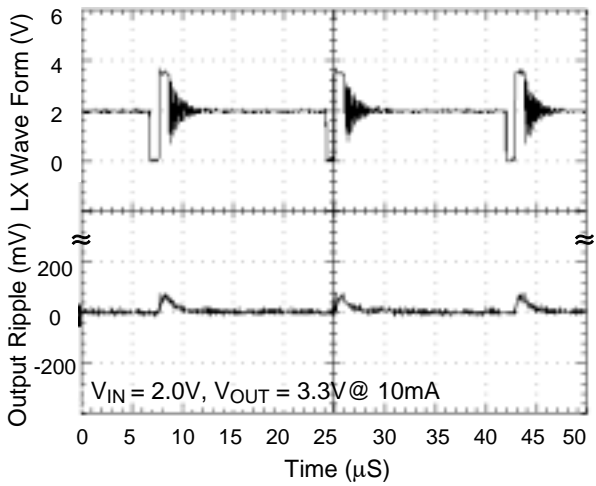
LX Pin Wave Form & Output Ripple



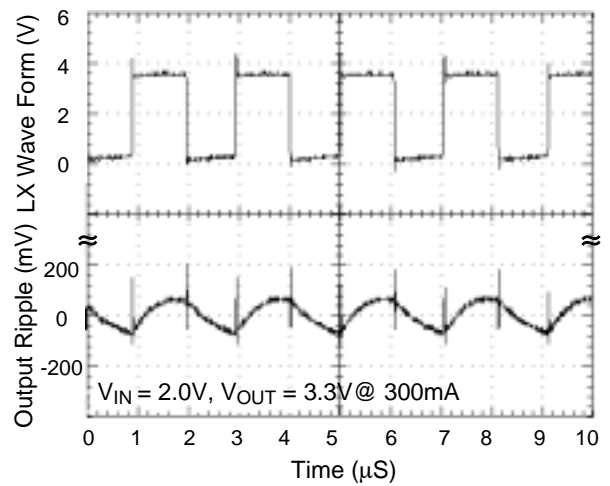
LX Pin Wave Form & Output Ripple



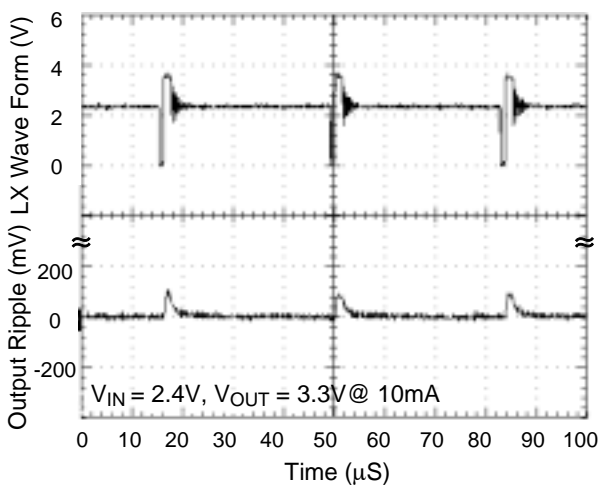
LX Pin Wave Form & Output Ripple



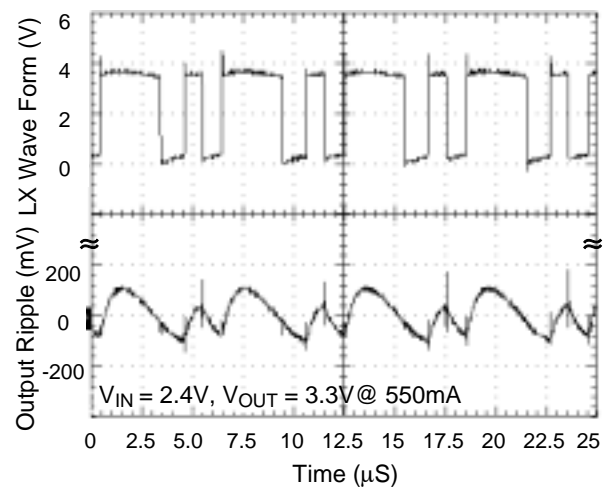
LX Pin Wave Form & Output Ripple



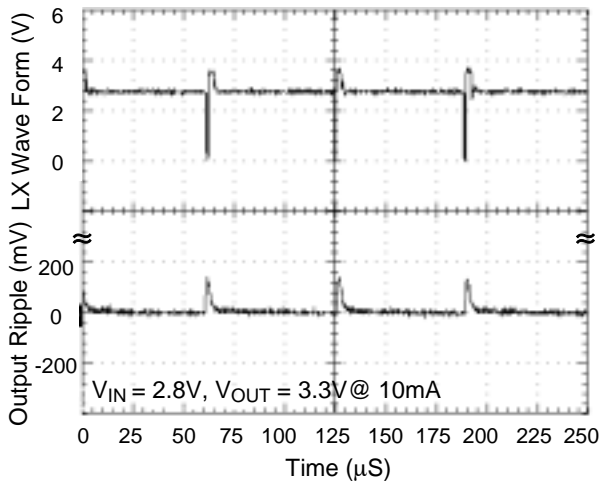
LX Pin Wave Form & Output Ripple



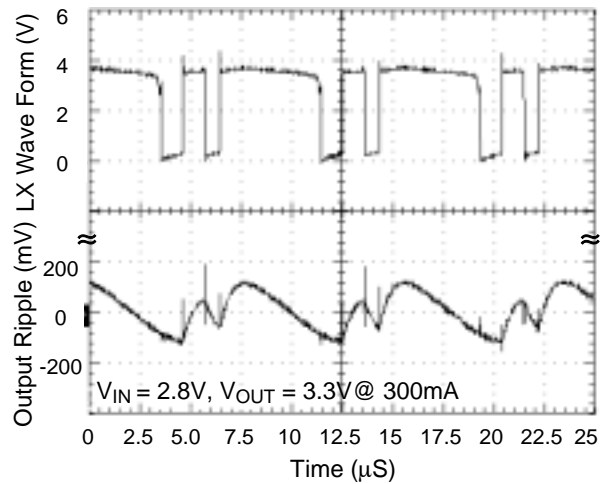
LX Pin Wave Form & Output Ripple



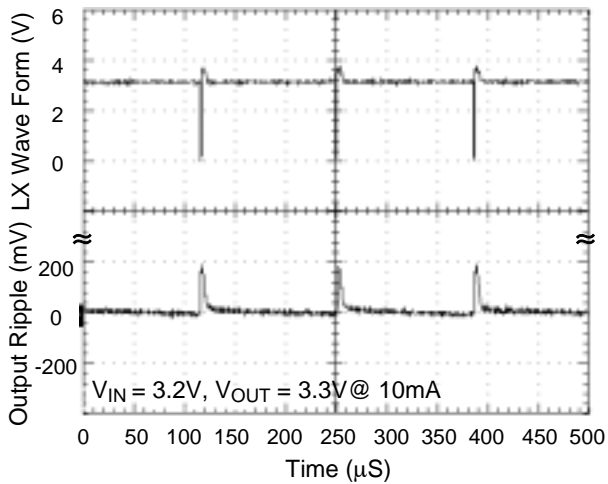
**LX Pin Wave Form & Output Ripple**



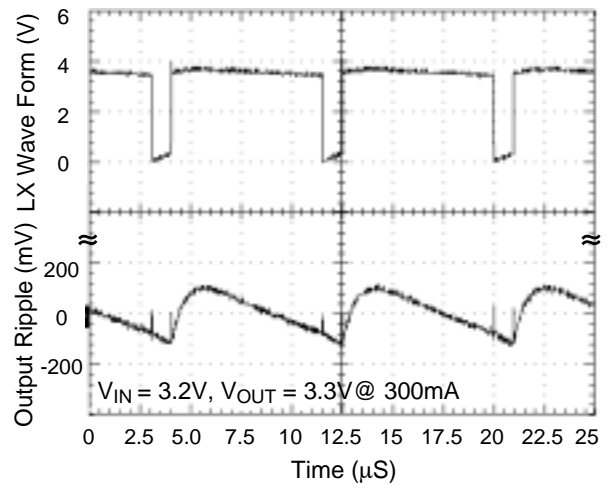
**LX Pin Wave Form & Output Ripple**



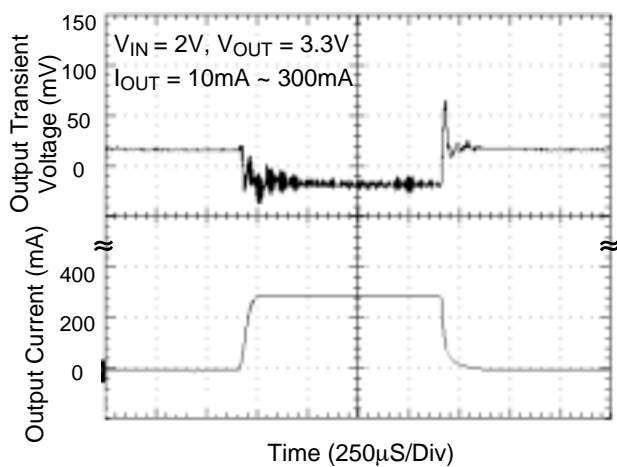
**LX Pin Wave Form & Output Ripple**



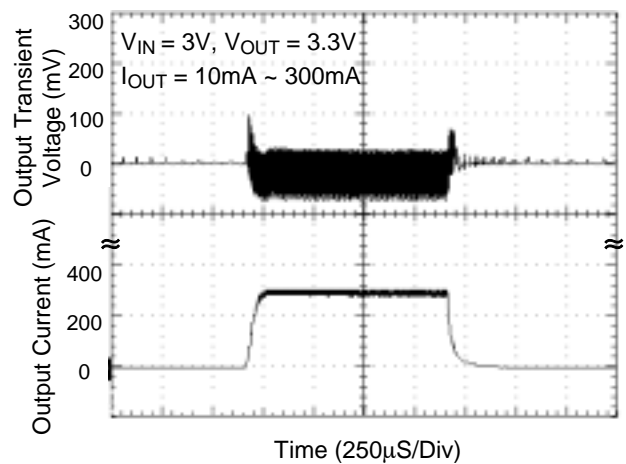
**LX Pin Wave Form & Output Ripple**



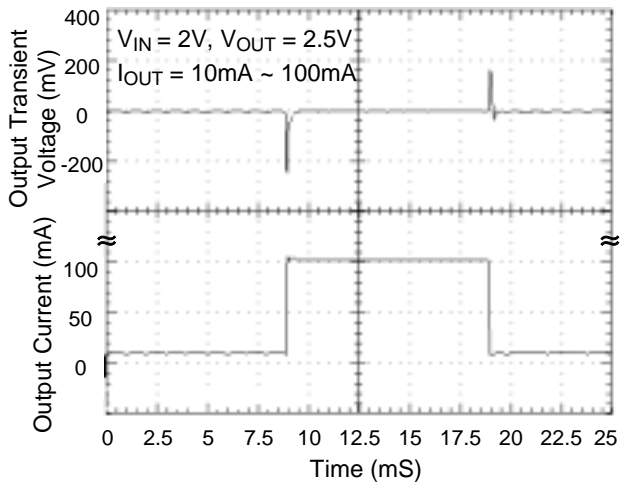
**Transient Response**



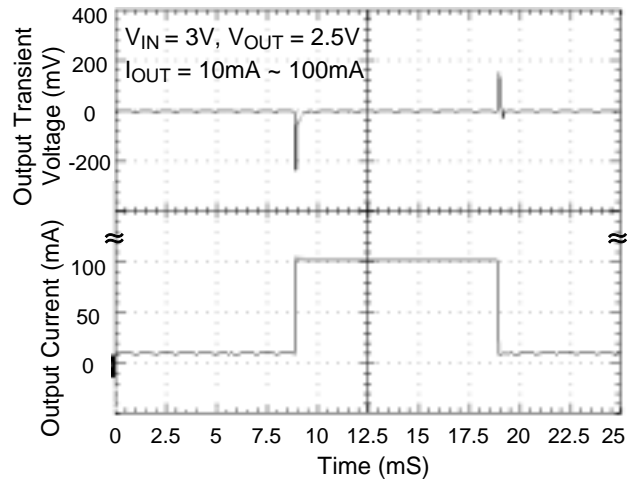
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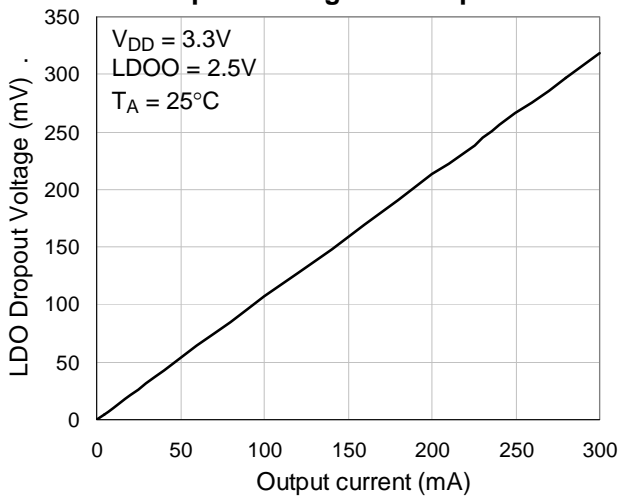
LDO Transient Response



LDO Transient Response

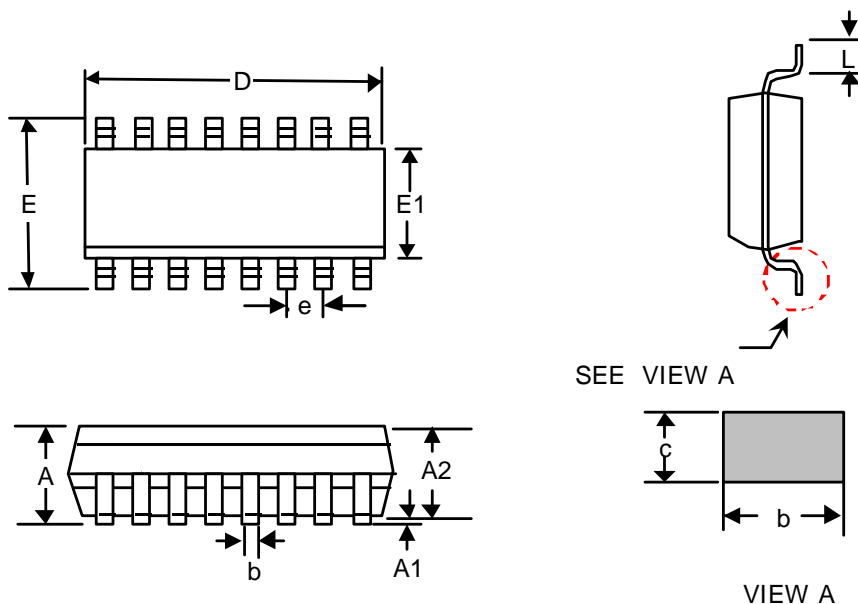


LDO Dropout Voltage vs. Output Current





**Package Information**



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	--	1.20	--	0.047
A1	0.05	0.15	0.002	0.006
A2	0.80	1.05	0.031	0.041
b	0.19	0.30	0.007	0.012
c	0.09	0.20	0.003	0.008
D	4.90	5.10	0.193	0.201
e	0.65		0.026	
E	6.20	6.60	0.244	0.260
E1	4.30	4.50	0.169	0.177
L	0.45	0.75	0.018	0.030

**16-Lead TSSOP Plastic Package**

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