

54F/74F86 2-Input Exclusive-OR Gate

General Description

This device contains four independent gates, each of which performs the logic exclusive-OR function.

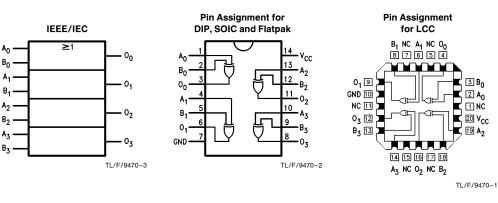
Military	Package Number	Package Description
	N14A	14-Lead (0.300" Wide) Molded Dual-in-Line
54F86DM (Note 2)	J14A	14-Lead Ceramic Dual-in-Line
	M14A	14-Lead (0.150" Wide) Molded Small Outline, JEDEC
	M14D	14-Lead (0.300" Wide) Molded Small Outline, EIAJ
54F86FM (Note 2)	W14B	14-Lead Cerpack
54F86LM (Note 2)	E20A	20-Lead Ceramic Leadless Chip Carrier, Type C
	54F86DM (Note 2) 54F86FM (Note 2)	Military Number 54F86DM (Note 2) J14A 54F86DM (Note 2) M14A M14A M14A 54F86FM (Note 2) W14B

Note 1: Devices also available in 13" reel. Use suffix = SCX and SJX.

Note 2: Military grade device with environmental and burn-in processing. Use suffix = DMQB, FMQB and LMQB.

Logic Symbol

Connection Diagrams



Unit Loading/Fan Out

		54F/74F			
Pin Names	Description	U.L. HIGH/LOW	Input I _{IH} /I _{IL} Output I _{OH} /I _{OL}		
A _n , B _n O _n	Inputs Outputs	1.0/1.0 50/33.3	20 μA/−0.6 mA −1 mA/20 mA		

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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Storage Temperature	-65°C to +150°C
Ambient Temperature under Bias	-55°C to +125°C
Junction Temperature under Bias	-55°C to +175°C
Plastic	-55°C to +150°C
V _{CC} Pin Potential to	
Ground Pin	-0.5V to $+7.0V$
Input Voltage (Note 2)	-0.5V to $+7.0V$
Input Current (Note 2)	-30 mA to $+5.0$ mA
Voltage Applied to Output	
in HIGH State (with $V_{CC} = 0V$)	
Standard Output	-0.5V to V _{CC}
TRI-STATE [®] Output	-0.5V to $+5.5V$
Current Applied to Output	
in LOW/State (Max)	twice the rated los (mA)

Recommended Operating Conditions

Free Air Ambient Temperature

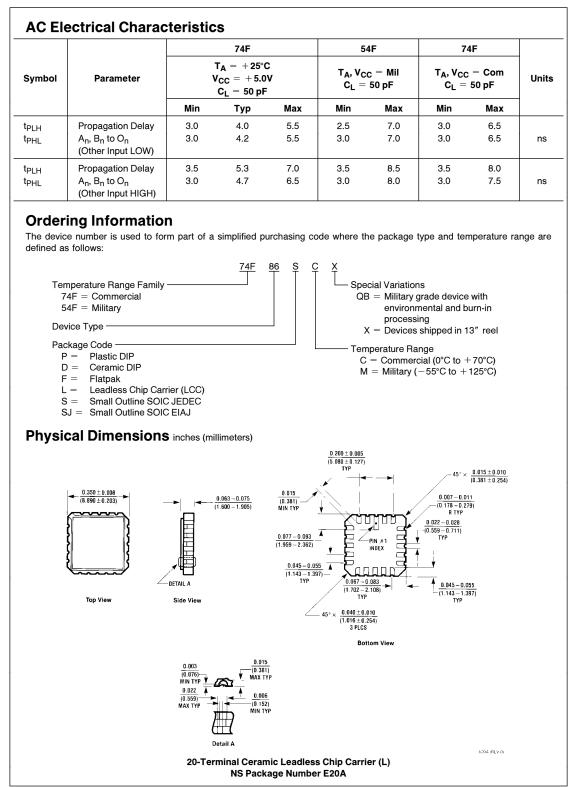
Military	-55°C to +125°C
Commercial	0°C to +70°C
Supply Voltage	
Military	+4.5V to +5.5V
Commercial	+4.5V to +5.5V

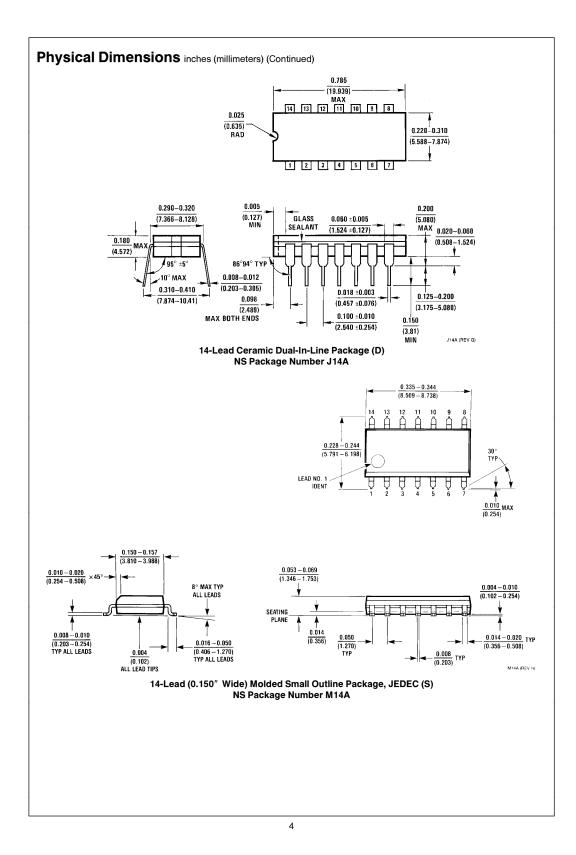
in LOW State (Max) twice the rated I_{OL} (mA) Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

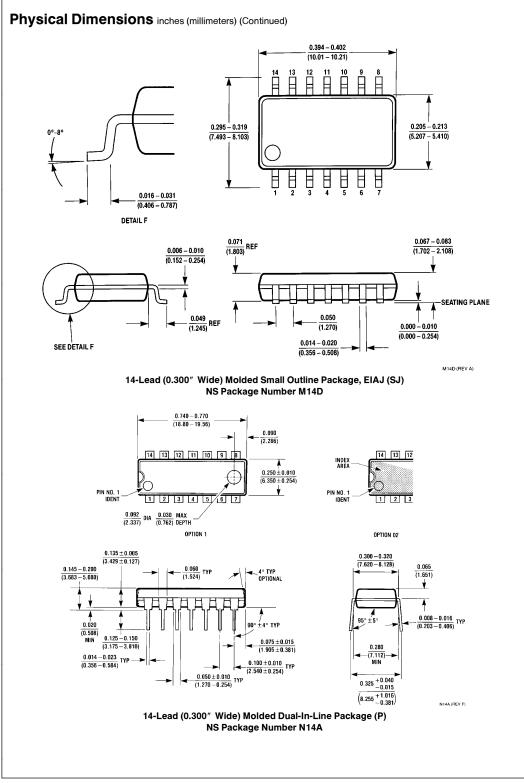
Note 2: Either voltage limit or current limit is sufficient to protect inputs.

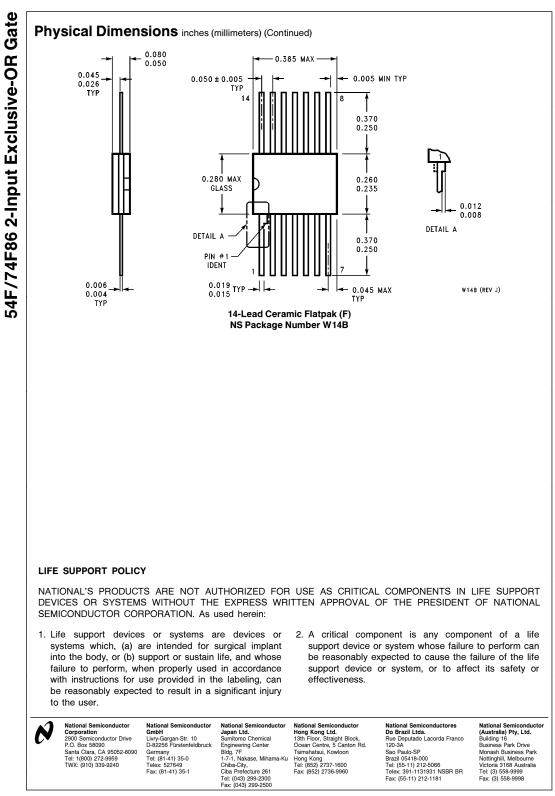
DC Electrical Characteristics

Symbol	Parameter		54F/74F			Units	v _{cc}	Conditions
			Min	Тур	Max	Units	VCC	Conditions
VIH	Input HIGH Voltage	2.0			V		Recognized as a HIGH Sign	
V _{IL}	Input LOW Voltage			0.8	V		Recognized as a LOW Signa	
V _{CD}	Input Clamp Diode Voltage				-1.2	V	Min	$I_{IN} = -18 \text{ mA}$
V _{OH}	Output HIGH Voltage	54F 10% V _{CC} 74F 10% V _{CC} 74F 5% V _{CC}	2.5 2.5 2.7			V	Min	$I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$ $I_{OH} = -1 \text{ mA}$
V _{OL}	Output LOW Voltage	54F 10% V _{CC} 74F 10% V _{CC}			0.5 0.5	V	Min	$I_{OL} = 20 \text{ mA}$ $I_{OL} = 20 \text{ mA}$
IIH	Input HIGH Current	54F 74F			20.0 5.0	μΑ	Max	$V_{IN} = 2.7V$
I _{BVI}	Input HIGH Current Breakdown Test	54F 74F			100 7.0	μΑ	Max	$V_{IN} = 7.0V$
ICEX	Output HIGH Leakage Current	54F 74F			250 50	μΑ	Max	$V_{OUT} = V_{CC}$
V _{ID}	Input Leakage Test	74F	4.75			V	0.0	$I_{ID} = 1.9 \mu A$ All other pins grounded
I _{OD}	Output Leakage Circuit Current	74F			3.75	μΑ	0.0	V _{IOD} = 150 mV All other pins grounded
IIL	Input LOW Current				-0.6	mA	Max	$V_{IN} = 0.5V$
l _{OS}	Output Short-Circuit C	Current	-60		-150	mA	Max	$V_{OUT} = 0V$
Іссн	Power Supply Current	t		12	18	mA	Max	V _O = HIGH
ICCL	Power Supply Current	t		18	28	mA	Max	$V_{O} = LOW$









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