

SANYO Semiconductors DATA SHEET

$LA73051 - \frac{Monolithic \ Linear \ IC}{3ch \ 75\Omega} \ Video \ Driver$

Overview

This LA73051 is a 3ch 75 Ω Video Driver IC. The LA73051 is ideal for use the video output driver such as VCR and DVD-player equipment.

Functions

• 6dB AMP+driver (3ch)

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		±7, +14	V
Allowable power dissipation	Pd max	Ta ≤ 80°C *	440	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +150	°C

*: Mounted on a board : 114.3×76.1×1.6mm³ glass epoxy board.

Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommending operation voltage	VCC		±5	V
			+9	
Operating voltage range	V _{CC} op		±4.0 to ±5.5	V
			+8 to +10	

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Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = \pm 5V$

Descentes	Question	Conditions	Ratings			11-2
Parameter	Symbol	Conditions	min	typ	max	Unit
Current dissipation	ICC1	No signal	28.9	34.0	39.1	mA
Voltage gain	VG	V _{IN} = 1Vp-p, f = 4.43MHz	5.7	6.2	6.7	dB
Frequency characteristics 1	VF1	V _{IN} = 1Vp-p, f = 100k/5MHz	-1.0	0	1.0	dB
Frequency characteristics 2	VF2	V _{IN} = 1Vp-p, f = 100k/27MHz		-25	-20	dB
Group delay	GD	f = 100k/4.43MHz		±10	±15	ns
Maximum output level	V _O max	f = 1kHz, THD = 1%	3.0	4.0		Vp-р
Control voltage H level	V _{cnt} H	Pin 7 input voltage	2.5		VCC	V
Control voltage L level	V _{cnt} L	Pin 7 input voltage	0		1.0	V

Design guarantee items

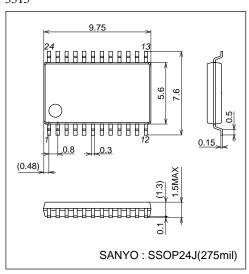
Descaration	Question	O sector a	Ratings			11-26
Parameter	Symbol Conditions	min	typ	max	Unit	
Video S/N	VG _{1V}			-75	-70	dB
Differential Gain	DG	V _{IN} = 1Vp-p, RAMP signal			1.0	%
Differential Phase	DP	V _{IN} = 1Vp-p, RAMP signal			1.0	deg.
Mute attenuation	VMUTEV	V _{IN} = 1Vp-p, f = 4.43MHz		-60	-55	dB
Cross-talk between	VCTKV	V _{IN} = 1Vp-p, f = 4.43MHz		-60	-55	dB
channel						

Truth Table

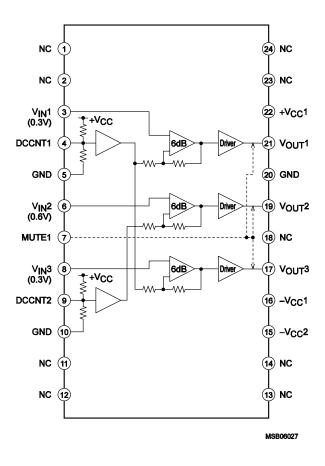
	Pin 7
н	THROUTH
L	MUTE

Package Dimensions

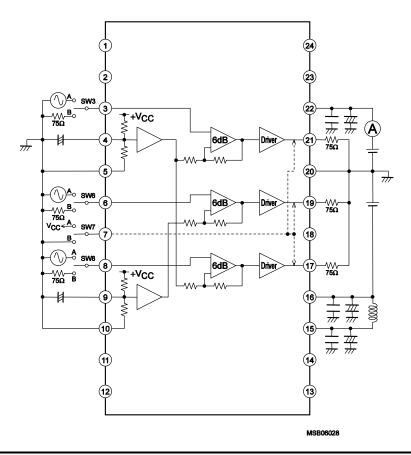
unit : mm 3315



Block Diagram



Test Circuit Diagram (Using ±power supply)



Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
1 2 11 12 13 14 18 23 24 3 6 8	NC VIN1 VIN2 VIN3	Input terminal. Non-bias. It is possible to use with being directly connected with DC. When DC coupling, it is necessary to add bias after the coupling.	+V _C C 9pF 1.2kΩ 1.6kΩ -V _C C -V _C C MSP06323
4 9	DCCNT1 DCCNT2	DC offset mode charge terminal between input and output. When a condenser is input at the position between pin 4 (DCCNT1) and GND, the operation of IC becomes the mode with 0.3VDC offset between input and output of 1, 3ch (pins 3 and 21, pins 8 and 17). Similarly when a condenser is input at the position between pin 9 (DCCNT2) and GND, it becomes the mode with 0.6VDC offset between input and output of 2ch (pins 6 and 19). And when pins 4, 9 and GND is shorted, it becomes the mode without DC offset between input and output.	+Vcc +Vcc +Vcc +Vcc -Vcc pin5,10 MSP06374
5 10 20	GND	Both ±power supply and +power supply are GND.	
7	MUTE1	Changeover terminal of Mute. When the Mute terminal is Low, it is Mute. When the terminal is Open, it is Low.	9kΩ 9kΩ S S S S S S S S S S S S S S S S S S S

Continued on next page.

Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
15 16	-VCC	$-V_{CC}$ of using ±power supply. Using +power supply, it is GND.	
17 19 21	V _{OUT} 3 V _{OUT} 2 V _{OUT} 1	Output terminal. Using ±power supply, in case of the mode with DC offset, it is possible to use without capacitor of output by setting pins 3, 8 to 0.3V-bias and by setting pin 6 to 0.6V-bias. And in case of the mode without DC offset, it is possible to use without capacitor of output by setting each input to zero-bias. When using +power supply, both of the modes needs coupling capacitor.	+VCC +VCC -VCC -VCC MSP06326
22	+VCC	Both ±power supply and +power supply are +V _{CC} .	

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