

# SANYO Semiconductors DATA SHEET

# LA7016 — Monolithic Linear IC VCR Electronic Switch

#### Features

- Wide input dynamic range.
- Low distortion.
- Good frequency characteristic.

### **Specifications**

#### Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		15	V
Allowable power dissipation	Pd max	Ta ≤ 65°C	300	mW
Operating temperature	Topr		-20 to +65	°C
Storage temperature	Tstg		-40 to +125	°C

#### Electrical Characteristics at Ta = 25°C, $V_{CC} = 12V$

Deventer	Symbol	Qualities	Ratings			11-11
Parameter		Conditions	min	typ	max	Unit
Circuit current	۱ <sub>D</sub>			9.3	12.5	mA
Total harmonic distortion	THD	*Rg = 600Ω, 4.5Vp-p, f = 1kHz, R <sub>L</sub> = $\infty$		0.007	0.1	%
Noise	en	$^{*}$ Rg = 600 $\Omega$ , f = 20Hz to 20kHz, RL = $\infty$		-93	-80	dBs
Crosstalk	I <sub>S</sub> 1	*input A : Rg = 50Ω, f = 3.58MHz 2Vp-p,	50	68		dB
		input B : Rg = $1k\Omega$				
Pedestal	∆Vped	V <sub>3</sub> = 2.2V to 3.0V	-100	0	+100	mV
Second harmonic		Rg = 50 $\Omega$ , f = 1MHz, 4.0Vp-p, R <sub>L</sub> = $\infty$	46	55		dB
Third harmonic		$Rg = 50\Omega$ , f = 1MHz, 4.0Vp-p, $R_L = \infty$	46	52		dB
Control, threshold voltage	V <sub>3s</sub>		2.2	2.6	3.0	V

Continued on next page.

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## SANYO Semiconductor Co., Ltd.

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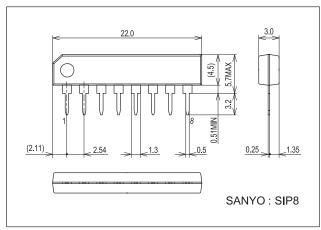
#### Continued from preceding page.

Parameter	Symbol	Conditions		11-21		
			min	typ	max	Unit
Pin voltage (pin 4)	V4			6.9	6.9	V
Pin voltage (pin 7)	V <sub>7</sub>	$V_3 = 2.2V$		7.6		V
Pin voltage (pin 7)	V <sub>7</sub>	V <sub>3</sub> = 3.0V		7.6		V
Pin voltage (pin 2)	V <sub>2</sub>	V <sub>3</sub> = 3.0V		7.6		V
Pin voltage (pin 2)	V <sub>2</sub>	V <sub>3</sub> = 2.2V		7.6		V

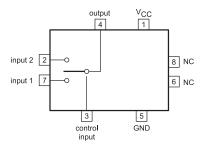
Note) \* : Test for input 1 and input2. For input 1 test, Vcont (pin 3 voltage) is 2.0V. For input 2 test, Vcont is 3.0V.

## **Package Dimensions**

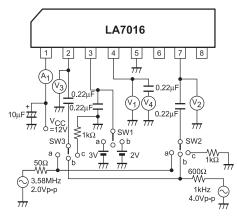
unit : mm (typ) 3016C



## **Equivalent Circuit**



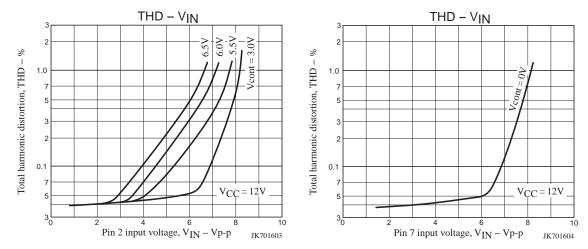
## **Test Circuit**

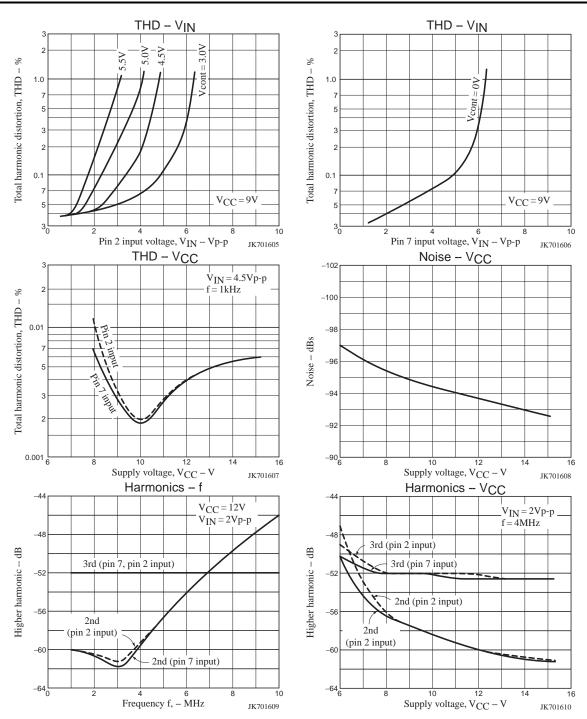


## **Test Conditions**

ltem	Symbol		Testadat		
		SW1	SW2	SW3	Test point
Circuit current	۱ <sub>D</sub>	С	С	С	A <sub>1</sub>
Distortion (1)	THD	b	b	С	V4
Distortion (2)	THD	а	с	b	V4
Noise (1)	e <sub>n</sub>	b	С	С	V4
Noise (2)	e <sub>n</sub>	а	с	с	V4
Crosstalk (1)	I <sub>S</sub> 1	b	с	а	V4
Crosstalk (2)	I <sub>S</sub> 2	а	а	С	V <sub>4</sub>
Pedestal	$\Delta V_{PED}$	a-b	С	С	V <sub>1</sub>
Pin voltage (pin 4)		b	С	С	V <sub>1</sub>
Pin voltage (pin 7)		b	с	с	V <sub>2</sub>
Pin voltage (pin 7)		а	С	С	V2
Pin voltage (pin 2)		а	С	С	V <sub>3</sub>
Pin voltage (pin 2)		b	С	С	V <sub>3</sub>

## **Main Characteristics**





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