



# 5-Band Graphic Equalizer

# **Applications**

 Portable component stereos, tape-recorders, radio-cassette recorders, car stereos.

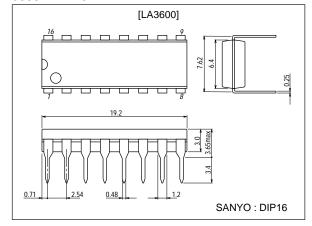
### **Features**

- On-chip one operational amplifier.
- 5-band graphic equalizer for one channel can be formed easily by externally connecting capacitors and variable resistors which fix f<sub>0</sub> (resonance frequency).
- Series connection of two LA3600's makes multiband (6 to 10 bands) available.
- Highly stable to capacitive load.

# **Package Dimensions**

unit:mm

#### 3006B-DIP16



# **Specifications**

#### **Absolute Maximum Ratings** at $Ta = 25^{\circ}C$

| Parameter                   | Symbol              | Conditions | Ratings     | Unit |
|-----------------------------|---------------------|------------|-------------|------|
| Maximum Supply Voltage      | V <sub>CC</sub> max |            | 20          | V    |
| Allowable Power Dissipation | Pd max              |            | 300         | mW   |
| Operating Temperature       | Topr                |            | -20 to +75  | °C   |
| Storage Temperature         | Tstg                |            | -40 to +125 | °C   |

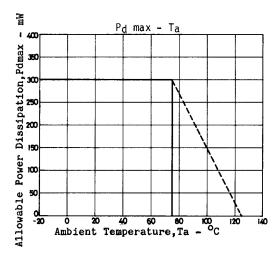
#### **Operating Conditions** at $Ta = 25^{\circ}C$

| Parameter                  | Symbol          | Conditions | Ratings | Unit |
|----------------------------|-----------------|------------|---------|------|
| Recommended Supply Voltage | V <sub>CC</sub> |            | 8       | V    |
| Operating Voltage Range    | VCC op          |            | 5 to 15 | V    |

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# $\textbf{Operating Characteristics} \ \ \text{at Ta} = 25^{\circ}C, \ V_{CC} = 8V, \ R_{L} = 10k\Omega, \ Rg = 600\Omega, \ See \ specified \ Test \ Circuit.$

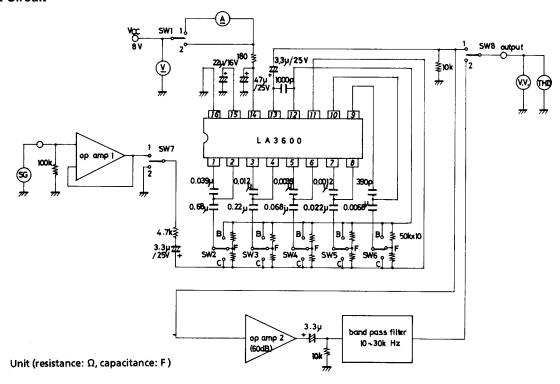
| Parameter                 | Symbol          | Conditions                         |   |      | Ratings |      |      |  |
|---------------------------|-----------------|------------------------------------|---|------|---------|------|------|--|
| Farameter                 | Symbol          | Conditions                         |   | min  | typ     | max  | Unit |  |
| Quiescent Current         | Icco            |                                    | 3.0   | 5.0  | 8.0     | mA   |      |  |
| Voltage Gain              | VG              | f=1kHz, Vin=-10dB at all flat mode |   |      | +0.8    | +2.2 | dB   |  |
|                           | BOOST           | f=100Hz                            |   | 8    | 10      | 12   | dB   |  |
|                           |                 | f=340Hz                            |   | 8    | 10      | 12   | dB   |  |
| Boost Amount              |                 | f=1kHz                             |   | 8    | 10      | 12   | dB   |  |
|                           |                 | f=3.4kHz                           |   | 8    | 10      | 12   | dB   |  |
|                           |                 | f=10kHz                            | Vo=-10dB is taken as<br>0dB at all flat mode at | 8    | 10      | 12   | dB   |  |
|                           | CUT             | f=100Hz                            | f=1kHz.   | -12  | -10     | -8   | dB   |  |
|                           |                 | f=340Hz                            |   | -12  | -10     | -8   | dB   |  |
| Cut Amount                |                 | f=1kHz                             |   | -12  | -10     | -8   | dB   |  |
|                           |                 | f=3.4kHz                           |   | -12  | -10     | -8   | dB   |  |
|                           |                 | f=10kHz                            |   | -12  | -10     | -8   | dB   |  |
| Total Harmonic Distortion | THD             | f=1kHz, V <sub>0</sub> =1.0V       |   | 0.03 | 0.1     | %    |      |  |
| Output Noise Voltage      | V <sub>NO</sub> | Rg=0, All flat B.P.F. 10Hz         |   | 2.0  | 20      | μV   |      |  |



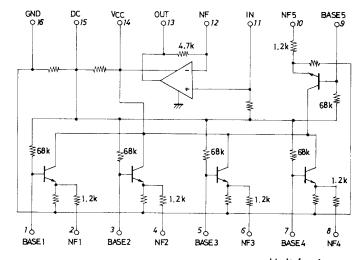
Test Method :  $V_{CC}\!\!=\!\!8\text{V},\,R_L\!\!=\!\!10\text{k}\Omega,\,Rg\!\!=\!\!600\Omega$ 

| Item            | SW1 | SW2 | SW3 | SW4 | SW5 | SW6 | SW7 | SW8 | Conditions        |
|-----------------|-----|-----|-----|-----|-----|-----|-----|-----|-------------------|
| Icco            | 1   | -   | -   | -   | -   | -   | 2   | 1   |                   |
| VG              | 2   | F   | F   | F   | F   | F   | 1   | 1   | f=1kHz, Vin=-10dB |
| BOOST           | 2   | В   | F   | F   | F   | F   | 1   | 1   | f=100Hz           |
| BOOST           | 2   | F   | В   | F   | F   | F   | 1   | 1   | f=340Hz           |
| BOOST           | 2   | F   | F   | В   | F   | F   | 1   | 1   | f=1kHz            |
| BOOST           | 2   | F   | F   | F   | В   | F   | 1   | 1   | f=3.4kHz          |
| BOOST           | 2   | F   | F   | F   | F   | В   | 1   | 1   | f=10kHz           |
| CUT             | 2   | С   | F   | F   | F   | F   | 1   | 1   | f=100Hz           |
| CUT             | 2   | F   | С   | F   | F   | F   | 1   | 1   | f=340Hz           |
| CUT             | 2   | F   | F   | С   | F   | F   | 1   | 1   | f=1kHz            |
| CUT             | 2   | F   | F   | F   | С   | F   | 1   | 1   | f=3.4kHz          |
| CUT             | 2   | F   | F   | F   | F   | С   | 1   | 1   | f=10kHz           |
| THD             | 2   | F   | F   | F   | F   | F   | 1   | 1   | f=1kHz, Vo=1.0V   |
| V <sub>NO</sub> | 2   | F   | F   | F   | F   | F   | 2   | 2   |                   |

## **Test Circuit**



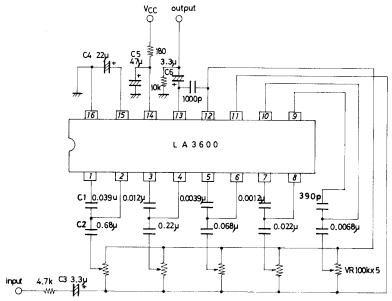
## **Equivalent Circuit Block Diagram**



Unit (resistance:  $\Omega$ , capacitance: F)

#### **Sample Application Circuit**

Unit (resistance:  $\Omega$ , capacitance: F)



fo (resonance frequency)

In the sample application circuit, fo for each of 5 bands is set as follows :

fo=108Hz, 343kHz, 1.08kHz, 3.43kHz, 10.8kHz

fo= 
$$\frac{1}{2\pi\sqrt{C1,C2,R1,R2}}$$
 (R1=1.2k $\Omega$ , R2=68k $\Omega$  on-chip resistor)

Description of external parts

C1, C2 : Capacitors used to fix fo (resonance frequency)

C2 : Input capacitor. Decreasing the capacitor value lowers the frequency response at low frequencies.

C3 : Input capacitor. Decreasing the capacitor value lowers the frequency response at low frequencies.

C4 : Decoupling capacitor. Decreasing the capacitor value makes the effect of power supply stronger, whereby

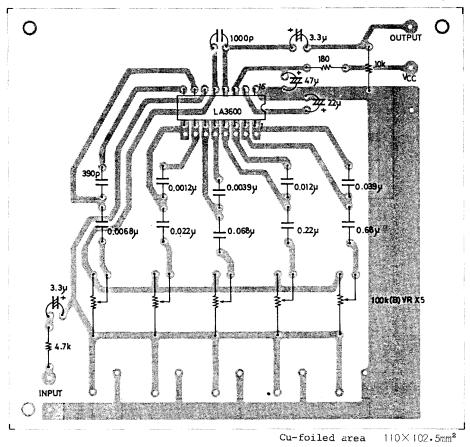
ripple is liable to occur.

C5 : Power capacitor.

C6 : Output capacitor. Decreasing the capacitor value lowers the frequency response at low frequencies.

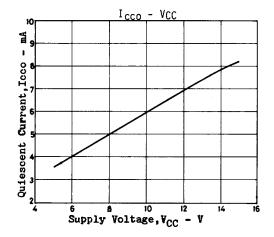
# **Sample Printed Circuit Pattern**

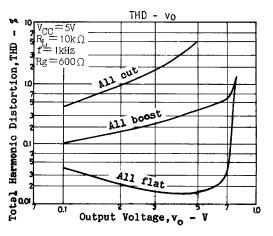
Unit (resistance:  $\Omega$ , capacitance: F)

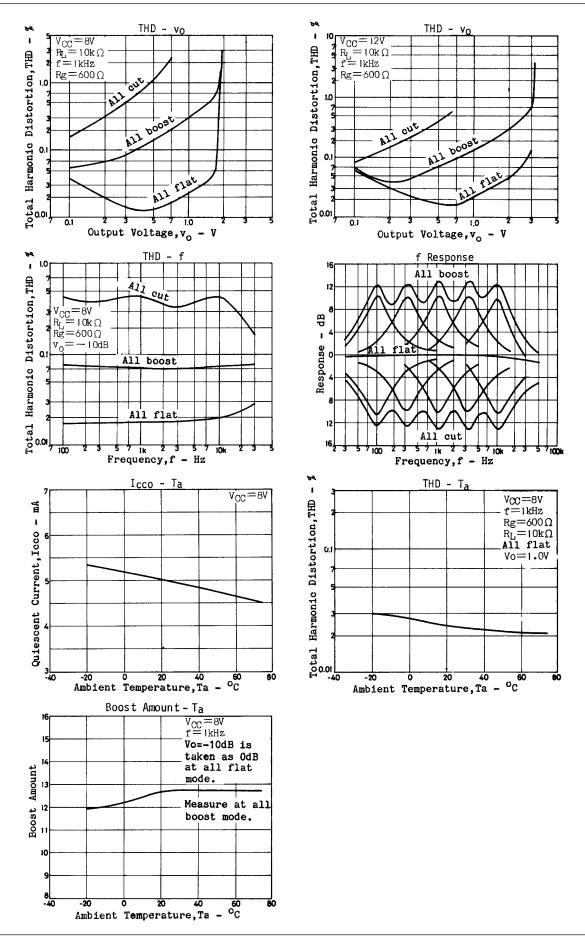


## Proper cares in using IC

- · Maximum supply voltage V<sub>CC</sub> max 20V must not be exceeded. The operating voltage is in the range of 5 to 15V.
- · Application of power with the pin-to-pin spaces shorted causes breakdown or deterioration of the IC to occur. When mounting the IC on the board or applying power, make sure that the pin-to-pin spaces are not shorted with solder, etc.







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