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Structure	:	Silicon Monolithic Integrated Circuit	
Product name	:	Five inputs Dual Circuits Video Signal Switchers	
Туре	:	B A 7626 F , BA7626 FS	
Features	:	 5 input line, 3 output line switching Built-in 6 dB amplifier 	

- 3) 5V operating voltage
- 4) $20k\Omega$ input impedance

OAbsolute Maximum Ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit	
Supply voltage		Vcc	9	V	
Power	BA7626F	Pd	300 *1	mW	
dissipation	BA7626FS	Fu	650 *2	11100	
Operating temperature		Topr	-25~+70	°C	
Storage temperature		Tstg	-55~+125	°C	

- *1 Deratings is done at 3.0mW/°C above Ta=25°C.
- *2 When mounted on a 70mm × 70mm × 1.6mm PCB board Deratings is done at 6.0mW/℃ above Ta=25℃.

OOperating Range (Ta=25°C)

Parameter	Symbol	Min	Тур	Max	Unit
Power supply voltage	Vcc	4.5	5.0	5.5	V

*This product is not designed for protection against radioactive rays.

Application example

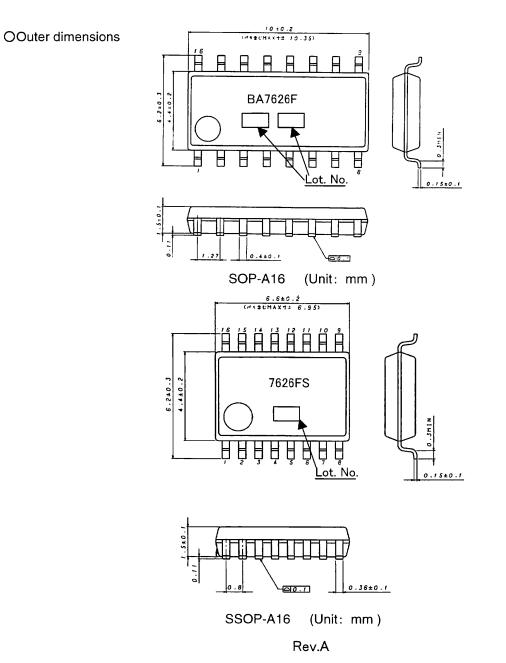
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OElectrical characteristics (Unless otherwise noted, Ta= 25°C, Vcc=5.0V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Circuit current	ICC	-	15.0	20.0	mA	-
Maximum output level	Vom	2.3	2.5	-	Vp-p	f=1kHz, THD=0.5%
Voltage gain	Gv	5.7	6.2	6.7	dB	f=1MHz, V _{IN} =1Vp-p
Interchannel crosstalk	G⊤	-	-65	-45	dB	f=4.43MHz,
						V _{IN} =1Vp-p
Mute level	СТМ	-	-35	-25	dB	f=4.43MHz,
						V _{IN} =1Vp-p
Frequency characteristic	G _f	-3	0	+3	dB	10MHz/1MHz,
						V _{IN} =1Vp-p
Input inpedance	Z _{IN}	16	20	24	kΩ	-
Circuit current	V _{TH}	2.1	-	3.3	V	-

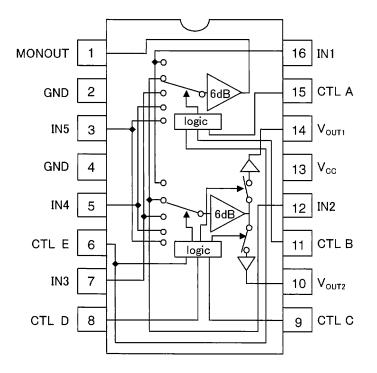






OBlock diagram

OPin number and pin name



Pin name
MONOUT
GND
IN5
GND
IN4
CTLE
IN3
CTLD
CTLC
VOUT2
CTLB
IN2
Vcc
VOUT1
CTLA
IN1

OCautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

4) Shorts between pins and miss-installation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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As of 18th. April 2005