# 2878 AND 2879

#### ų 3 K3-4 0 ű 9 Š . ĥ ~ SUB **B**2 9 K 1-2 5 ຽ 4 Æ **SND** 3 ۍ 2 ш Dwg. No. A-11,974

#### ABSOLUTE MAXIMUM RATINGS at +25°C Free-Air Temperature for any driver

(unless otherwise noted)

Output Voltage, V <sub>CEX</sub> (UDN2878W)
(UDN2879W & UDN2879W-2) 80 V
Output Current, I <sub>C</sub>
(UDN2878W & UDN2879W) 5.0 A
(UDN2879W-2) 4.0 A
Input Voltage, V <sub>IN</sub> 15 V
Input Current, I <sub>IN</sub>
Supply Voltage, V <sub>S</sub> 10 V
Total Package Power Dissipation,
P <sub>D</sub> See Graph
Operating Ambient Temperature Range,
T <sub>A</sub>
Storage Temperature Range,
T <sub>S</sub> <b>-55°C to +150°C</b>

# **QUAD HIGH-CURRENT DARLINGTON SWITCHES**

These quad Darlington arrays are designed to serve as interface between low-level logic and peripheral power devices such as solenoids, motors, incandescent displays, heaters, and similar loads of up to 320 W per channel. Both integrated circuits include transient-suppression diodes that enable use with inductive loads. The input logic is compatible with most TTL, DTL, LSTTL, and 5 V CMOS logic.

Type UDN2878W and UDN2879W 4 A arrays are identical except for output-voltage ratings. The former is rated for operation to 50 V (35 V sustaining), while the latter has a minimum output breakdown rating of 80 V (50 V sustaining). The lower-cost UDN2879W-2 is recommended for applications requiring load currents of 3 A or less. These less expensive devices are identical to the basic parts except for the maximum allowable load-current rating.

For maximum power-handling capability, all drivers are supplied in a 12-pin single in-line power-tab package. The tab needs no insulation. External heat sinks are usually required for proper operation of these devices.

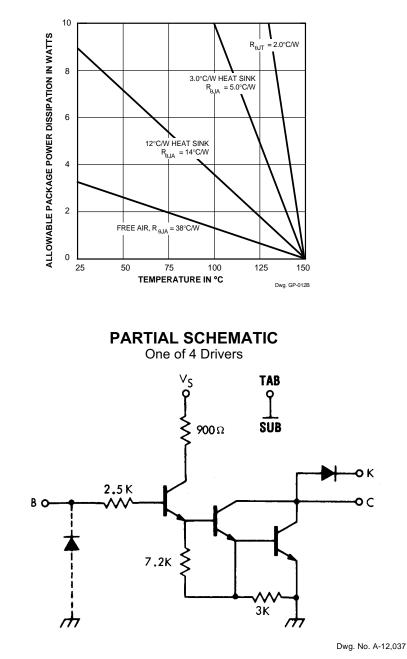
## FEATURES

- Output Currents to 4 A
- Output Voltages to 80 V
- Loads to 1280 W
- TTL, DTL, or CMOS Compatible Inputs
- Internal Clamp Diodes
- Plastic Single In-Line Package
- Heat-Sink Tab

Always order by complete part number:

Part Number	Max. I <sub>C</sub>	Max. V <sub>CEX</sub>	Min. V <sub>CE (sus)</sub>
UDN2878W	5.0 A	50 V	35 V
UDN2879W	5.0 A	80 V	50 V
UDN2879W-2	4.0 A	80 V	50 V





NOTE: Pin 3 must be connected to ground for proper operation.



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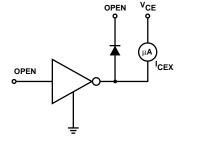
# ELECTRICAL CHARACTERISTICS at V<sub>S</sub> = 5.0 V, T<sub>A</sub> = +25°C (unless otherwise noted).

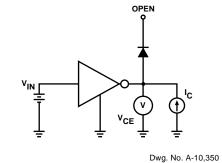
		Test	Applicable			Limit	s
Characteristic	Symbol	Fig.	Devices	Test Conditions	Min.	Max.	Units
Output Leakage Current	I <sub>CEX</sub>	1	UDN2878W	V <sub>CE</sub> = 50 V	_	100	μA
				V <sub>CE</sub> = 50 V, T <sub>A</sub> = +70°C	_	500	μA
			UDN2879W/W-2	V <sub>CE</sub> = 80 V	_	100	μA
				V <sub>CE</sub> = 80 V, T <sub>A</sub> = +70°C	_	500	μA
Output Sustaining	V <sub>CE(sus)</sub>	—	UDN2878W	I <sub>C</sub> = 4 A, L = 10 mH	35	_	V
Voltage			UDN2879W	I <sub>C</sub> = 4 A, L = 10 mH	50	_	V
			UDN2879W-2	I <sub>C</sub> = 3 A, L = 10 mH	50	_	V
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>	2	All	I <sub>C</sub> = 500 mA, V <sub>IN</sub> = 2.75 V	_	1.1	V
				I <sub>C</sub> = 1.0 A, V <sub>IN</sub> = 2.75 V	_	1.3	V
				I <sub>C</sub> = 2.0 A, V <sub>IN</sub> = 2.75 V	_	1.5	V
				I <sub>C</sub> = 3.0 A, V <sub>IN</sub> = 2.75 V	_	1.9	V
			UDN2878/79W	I <sub>C</sub> = 4.0 A, V <sub>IN</sub> = 3.0 V	_	2.4	V
Input Current	I <sub>IN</sub>	3	All	V <sub>IN</sub> = 2.75 V	_	550	μA
				V <sub>IN</sub> = 3.75 V	_	1000	μA
Input Voltage	V <sub>IN(ON)</sub>	4	All	V <sub>CE</sub> = 2.2 V, I <sub>C</sub> = 3.0 A	_	2.75	V
			UDN2878/79W	$V_{CE} = 2.2 \text{ V}, I_{C} = 4.0 \text{ A}$	_	2.75	V
Supply Current per Driver	ا <sub>S</sub>	7	All	I <sub>C</sub> = 500 mA, V <sub>IN</sub> = 2.75 V	_	6.0	mA
Turn-On Delay	t <sub>PLH</sub>	—	All	0.5 E <sub>in</sub> to 0.5 E <sub>out</sub>	_	1.0	μs
Turn-Off Delay	t <sub>PHL</sub>	—	All	0.5 $E_{in}$ to 0.5 $E_{out}$ , $I_{C}$ = 3.0 A	_	1.5	μs
Clamp Diode	I <sub>R</sub>	5	All	V <sub>R</sub> = 50 V	_	50	μA
Leakage Current				V <sub>R</sub> = 50 V, T <sub>A</sub> = +70°C	_	100	μA
			UDN2879W/W-2	V <sub>R</sub> = 80 V	_	50	μA
				V <sub>R</sub> = 80 V, T <sub>A</sub> = +70°C	_	100	μA
Clamp Diode	V <sub>F</sub>	6	All	I <sub>F</sub> = 3.0 A	_	2.5	V
Forward Voltage			UDN2878/79W	I <sub>F</sub> = 4.0 A	_	3.0	V

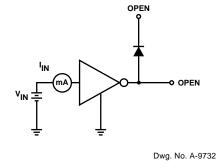
Caution: High-current tests are pulse tests or require heat sinking.

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**TEST FIGURES** 



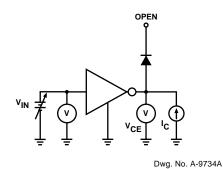




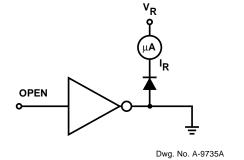
Dwg. No. A-9729A







**FIGURE 1** 



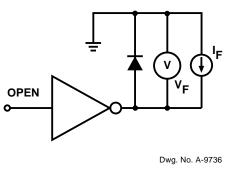
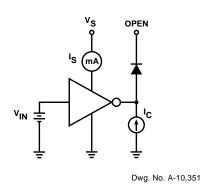


FIGURE 4

**FIGURE 5** 

**FIGURE 6** 

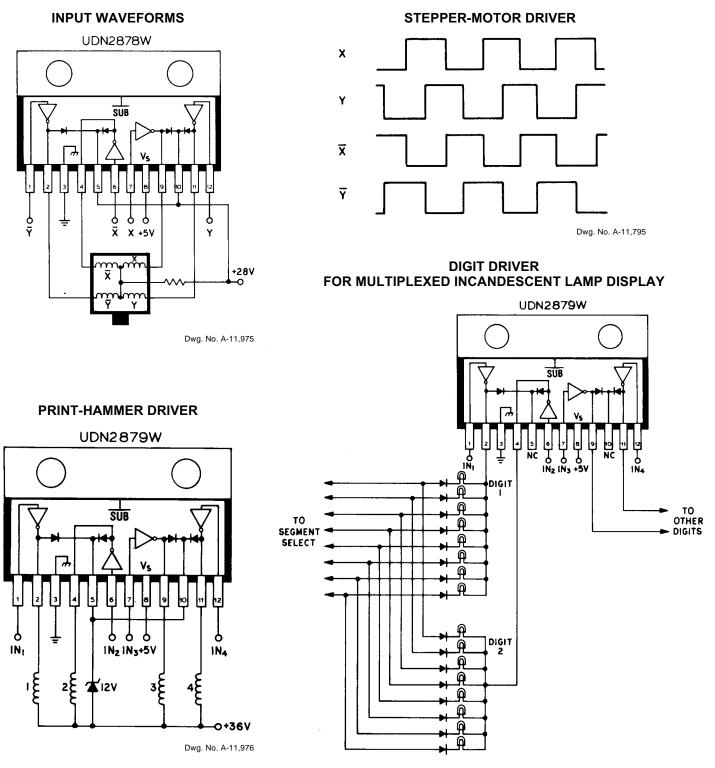


**FIGURE 7** 

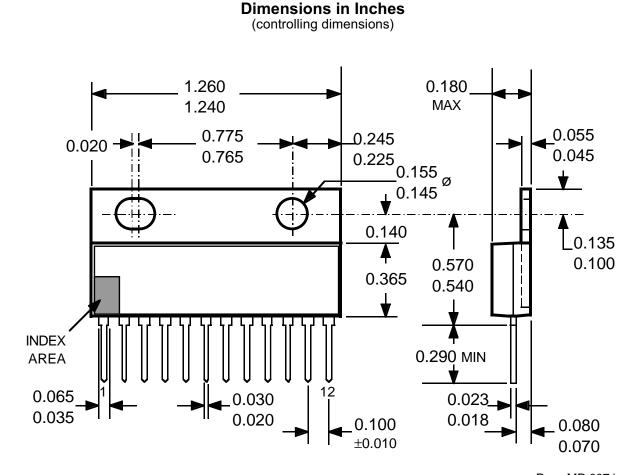


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#### **TYPICAL APPLICATIONS**



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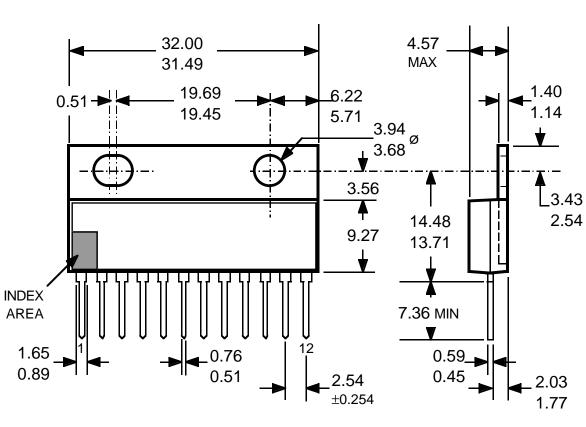
Dwg. MP-007 in

NOTES: 1. Lead thickness is measured at seating plane or below.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.
- 4. Lead gauge plane is 0.030" below seating plane.
- 5. Supplied in standard sticks/tubes of 15 devices.



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Dimensions in Millimeters (for reference only)

Dwg. MP-007 mm

NOTES: 1. Lead thickness is measured at seating plane or below.

- 2. Lead spacing tolerance is non-cumulative.
- 3. Exact body and lead configuration at vendor's option within limits shown.
- 4. Lead gauge plane is 0.762 mm below seating plane.
- 5. Supplied in standard sticks/tubes of 15 devices.

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# **POWER SINK DRIVERS**

#### IN ORDER OF 1) OUTPUT CURRENT, 2) OUTPUT VOLTAGE, 3) NUMBER OF DRIVERS

0.	tout Datin				Features	5		
Output Ratings *		Serial	Serial Latched Diode Inte			Internal		
mA	v	#	Input	Drivers	Clamp	Outputs	Protection	Part Number <sup>†</sup>
75	17	8	X	Х		constant current	_	6275
	17	16	Х	Х	_	constant current	-	6276
100	20	8	_	_	_	saturated	_	2595
	30	32	Х	Х	-	-	_	5833
	40	32	Х	. X .		saturated	-	5832
	50	8	addre	essable deco	der/driver	DMOS	-	6B259
	50 50	8 8	x	X X	-	DMOS DMOS	-	6B273 6B595
100		-			_		—	
120	24	8	Х	Х		constant current	-	6277
250	50	8	addre	essable deco	der/driver	DMOS	-	6259
	50	8	_	X	-	DMOS	-	6273
	50	8	Х	Х	-	DMOS	-	6595
	50	8	_	_	Х	saturated	_	2596
	60	4	—	—	Х	saturated	Х	2557
350	50	4	_	Х	Х	-	-	5800
	50	7	_	_	X X	_	_	2003
	50	7	-	-	X	-	-	2004
	50	8	-		X X	-	-	2803
	50 50	8	x	X	X	-	-	5801 5821
	50 50	O Q	Â	X X	x	-	_	5841
	50	8		essable deco		DMOS	_	6A259
	50	8	X	X		DMOS	_	6A595
	80	8	x	x	_	_	_	5822
	80	8	X	X	Х	_	_	5842
	95	7	_	_	Х	_	_	2023
	95	7	_	_	Х	-	-	2024
450	30	28	dual 4	4- to 14-line c	decoder/driv	ver –	_	6817
600	60	4	_	_	_	saturated	Х	2547
	60	4	_	-	Х	saturated	Х	2549 and 2559
700	60	4	_	_	Х	saturated	Х	2543
750	50	8	_	_	Х	saturated	_	2597
1000	46	4	stepp	er motor con	troller/drive	r MOS	_	7024 and 7029
1200	46	4		stepping con			_	7042
1250	50	4		per motor tran			Х	5804
1800	50	4		_	Х	_	_	2540
3000	46	4	stepp	er motor con	troller/drive	r MOS	-	7026
	46	4		stepping con			_	7044

\* Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits.

<sup>†</sup> Complete part number includes additional characters to indicate operating temperature range and package style.



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