

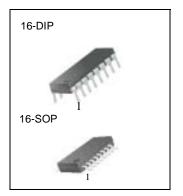
TL494 SMPS Controller

Features

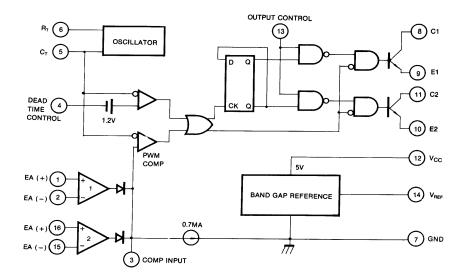
- Internal regulator provides a stable 5V reference supply trimmed to 5%
- Uncommitted output TR for 200mA sink or source current
- Output control for push-pull or single-ended operation
- Variable duty cycle by dead time control (pin 4) Complete PWM control circuit
- On-chip oscillator with master or slave operation
- Internal circuit prohibits double pulse at either output

Description

The TL494 is used for the control circuit of the PWM switching regulator. The TL494 consists of 5V reference voltage circuit, two error amplifiers, flip flop, an output control circuit, a PWM comparator, a dead time comparator and an oscillator. This device can be operated in the switching frequency of 1 KHz to 300 KHz.



Internal Block Diagram



Rev. 5.0

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	42	V
Collector Supply Voltage	Vc	42	V
Output Current	Io	250	mA
Amplifier Input Voltage	VIN	Vcc + 0.3	V
Power Dissipation (T _A = 25°C)	PD	1 (TL494CN) 0.9 (TL494CD)	W
Operating Temperature Range	TOPR	0 ~ +70	°C
Storage Temperature Range	TSTG	-65 ~ + 150	°C

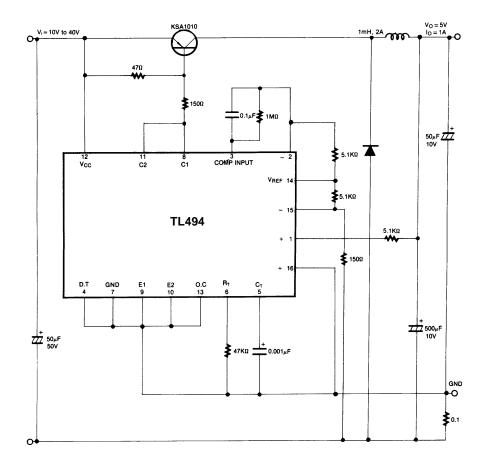
Electrical Characteristics

(V_{CC} = 20V, f = 10KHz, $T_A = 0$ °C to + 70°C, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
REFERENCE SECTION							
Reference Output Voltage	VREF	IREF = 1mA	4.75	5.0	5.25	V	
Line Regulation	ΔV REF	Vcc = 7V to 40V	-	2.0	25	mV	
Temperature Coefficient of VREF	ΔVREF/ΔT	ΔT TA = 0°C to 70°C		0.01	0.03	%/°C	
Load Regulation	ΔVREF	IREF = 1mA to 10mA	-	1.0	15	mV	
Short-Circuit Output Currnet	Isc	VREF = 0V	10	35	50	mA	
OSCILLATOR SECTION							
Oscillation Frequency	f	$C_T = 0.01 \mu F, R_T = 12 K\Omega$	-	10	-	KHz	
Frequency Change with Temperature	Δf/ΔΤ	$C_T = 0.01 \mu F, R_T = 12 K\Omega$	-	-	2	%	
DEAD TIME CONTROL SECTION			1			ı	
Input Bias Current	IBIAS	VCC = 15V, 0V≤V4≤5.25V	-	-2.0	-10	μΑ	
Maximum Duty Cycle	D _(MAX)	VCC = 15V, V4 = 0V O.C Pin = VREF	45	-	-	%	
Input Threshold Voltage	VITH	Zero Duty Cycle	-	3.0	3.3	,,,	
		Max. Duty Cycle	0	-	-	V	
ERROR AMP SECTION				I	ı	l	
Input Offset Voltage	Vio	V ₃ = 2.5V	-	2.0	10	mV	
Input Offset Current	lio	V ₃ = 2.5V	-	25	250	mΑ	
Input Bias Current	IBIAS	V3 = 2.5V	-	0.2	1.0	μΑ	
Common Mode Input Voltage	Vсм	7V ≤ V _{CC} ≤ 40V	-0.3	-	Vcc	V	
Open-Loop Voltage Gain	Gvo	0.5V ≤ V ₃ ≤3 .5V	70	95	-	dB	
Unit-Gain Bandwidth	BW	-	-	650	-	KHz	
PWM COMPARATOR SECTION				I	ı	l	
Input Threshold Voltage	VITH	Zero Duty Cycle	-	4	4.5	V	
Input Sink Currnet	ISINK	V ₃ =0.7V	-0.3	-0.7	-	mV	
OUTPUT SECTION						I	
Output Saturation Voltage Common Emitter	VCE(SAT)	VE = 0, IC = 200mA	-	1.1	1.3	V	
Common Collector	VCC(SAT)	V _C = 15V, I _E = -200mA	-	1.5	2.5		
Collector Off-State Currnet	IC(OFF)	Vcc = 40V, VcE = 40V	-	2	100	. ^	
Emitter Off-State Current	IE(OFF)	Vcc = Vc = 40V, VE = 0	-	-	-100	μΑ	
TOTAL DEVICE	· · · · · · · · · · · · · · · · · · ·	ı	•	1			
Supply Current	Icc	Pin 6 = VREF, VCC = 15V	-	6	10	mA	
OUTPUT SWITCHING CHARACTERISTIC							
Rise Time	tR	-	-	-	-	-	
Common Emitter	-	-	-	100	200		
Common Collector	-	-	-	100	200	ns	
Fall Time	tF	-	-	-	-	-	
Common Emitter	-	-	-	25	100	ns	
Common Collector	-	-	-	40	100		

Typical Application

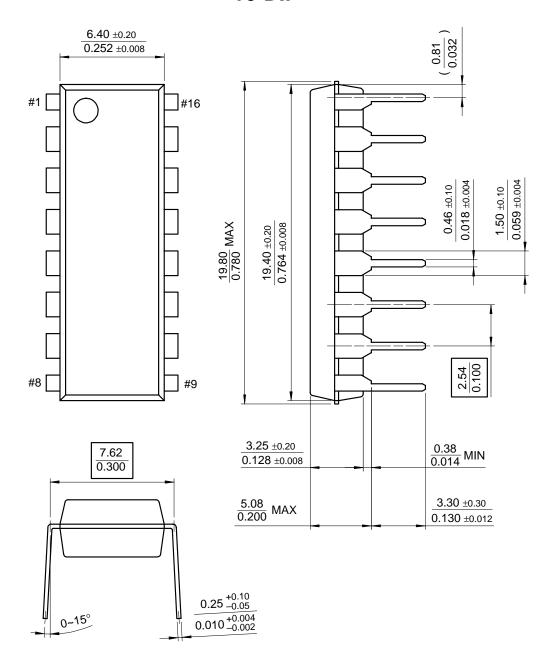
Pulse Width Modulated Step-down Converter



Mechanical Dimensions

Package

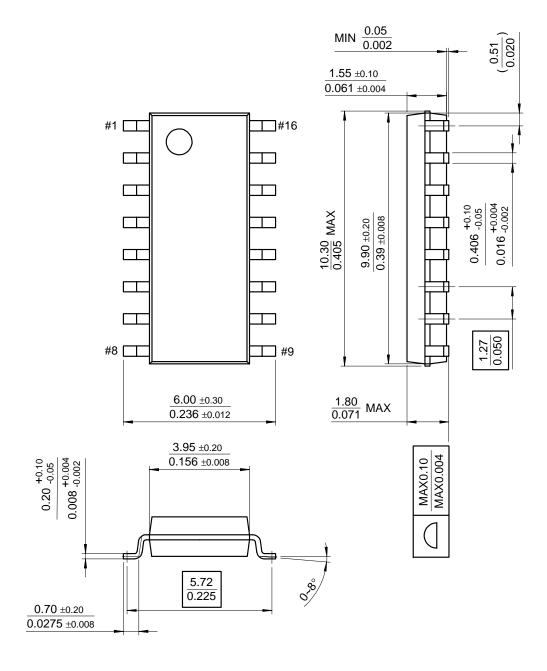
16-DIP



Mechanical Dimensions (Continued)

Package

16-SOP



Ordering Information

Product Number	Package	Operating Temperature
TL494CN	16 DIP	0 ~ + 70°C
TL494CD	16 SOP	0~+700

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