

SEMICONDUCTOR®

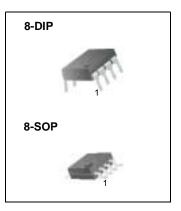
MC4558 Dual Operational Amplifier

Features

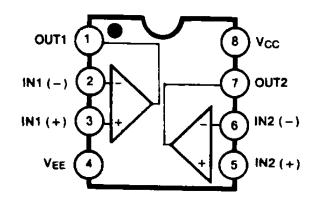
- No frequency compensation required.
- No latch up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

Descriptions

The MC4558 series is a monolithic integrated circuit designed for dual operational amplifier.

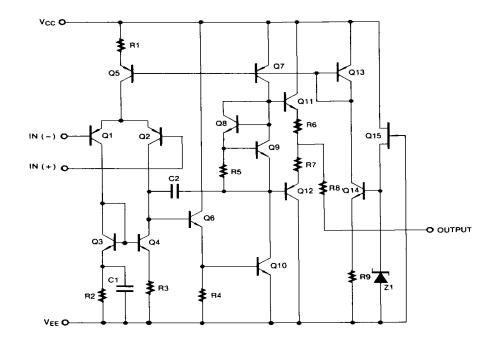


Internal Block Diagram



Schematic Diagram

(One Section Only)



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±22	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage	VI	±15	V
Power Dissipation	PD	400	mW
Operating Temperature Range MC4558C MC4558V	TOPR	0 ~ 70 -40 ~ 85	°C
Storage Temperature Range	TSTG	-65 ~ 150	°C

Electrical Characteristics

(VCC = 15V, VEE = - 15V , TA = 25 °C unless otherwise specified)

Denemator	0h.al	Conditions		MC4558C/MC4558V				
Parameter	Symbol			Min	Тур	Max	Unit	
Input Offset Voltage	Vio	Rs≤10KΩ		-	2	6	mV	
	VIO		Note 1	-	-	7.5	111V	
Input Offset Current				-	5	200		
	lio		TA=TA(MAX)	-	-	300	nA	
			TA =TA(MIN)	-	-	300		
Input Bias Current				-	30	500		
	IBIAS		TA=TA(MAX) -		-	800	nA	
			TA =TA(MIN)	-	-	800		
Large Signal Voltage Gain	Gv	VO(P-P)= ±10\	/,RL≤2KΩ	20	200	-	V/mV	
	0,		Note 1	-	-	-		
Common Mode Input Voltage Range	VI(R)			±12 ±1		-	V	
	VI(R)		Note 1	-	-	-	v	
Common Mode Rejection Ratio	CMRR	Rs≤10KΩ		70	90	-	dB	
	OWINN		Note 1	-	-	-		
Supply Voltage Rejection Ratio	PSRR	Rs≤10KΩ		76	90	-	dB	
			Note 1	76	90	-		
Output Voltage Swing	VO(P.P)	RL≥10KΩ		±12	±14	-	v	
	VO(F.F)	RL≥2KΩ		±10	±13	-	v	
Supply Current (Both Amplifiers)				-	3.5	5.8		
	ICC		TA =TA(MAX)	-	-	5.0	mA	
			TA =TA(MIN)	-	-	6.7		
Power Consumption (Both Amplifiers)				-	70	170		
	PC		TA =TA(MAX)	-	-	150	mW	
			$T_a = T_A(MIN)$	-	-	200		
Slew Rate (Note2)	SR	VI =10V, RL≥2KΩ Cl≤100pF		1.2	-	-	V/µs	
Rise Time (Note2)	TR	VI =20mV, RL≥2KΩ CI≤100pF		-	0.3	-	μs	
Overshoot (Note2)	OS	VI =20mV, RL≥2KΩ CI≤100pF		-	15	-	%	

Note :

 $1. \text{ MC4558C}: \text{T}_{A}(\text{MIN}) \leq \text{T}_{A} \leq \text{T}_{A}(\text{MAX}) = 0 \leq \text{T}_{A} \leq 70 \text{ °C} \text{ , } \text{MC4558V}: \text{T}_{A}(\text{MIN}) \leq \text{T}_{A} \leq \text{T}_{A}(\text{MAX}) = -40 \leq \text{T}_{A} \leq +85 \text{ °C} \text{ , } \text{MC4558V}: \text{T}_{A}(\text{MIN}) \leq \text{T}_{A} \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \leq 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{MC4558V} = -40 \approx 10 \text{ C} \text{ , } \text{ ,$

2. Guaranteed by design.



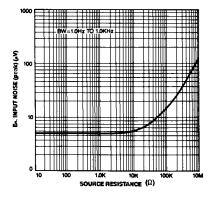


Figure 1. Burst Noise vs Source Resistance

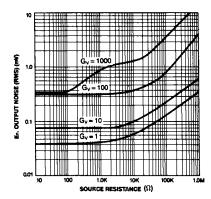


Figure 3. Output Noise vs Source Resistance

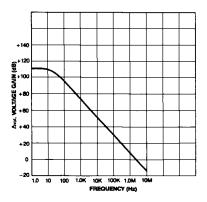


Figure 5. Open Loop Frequency Response

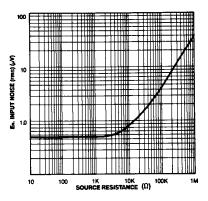


Figure 2. RMS Noise vs Source Resistance

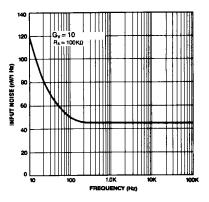


Figure 4. Spectral Noise Density

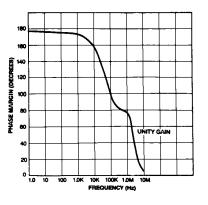


Figure 6. Phase Margin vs Frequency

Typical Performance Characteristics (continued)

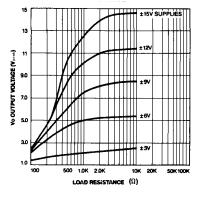


Figure 7. Positive Output Voltage Swing vs Load Resistance

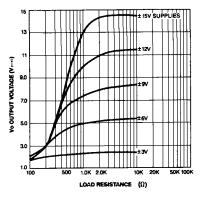


Figure 8. Negative Output Voltage Swing vs Load Resistance

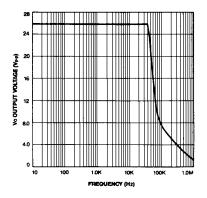
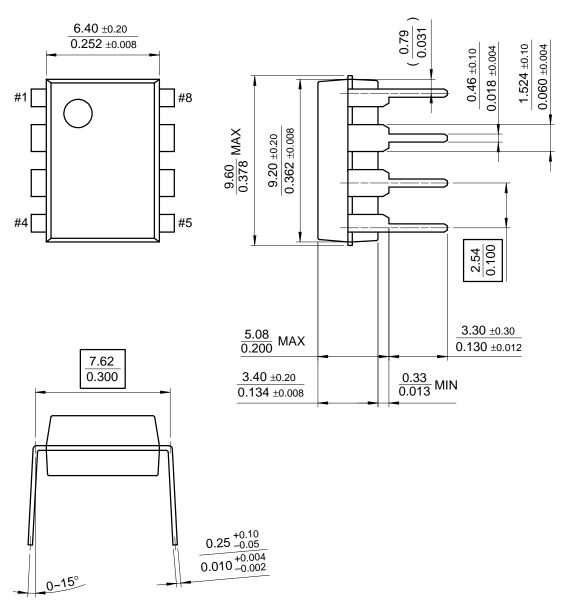


Figure 9. Power Bandwidth (Large Signal Output Swing vs Frequency)

Mechanical Dimensions

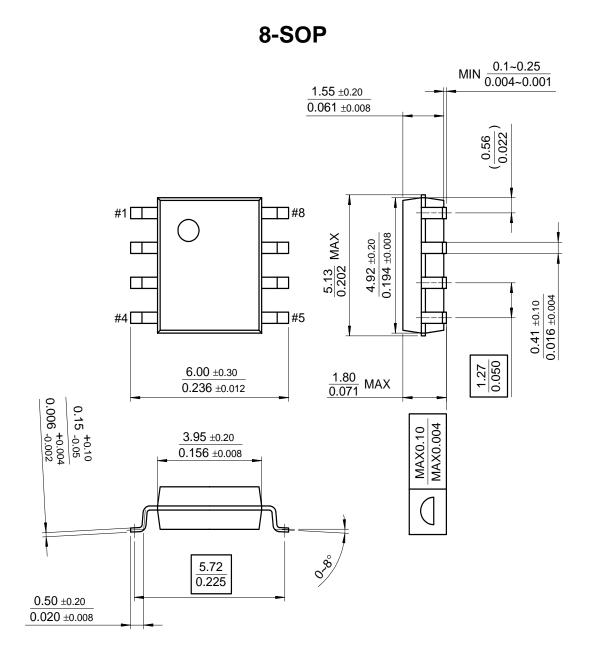
Package



8-DIP

Mechanical Dimensions (Continued)

Package



Ordering Information

Product Number	Package	Operating Temperature		
MC4558CP	8-DIP	0 ~ + 70°C		
MC4558CD	8-SOP	0~+700		
MC4558VP	8-DIP	-40 ~ +85°C		
MC4558VD	8-SOP	-40 ~ +65 C		

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