

SANYO Semiconductors DATA SHEET

$\label{eq:loss} LA73050 - \begin{array}{c} \mbox{Monolithic Linear IC} \\ \mbox{6ch 75}\Omega \mbox{ Video Driver} \end{array}$

Overview

This LA73050 is a 6ch 75 Ω Video Driver IC. The LA73050 is ideal for use the video output driver such as VCR and DVD-player equipment.

Functions

• 6dB AMP+driver (6ch)

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

-				
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		±7, +14	V
Allowable power dissipation	Pd max	Ta ≤ 80°C ∗	600	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-55 to +150	°C

* When mounted on a 114.3×76.1×1.6mm³ glass epoxy board.

Recommended Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Recommending operation voltage	Vcc		±5, +9	V
Operating voltage range	V _{CC} op		±4.0 to ±5.5	V
			+8 to +10	

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Descusion	Quarterst	One differen	Ratings			11-20
Parameter	Symbol	Conditions	min	typ	max	Unit
Current dissipation	ICC1	No signal	56.1	66	75.9	mA
Voltage gain	VG	V _{IN} = 1Vp-p, f = 4.43MHz	5.7	6.2	6.7	dB
Frequency characteristics 1	VF1	V _{IN} = 1Vp-p, f = 100k/5MHz	-1.0	0	1.0	dB
Frequency characteristics 2	VF2	V _{IN} = 1Vp-p, f = 100k/27MHz		-25	-20	dB
Group delay	GD	f = 100k/4.43MHz		±10	±15	ns
Maximum output level	V _O max	f = 1kHz, THD = 1%	3.0	4.0		Vp-p
Control voltage H level	V _{cnt} H	Pins 7, 16 input voltage	2.5		V _{CC}	V
Control voltage L level	V _{cnt} L	Pins 7, 16 input voltage	0		1.0	V

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = \pm 5V$, The mode with DC offset.

Design guarantee items

Description	0. milest	Qualifican	Ratings			
Parameter	Symbol	Conditions	min	typ	max	Unit
Video S/N	VG _{1V}			-75	-70	dB
Differential Gain	DG	V _{IN} = 1Vp-p, RAMP signal			1.0	%
Differential Phase	DP	V _{IN} = 1Vp-p, RAMP signal			1.0	deg.
Mute attenuation	VMUTEV	V _{IN} = 1Vp-p, f = 4.43MHz		-60	-55	dB
Cross-talk between	^V СТКV	V _{IN} = 1Vp-p, f = 4.43MHz		-60	-55	dB
channel						

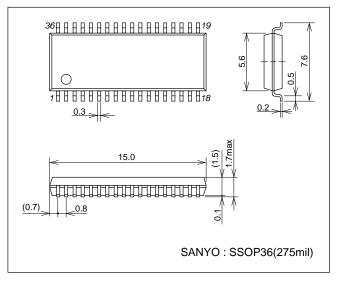
Truth Table

	Pins 7, 16
н	THROUTH
L	MUTE

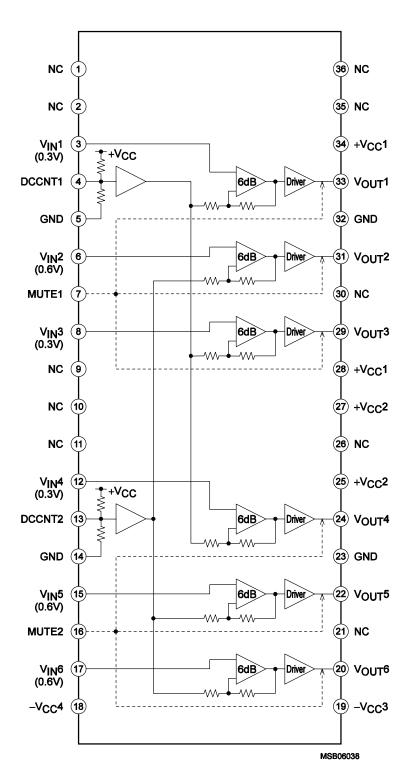
Package Dimensions

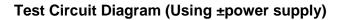
unit : mm

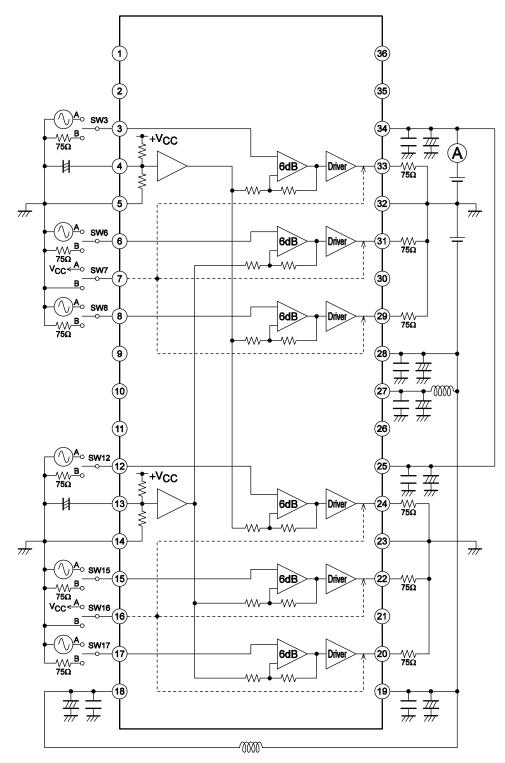
3247A



Block Diagram







MSB06039

Pin Fun	nctions		
Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
1	NC		
2			
9			
10			
11			
21			
26			
30			
35			
36			
3	V _{IN} 1	Input terminal.	
6	V _{IN} 2	Non-bias. It is possible to use with being directly connected	+Vcc
8	V _{IN} 3	with DC. When DC coupling, it is necessary to add bias after	9pF
12	VIN ⁴	the coupling.	
15	V _{IN} 5		
17	V _{IN} 6		
			1.2kΩ 1.6kΩ
			│ ★ ⊥╚┌─
			$\downarrow \qquad \qquad$
			-Vcc -Vcc
			MSP06323
4	DCCNT1	DC offset mode charge terminal between input and output	
13	DCCNT2	When a condenser is input at the position between pin 4	
		(DCCNT1) and GND, the operation of IC becomes the mode	+VCC +VCC
		with 0.3V DC offset between input and output of 1, 3, 4ch	
		(pins 3 and 33, pins 8 and 29, pins 12 and 14).	
		Similarly when a condenser is input at the position between	
		pin 13 (DCCNT2) and GND, it becomes the mode with 0.6V DC	
		offset between input and output of 2, 5, 6ch	
		(pins 6 and 31, pins 15 and 22, pins 17 and 20).	
		And when pins 4, 13 and GND is shorted, it becomes the mode	
		without DC offset between input and output.	$\downarrow \downarrow \downarrow \underline{\vee}_{VCC}$
			-V _{CC} pin5,14
			MSP06364
5	GND	Both ±power supply and +power supply are GND.	
14			
23			
32			
7	MUTE1	Changeover terminal of Mute.	
16	MUTE2	When the Mute terminal is Low, it is Mute.	+V _{CC}
		When the terminal is Open, it is Low.	\uparrow
		When the terminal is open, it is how.	\square
			\bigtriangledown
			9kΩ
			∔ g≽ ⊥
			MSP06325
	1		

Continued on next page.

Pin No.	Pin Name	Terminal Explanation	Equivalent Circuit
18	-VCC.	-V _{CC} of using ±power supply.	
19		Using +power supply, it is GND.	
27			
28			
20	VOUT ⁶	Output terminal.	
22	VOUT ⁵	Using \pm power supply, in case of the mode with DC offset, it is	+VCC +VCC
24	VOUT ⁴	possible to use without capacitor of output by setting pins 3, 8,	
29	VOUT3	12 to 0.3V-bias and by setting pins 6, 15, 17 to 0.6V-bias.	
31	V _{OUT} 2	And in case of the mode without DC offset, it is possible to use	
33	VOUT1	without capacitor of output by setting each input to zero-bias.	
		When using +power supply, both of the modes needs coupling	♦ ♦ ♦ ○
		capacitor.	
			└─── ∲
			ψ –Vcc
			MSP06326
25	+VCC	Both ±power supply and +power supply are +V _{CC} .	
34			

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