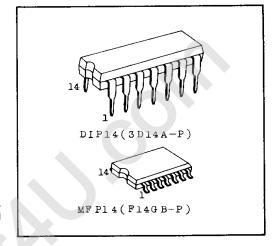
# ICANIIRA RL'ICANISRA RL' TC4023BP/BF



TC4011BP/TC4011BF QUAD 2 INPUT NAND GATE TC4012BP/TC4012BF DUAL 4 INPUT NAND GATE TC4023BP/TC4023BF TRIPLE 3 INPUT NAND GATE

The TC4011BP/BF, TC4023BP/BF, and TC4012BP/BF are 2-input, 3-input, and 4-input positive logic NAND gates respectively.

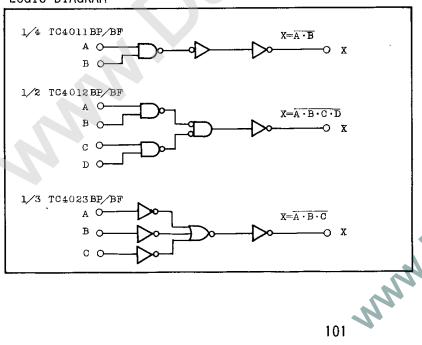
Since all the outputs of these gates are provided with the inverters as buffers, the input/output characteristics have been improved and the variation of propagation delay time due to the increase in load capacity is kept down to the minimum.



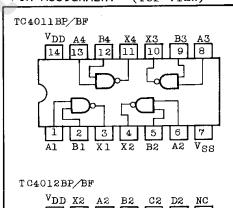
### ABSOLUTE MAXIMUM RATINGS

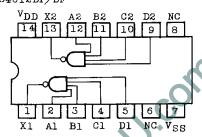
	,		
CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	$v_{\mathrm{DD}}$	$V_{SS}$ -0.5 $\sim V_{SS}$ +20	V
Input Voltage	VIN	$V_{SS}-0.5 \sim V_{DD}+0.5$	V
Output Voltage	V <sub>OUT</sub>	$V_{SS}-0.5 \wedge V_{DD}+0.5$	V
DC Input Current	IIN	±10	mA
Power Dissipation	PD	300(DIP)/180(MFP)	mW
Operating Temperature Range	TA	-40 ~ 85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 ∿ 150	°C
Lead Temp./Time	T <sub>so1</sub>	260°C • 10 sec	

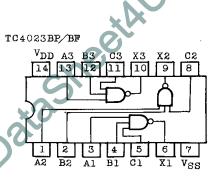
### LOGIC DIAGRAM



#### PIN ASSIGNMENT (TOP VIEW)







# TC4011BP/BF, TC4012BP/BF, TC4023BP/BF

## RECOMMENDED OPERATING CONDITIONS (VSS=0V)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNITS
DC Supply Voltage	$v_{\mathrm{DD}}$	3	_	18	V
Input Voltage	$v_{IN}$	0	_	$v_{ m DD}$	V

## STATIC ELECTRICAL CHARACTERISTICS ( $V_{SS}=0V$ )

CHARACTERISTIC		SYMBOL	TEST CONDITION	V <sub>DD</sub> -40°C		25°C			85°C		UNITS	
			TEST CONDITION	(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	1
High-Level		V <sub>OH</sub>	I <sub>OUT</sub>  <1μA	5	4.95	-	4.95	5.00	_	4.95	-	
Output Voltage			10	9.95	-	9.95	10.00	-	9.95	_		
			V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	15	14.95	_	14.95	15.00	_	14.95	_	V
Low-Leve	1	VOL	I <sub>OUT</sub>  <1μA	5	-	0.05	_	0.00	0.05	-	0.05	V
Output V			i .	10	-	0.05	1	0.00	0.05	-	0.05	
			V <sub>IN=VDD</sub>	15		0.05		0.00	0.05	_	0.05	
			V <sub>OH</sub> =4.6V	5	-0.61	_	-0.51	-1.0	_	-0.42	-	
Output H	igh		$V_{OH}=2.5V$	5		-	-2.1	-4.0	-	-1.7	-	
	-0	IOH	V <sub>OH</sub> =9.5V	10	-1.5	~	-1.3	-2.2	-	-1.1	-	
Current			V <sub>OH</sub> =13.5V	15	-4.0	-	-3.4	-9.0	-	-2.8	_	
			V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>									A
			V <sub>OL</sub> =0.4V	5	0.61	_	0.51	1.2	-	0.42	-	mA
Output Lo	wc	I <sub>OL</sub>	V <sub>OL</sub> =0.5V	10	1.5	-	1.3	3.2	_	1.1	_	
Current			V <sub>OL</sub> =1.5V	15	4.0	-	3.4	12.0		2.8	-	
			$v_{IN}=v_{DD}$					ĺ				
			V <sub>OUT</sub> =0.5V, 4.5V	5	3.5	_	3.5	2.75	_	3.5	_	
Input Hig	gh	ν <sub>IH</sub>	V <sub>OUT</sub> =1.0V, 9.0V	10	7.0	-	7.0	5.5	_	7.0	_	
Voltage			V <sub>OUT</sub> =1.5V,13.5V	15	11.0	-	11.0	8.25	_	11.0	_	
			I <sub>OUT</sub>  <1μA									
	_		V <sub>OUT</sub> =4.5V	5	_	1.5	_	2.25	1.5	_	1.5	v
Input Low	v	$v_{\mathrm{IL}}$	V <sub>OUT</sub> =9.0V	10	-	3.0	-	4.5	3.0	_	3.0	
Voltage			V <sub>OUT</sub> =13.5V	15	-	4.0	-	6.75	4.0	-	4.0	
_			I <sub>OUT</sub>  <1μA									
Input	"H" Level	I <sub>IH</sub>	V <sub>IH</sub> =18V	18	-	0.1	-	10-5	0.1	-	1.0	
Current	"L" Level	I <sub>IL</sub>	V <sub>IL</sub> =0V	18	_	-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	.
Quiescent	;		V =V V	5	_	0.25	-	0.001	0.25		7.5	μА
	1	IDD	$v_{IN}=v_{SS}, v_{DD}$	10	_	0.5		0.001	0.5	_	15	
Device Current			*	15	_	1.0	I	0.002	1.0	_	30	Ì
				. — Д								

<sup>\*</sup> All valid input combinations.

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta=25°C, VSS=0V, CL=50pF)

CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>DD</sub> (V)	MIN.	TYP.	MAX.	UNITS
Output Transition Time (TC4012BP/BF)	t <sub>TLH</sub>		5 10 15	-	80 50 40	200 100 • 80	
Output Transition Time (TC4012BP/BF)	t <sub>THL</sub>	·	5 10 15	<u>-</u> - -	80 50 40	200 100 80	
Output Transition Time (TC4011BP/BF) (TC4023BP/BF)	t <sub>TLH</sub>		5 10 15	- - -	70 35 30	200 100 80	
Output Transition Time (TC4011BP/BF) (TC4023BP/BF)	tTHL		5 10 15	- - -	70 35 30	200 100 80	
Propagation Delay Time (TC4011BP/BF)	tpLH		5 10 15	-	65 30 25	200 100 80	
Propagation Delay Time (TC4011BP/BF)	t <sub>pHL</sub>		5 10 15	_ _ _	65 30 25	200 100 80	ns
Propagation Delay Time (TC4012BP/BF)	tpLH		5 10 15	-	95 45 30	250 120 90	
Propagation Delay Time (TC4012BP/BF)	tpHL		5 10 15	- -	95 45 30	250 120 90	
Propagation Delay Time (TC4023BP/BF)	t <sub>pLH</sub>	-	5 10 15	- -	90 45 35	250 100 80	
Propagation Delay Time (TC4023BP/BF)	t <sub>pHL</sub>		5 10 15	-	90 45 35	250 100 80	. •
Input Capacitance	$c_{IN}$		•	_	5	7.5	pF



