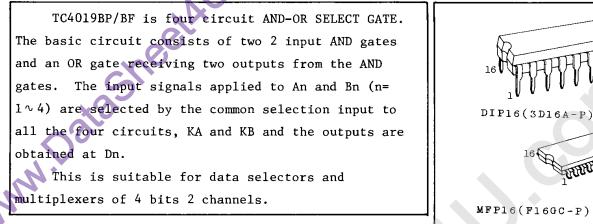
TC4019BP/BF

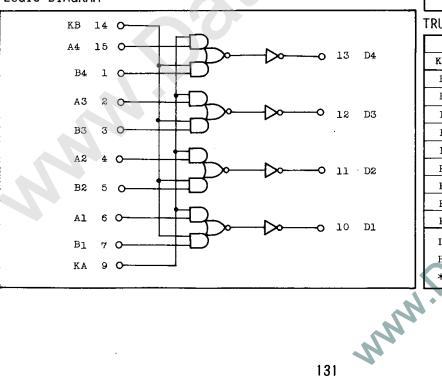
TC4019BP/TC4019BF QUAD AND/OR SELECT GATE

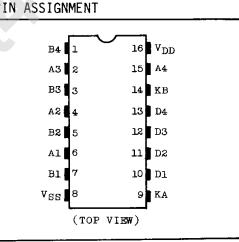


ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	V _{DD}	$v_{SS} = 0.5 \circ v_{SS} + 20$	V
Input Voltage	VIN	$v_{SS} = 0.5 \sim v_{DD} + 0.5$	V
Output Voltage	V _{OUT}	$v_{SS} - 0.5 \sim v_{DD} + 0.5$	V
DC Input Current	IIN	±10 •) mA
Power Dissipation	PD	300(DIP)/180(MFP)	mW
Operating Temperature Range	т _А	-40 ~85	°C
Storage Temperature Range	T _{stg}	-65 ~150	°C
Lead Temp./Time	T _{sol}	260°C • 10 sec	

LOGIC DIAGRAM





TRUTH TABLE

	INP	OUTPUT		
KA	КB	An	Bn	Dn
Н	L	H	*	H
н	L	L	*	L
L	н	*	Н	н
L	Н	*	L	L
L	L	*	*	L
н	Н	Ĺ	L	L
н	Н	L	Н	Н
н	Н	Н	Г	Н
Н	Н	Н	H	Н
г:	LOW LE	VEL		
н ;	HIGH L	EVEL		
*:	DON'T C	ARE		

TC4019BP/BF

RECOMMENDED OPERATING CONDITIONS (v_{SS} =0v)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNI TS
DC Supply Voltage	V _{DD}	3	-	18	V
Input Voltage	V _{IN}	0	-	v _{DD}	V

STATIC ELECTRICAL CHARACTERISTICS (v_{SS} =0v)

CHARACTERISTIC SYMBOL		TEST CONDITIONS VD		-40°C		25°C			85°C		UNITS
CHARACTERISTI		TEST CONDITIONS	(V)	MIN.	MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	UNITS
High-Level V _{OH}	1 _{0UT} <1µA	5	4.95	-	4.95	5.00	-	4.95	-		
Output Voltag	e	$V_{IN}=V_{SS}$, V_{DD}	10	9.95	-		10.00		9.95		
		VIN-VSS, VDD	15	14.95		14.95	15.00		14.95		v
Low-Level	V _{OL}	I _{OUT} <1µA	5	-	0.05	-	0.00	0.05	-	0.05	
Output Voltag	e	$v_{1N} = v_{SS}, v_{DD}$	10	-	0.05		0.00	0.05		0.05	
· ····································		$V_{OH}=4.6V$	15 5		0.05		0.00	0.05	-	0.05	
		*	-	-0.61	-	-0.51	1	-	-0.42	. –	
Output High	I _{OH}	V _{OH} =2.5V	5	-2.5	-	-2.1	-4.0 -2.2	-	-1.7	-	
Current		v _{OH} =9.5v	10		-			-	-1.1	-	
		V _{OH} =13.5V	15	-4.0	-	-3.4	-9.0	-	-2.8	-	
		$v_{IN} = v_{SS}, v_{DD}$									mA
		$V_{OL}=0.4V$	5	0.61	-	0.51	1.2	-	0.42	-	
Output Low	In	V _{OL} =0.5V	10	1.5	*	1.3	3.2	-	1.1	-	
Current	Current	VOL=1.5V	15	4.0	-	3.4	12.0	-	2.8	-	
		V _{IN} =V _{SS} , V _{DD}									
		V _{OUT} =0.5V, 4.5V	5	3.5	-	3.5	2.75	1	3.5		
Input High	VIH	V _{OUT} =1.0V, 9.0V	10	7.0	-	7.0	5.5	~	7.0	-	
Voltage	r.n	V _{OUT} =1.5V,13.5V	15	11.0	-	11.0	8.25	-	11.0	-	
		I _{OUT} <1µA						,			V
		VOUT=0.5V, 4.5V	5	-	1.5	-	2.25	1.5	-	1.5	
Input Low	VIL	V _{OUT} =1.0V, 9.0V	10	-	3.0	-	4.5	3.0	-	3.0	
Voltage		V _{OUT} =1.5V,13.5V	15	_	4.0	~	6.75	4.0	-	4.0	
		I _{OUT} <1µA						:			
Input "H" Leve	L I _{IH}	V _{IH} =18V	18	-	0.1	-	10 ⁻⁵	0.1	-	1.0	μA
Current "L" Level	I IIL	VIL=OV	18	-	-0.1	-	-10 ⁻⁵	-0.1	-	-1.0	μ
Quiescent	Inn	V _{IN} =V _{SS} , V _{DD}	5	-	0.25	-	0.001	0.25	-	7.5	
Device Current	IDD	*IN ⁻ *SS> *DD *	10	-	0.5	-	0.001	0.5	-	15	μA
Device Current	-		15	-	1.0	-	0.002	1.0	-	30	

* All valid input combinations

			<u> </u>			· · · · · · · · · · · · · · · · · · ·	
CHARACTERISTIC	SYMBOL	TEST CONDITION	$v_{DD}(v)$	MIN.	TYP.	MAX.	UNITS
Output Transition Time (Low to High)	t _{TLH}		5		70	200	
Output Transition Time (High to Low)	t _{THL}		10 15	-	35 30	100 80	
Propagation Delay Time (A, B - D)	t _{pLH}		5	-	75	300	
	t _{pHL}		10 15	-	40 30	120 100	ns
Propagation Delay Time (KA, KB - D)	t _{pLH}		5	-	75	300	
	t _{pHL}		10	-	40	120	
	P		15	-	30	100	
Input Capacitance	C _{IN}	An, Bn Input		-	5	7.5	pF
	- 11	KA, KB Input		-	12	20	P.

DYNAMIC ELECTRICAL CHARACTERISTICS (Ta=25°C, V_{SS}=0V, C_L=50pF)

WAVEFORM FOR MEASUREMENT OF DYNAMIC CHARACTERISTICS

