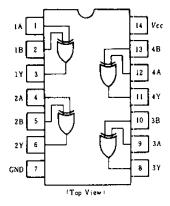
PIN ARRANGEMENT



FUNCTION TABLE

Inputs		Output
A	В	Y
L	L	L
L	н	н
Н	L	Н
Н	Н	L

H; high level, L; low level

ELECTRICAL CHARACTERISTICS ($Ta = -20 \sim +75^{\circ}C$)

Item	Symbol	Test Conditions		min	typ*	max	Unit
Turnet and the me	ViB			2.0			V
Input voltage	VIL					0.8	v
	Voн	$V_{CC} = 4.75 V, V_{IH} = 2V, V_{IL} = 0.8 V, I_{C}$	$\rho_{H} = -400 \mu A$	2.7		_	v
Output voltage Vol	Vor	$V_{0L} = 4.75$ V, $V_{1H} = 2$ V, $V_{1L} = 0.8$ V	$Io_L = 4mA$			0.4	
	VOL		$Io_L = 8 \text{mA}$	-		0.5	v
	h	$V_{cc} = 5.25 V, V_l = 7 V$		-		0.2	mA
Input current	Ін	$V_{cc} = 5.25 V, V_l = 2.7 V$		-		40	μA
IIL		$V_{cc} = 5.25 V, V_l = 0.4 V$		-		-0.8	mA
Short-circuit output current	Ios	$V_{\rm CC} = 5.25 \rm V$		- 20	_	-100	mA
Supply current **	Icc	$V_{cc} = 5.25 \mathrm{V}$		-	6.1	10	mA
Input clamp voltage	Vik	$V_{CC} = 4.75 \text{V}, \ I_{IN} = -18 \text{mA}$			_	-1.5	v

* V_{CC}=5V, Ta=25°C

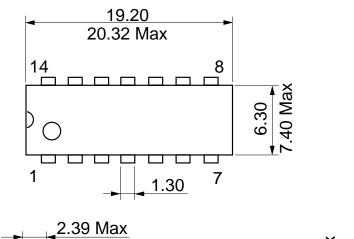
** I_{CC} is measured with all outputs open and all inputs grounded.

SWITCHING CHARACTERISTICS (*Vcc*=5V, *Ta*=25°C)

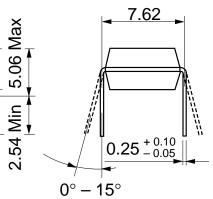
Item	Symbol	Inputs	Test C	onditions	min	typ	max	Unit
	tPLH	A or B		Other inputs=0V	_	12	23	ns
Propagation delay time	tph l		$C_L = 15 \mathrm{pF}$,		-	10	17	ns
r ropagation delay time	1PLH		A or B $R_L = 2k\Omega$	Other in	_	20	30	ns
tPHL			Other inputs=4.5V		- 13	22	ns	

Note) Refer to Test Circuit and Waveform of the Common Item

Unit: mm



 0.48 ± 0.10



0.51 Min

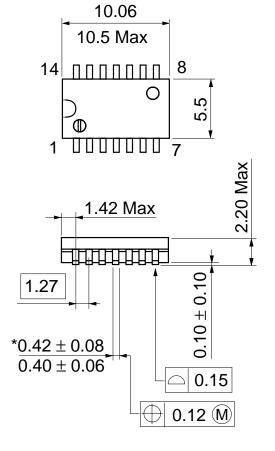
RANK

Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

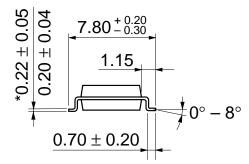
 2.54 ± 0.25

Unit: mm





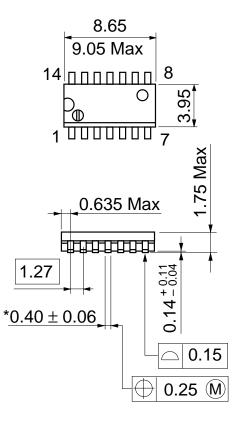
*Dimension including the plating thickness Base material dimension



Hitachi Code	FP-14DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.23 g

Unit: mm



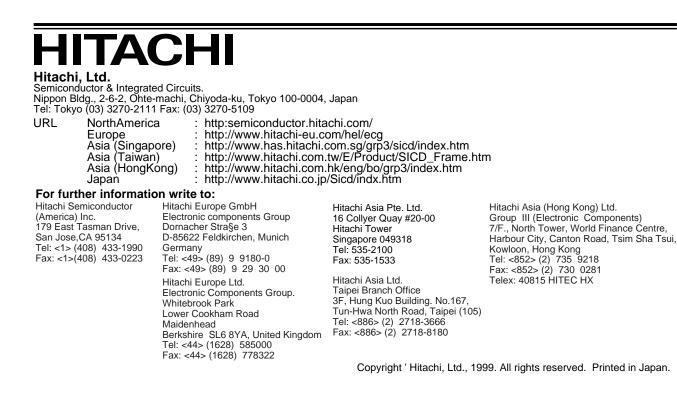


Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

*Pd plating

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