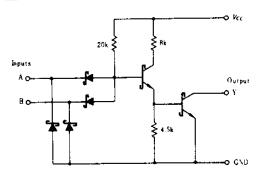
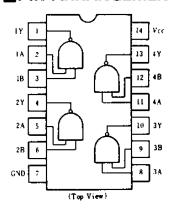
ECIRCUIT SCHEMATIC(1/4)



PIN ARRANGEMENT



ERECOMMENDED OPERATING CONDITIONS

Item	Symbol	min	typ	max	Unit
High level output voltage	Voн	_	_	5.5	v
Low level output current	Iol	-		8	mA

ELECTRICAL CHARACTERISTICS ($Ta = -20 \sim +75^{\circ}C$)

Item	Symbol	Test Conditions		min	typ*	max	Unit
	Vin			2.0	_	_	v
Input voltage	VIL			-	_	0.8	v
Output voltage Vol		$V_{CC} = 4.75 \text{V}, V_{IH} = 2 \text{V}$	IoL=8mA	_	_	0.5	v
	Vol		IoL = 4mA			0.4	<u> </u>
I_{IH} $V_{CC} = 5.25 \text{V}, V_{I} = 2.7 \text{V}$			_	2 0	μA		
Input current	ItL	$V_{CC} = 5.25 \text{V}, V_I = 0.4 \text{V}$		-	_	-0.4	mА
	Iı	$V_{CC} = 5.25 \text{V}, V_I = 7 \text{V}$		_	_	0.1	mA
Output current	Іон	$V_{CC} = 4.75 \text{V}, V_{IL} = 0.8 \text{V}, V_{OH} = 5.5 \text{V}$		-	_	100	μA
	Іссн	Vcc=5.25V		-	0.8	1.6	mA
Supply current	Iccı	Vcc=5.25V		-	2.4	4.4	mA
Input clamp voltage	Vik	$V_{CC} = 4.75 \text{V}, I_{IN} = -18 \text{mA}$		-	_	-1.5	v

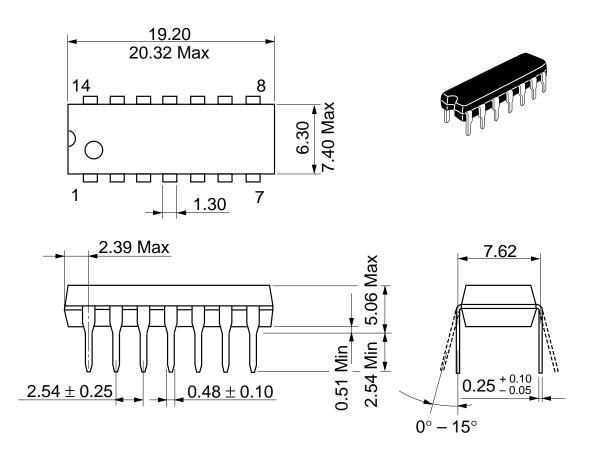
^{*} VCC=5V, Ta=25°C

ESWITCHING CHARACTERISTICS (Vcc=5V, Ta=25°C)

<u> </u>	Symbol	Test Conditions	min	typ	max	Unit
Propagation delay time	tplH	$C_L = 15 \text{pF}, R_L = 2 \text{k}\Omega$	_	i7	32	ns
	tphL			15	28	ns

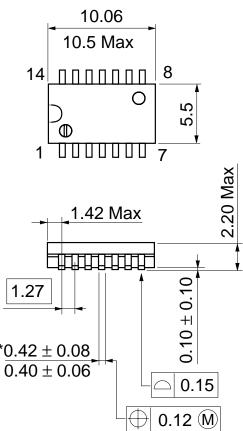
Note) Refer to Test Circuit and Waveform of the Common Item

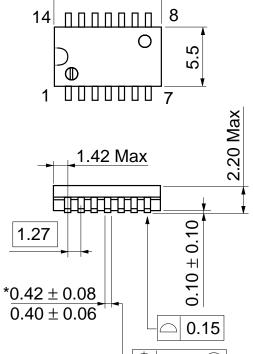
Unit: mm



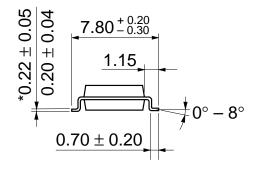
Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

Unit: mm





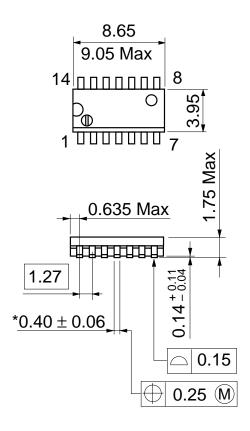




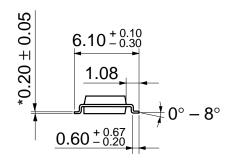
Hitachi Code	FP-14DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.23 g

Dimension	including	the	plating	thickness
Bas	se materia	al dir	mensio	1

Unit: mm







Hitachi Coo	de	FP-14DN	
JEDEC		Conforms	
EIAJ		Conforms	
Weight (refe	erence value)	0.13 g	

*Pd plating

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