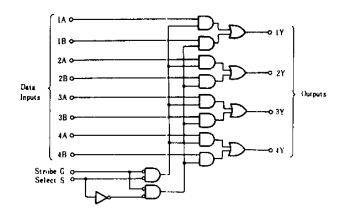
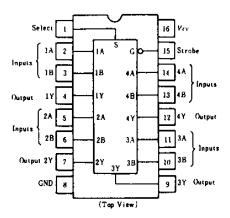
This data selector/multiplexer contains inverters and drivers to supply full on-chip data selection to the four output gates. A separate strobe input is provided. A 4-bit word is selected from one of two sources and is routed to the four outputs. Then, outputs present true data to minimize propagation delay time.

#### **■BLOCK DIAGRAM**



#### **■PIN ARRANGEMENT**



#### **EFUNCTION TABLE**

	Inputs				
Strobe	Select	A	В	Y	
Н	×	×	×	L	
L	L	L	×	L	
L	L	Н	×	Н	
L	Н	×	L	L	
L	Н	×	Н	Н	

H; high level, L; low level, X; irrelevant

# **ELECTRICAL CHARACTERISTICS** ( $Ta = -20 \sim +75^{\circ}C$ )

Item		Symbol	Test Conditions		min	typ*	max	Unit
Input voltage		ViH			2.0			v
		VIL			_	_	0.8	v
		Vон	$V_{CC} = 4.75 \text{V}, V_{IH} = 2 \text{V}, V_{IL} = 0.8 \text{V}, I_{OH} = -400 \mu\text{A}$		2.7			v
Output voltage	<u></u>	Vol. Vcc=4.75V, ViH=2V, ViL=0.8V		$I_{OL} = 4 \text{mA}$	_		0.4	v
			IoL = 8mA	-		0.5	V	
S, G   A, 1   S, G   A, 2   S, G   A, 3   S, G   A, 3	S,G	,	1/ F 053/ 1/ 0 53/	•••	_	_	40	
	A, B	Iгн	$V_{CC} = 5.25 \text{V},  V_I = 2.7 \text{V}$			+	20	μA
	S, G	T	$V_{CC} = 5.25 \text{V},  V_I = 0.4 \text{V}$		_		-0.8	
	A, B	ItL		[			-0.4	mA
	S,G	3 .					0.2	
Ā	A, B	<b>I</b> t	$V_{CC}=5.25$ V, $V_{I}=7$ V		_	_	0.1	mA
Short-circuit output curre		Ios	$V_{CC} = 5.25 \text{V}$		-20	-	100	mA
Supply current**		lcc	Vcc=5.25V		-	9.7	16	mΑ
Input clamp voltage		Vik	$V_{\rm CC} = 4.75 \text{V}, I_{IN} = -18 \text{mA}$		_ [		-1.5	V

<sup>\*</sup> *V<sub>CC</sub>*=5V, *Ta*=25°C

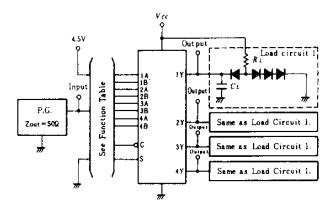
## **ESWITCHING CHARACTERISTICS** ( $V_{CC} = 5V$ , $T_a = 25^{\circ}C$ )

Item	Symbol	Inputs	Output	Test Conditions	min	typ	max	Unit
Propagation delay time	ŧр;,н	D	v	Y $C_L = 15 \text{pF},  R_L = 2 \text{k}\Omega$ Y		9	14	ns
	tphi.	Data	ıta ı			9	14	ns
	tri.H	Charles	Y			13	20	ns
	tehi.	Strobe			_	14	21	ns
	tp1.H	C )	v			15	23	ns
	tehl	Select			_	18	27	ns

<sup>\*\*</sup> ICC is measured with all outputs open and all inputs at 4.5V.

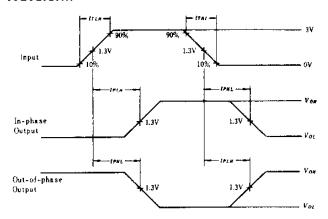
#### **M**TESTING METHOD

## 1) Test Circuit



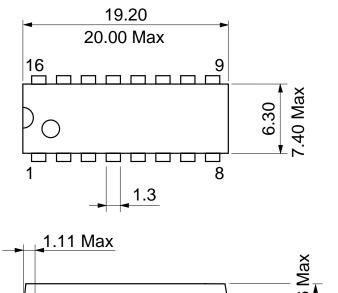
Notes) 1.  $C_L$  includes probe and jig capacitance. 2. All diodes are 1S2074  $\stackrel{\frown}{H}$ .

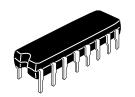
#### Waveform

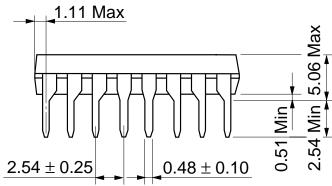


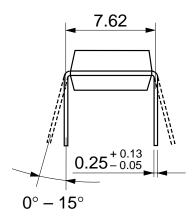
Input pulse;  $t_{TLH} \le 15 \text{ns}$ ,  $t_{THL} \le 6 \text{ns}$ , PRR = 1 MHz, duty cycle 50%.

Unit: mm



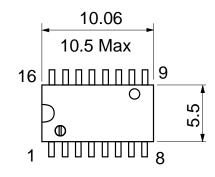


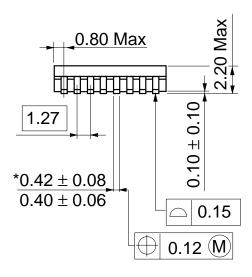




Hitachi Code	DP-16
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	1.07 g

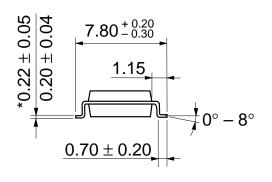
Unit: mm





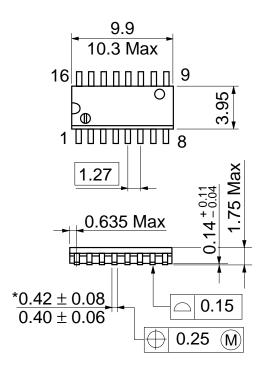
\*Dimension including the plating thickness
Base material dimension



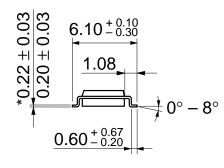


Hitachi Code	FP-16DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.24 g

Unit: mm







\*Dimension including the plating thickness
Base material dimension

Hitachi Code	FP-16DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.15 g

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